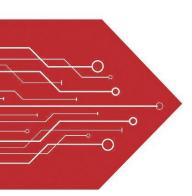
# MSKSEMI















**ESD** 

**TVS** 

**TSS** 

MOV

**GDT** 

**PLED** 

Broduct data speet



#### **MAIN APPLICATIONS**

USB 2.0&3.0 power and data line protection

Digital video interface (DVI)

Notebook computers

Video graphics cards

Monitors and flat panel displays

10/100/1000 ethernet

SIM ports

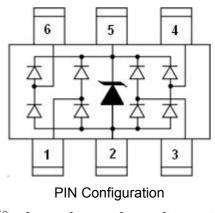
**ATM** interfaces

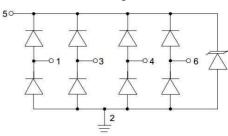
### PROTECTION SOLUTION TO MEET

IEC61000-4-2 (ESD) ±20kV (air), ±20kV (contact)

IEC61000-4-4 (EFT) 40A (5/50ns)

IEC61000-4-5 (Lightning) 5A (8/20µs)





Circuit Diagram SOT-23-6

## **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, RH=45%-75%, unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak pulse power dissipation on 8/20µs waveform	P <sub>PP</sub>	250	W
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>	+/- 20 +/-20	kV
Lead soldering temperature	TL	260 (10 sec.)	$^{\circ}$
Operating junction temperature range	TJ	-55 to +125	$^{\circ}$ C
Storage temperature range	T <sub>STG</sub>	-55 to +150	$^{\circ}$ C

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Reverse working voltage	V <sub>RWM</sub>				5.0	V
Reverse breakdown voltage	V <sub>BR</sub>	I <sub>T</sub> =1mA	6.0			V
Reverse leakage current	I <sub>R</sub>	V <sub>RWM</sub> =5V			1	μA
Forward voltage	V <sub>F</sub>	I <sub>T</sub> =10mA		0.8	1.0	V
Clamping voltage (I/O pin to Ground)	Vc	I <sub>PP</sub> =1A, t <sub>P</sub> =8/20μs		9.5	11	V
	Vc	I <sub>PP</sub> =10A, t <sub>P</sub> =8/20μs		12.5	20	V
Junction capacitance	n conscitores C	V <sub>RWM</sub> =0V, f=1MHz Any I/O pin to Ground		0.65	0.8	,r
	Сл	V <sub>RWM</sub> =0V, f=1MHz Between I/O pins		0.3	0.5	pF









Symbol	Parameter
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
V <sub>RWM</sub>	Working Peak Reverse Voltage
I <sub>R</sub>	Maximum Reverse Leakage Current @ V <sub>RWM</sub>
I <sub>T</sub>	Test Current
V <sub>BR</sub>	Breakdown Voltage @ I⊤

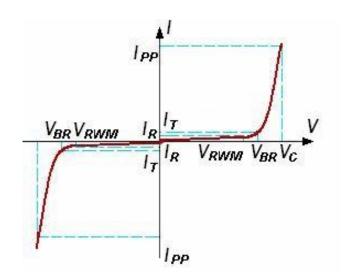


FIG1: Pulse Waveform

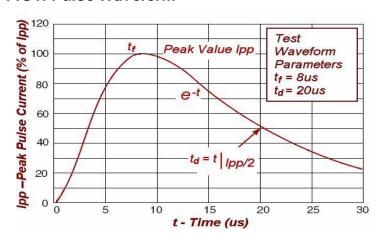
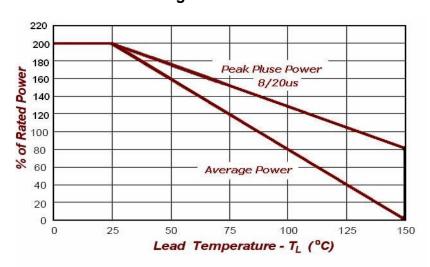
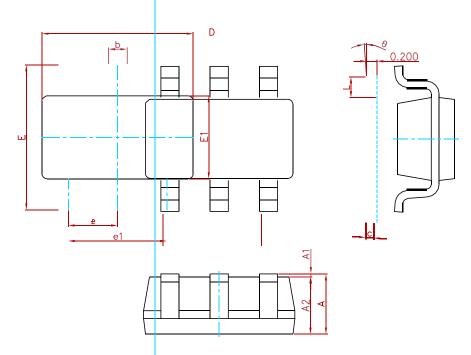


FIG2:Power Derating



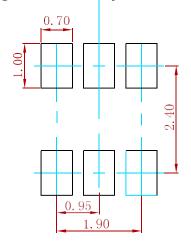


# PACKAGE MECHANICAL DATA



Symbol Dimension		n Millimeters	Dimension	s In Inches	
Syllibol	Min.	Max.	Min.	Max.	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E1	1.500	1.700	0.059	0.067	
E	2.650	2.950	0.104	0.116	
е	0.950(	BSC)	0.037	(BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

# Suggested Pad Layout



#### Note:

- 1.Controlling dimension:in millimeters.
  2.General tolerance:± 0.05mm.
  3.The pad layout is for reference purposes only.

# **REEL SPECIFICATION**

P/N	PKG	QTY
ESD5425E-MS	SOT-23-6	3000



Semiconductor

## Compiance

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