MSKSEMI 美森科













ESD

TVS

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PIFD

HT78XX-MS

Product specification





GENERAL DESCRIPTION

The HT78XX-MS series is a set of low voltage differential (LDO) converters with a wide voltage input range of 2.8V to 5.0V, low voltage differential, low power consumption, and miniaturized packaging. The output volt age range is 3.0-5.0V, and the HT78XX-MS has low static current characteristics as low as 3.0uA. The circuit also has a CE enable control port, which can put the circuit into sleep mode. It is particularly suitable for battery powered and long-term standby system equipment applications, helping to reduce standby power consumption of system equipment, effectively extending standby time and battery life.

FEATURES

- Low Power Consumption
- Low Voltage Drop
- Low Temperature Coefficient
- Withstanding Voltage 12V
- Quiescent Current 3.0µA
- Output Voltage Accuracy: tolerance ±2%
- High output current: 450mA

TYPICALAPPLICATIONS

- Battery-powered Equipments
- Communication Equipments
- Audio/Video Equipments

PACKAGE/ORDER INFORMATION

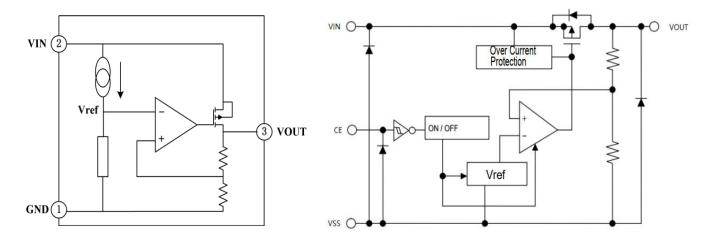
Part Number	Output	Pa	ckage	Pin Configuration	Marking	QTY
HT7828-MS	2.8V				MSKSEMI HT7828	1000
HT7830-MS	3.0V				MSKSEMI HT7830	1000
HT7833-MS	3.3V	SOT89-3			MSKSEMI HT7833	1000
HT7836-MS	3.6V			引 図 ③ GND VIN VOUT	MSKSEMI HT7836	1000
HT7850-MS	5.0V				MSKSEMI HT7850	1000

PIN DESCRIPTION

PIN No.	Name	Functions Description
1	GND	ground
2	VIN	input
3	VOUT	output



FUNCTIONALBLOCKDIAGRAM



ABSOLUTE MAXIMUM RATINGS

Description	Symbol	Value range	Unit
Limit Power Voltage	V _{IN}	-0.3∼+15	V
Storage Temperature Range	Tstg	-50∼+125	$^{\circ}$ C
Operating Free-air Temperature Range	TA	-40∼ + 85	$^{\circ}$ C

Note: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

HEAT DISSIPATION

Description	Symbol	Package	Value range	Unit
Thermal resistance	$\theta_{ m JA}$	SOT89	200	°C/W
Power dissipation	Pw	SOT89	500	mW



$\begin{tabular}{ll} \textbf{DCCHARACTERISTICS} (unless otherwise noted T_A = \pm 25 ^{\circ} C) \\ \end{tabular}$

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Input Voltage	V _{IN}		3.0		12	V
Output Voltage	V _{OUT}		3.0		5.0	V
Voltage Accuracy		$I_{OUT} = 1 \text{mA}$	-2		+2	%
Output Current	I _{OUT}	$V_{IN}=V_{OUT}+2.0V$		_	450	mA
Load Regulation	$\triangle V_{OUT}$	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 150mA$		50	l	mV
Line Regulation	$\triangle V_{OUT} / V_{OUT} * \triangle V_{IN}$	V_{OUT} +1.0 V ≤ V_{IN} ≤20 V I_{OUT} =10 m A		0.015	0.2	%/V
Voltage Drop	$V_{ m DIF}^{(\!\scriptscriptstyle 1\!\!)}$	I _{OUT} =100mA,V _{OUT} =3.3V		200		mV
Quiescent Current	I_{SS}	$V_{\text{CE}} = V_{\text{IN}}$	_	3.0	5.0	μΑ
Standby Current	I _{STANDBY}	V _{CE} =V _{SS}			0.2	μΑ
	V_{CEH}	V _{IN} =V _{OUT} +2.0V	1.7		24	V
	$ m V_{CEL}$	V _{IN} =V _{OUT} +2.0V	0		0.3	V
short-circuit current	I _{SHORT}	V _{IN} =V _{OUT} +2.0V	_	400	_	mA
Temperature Coefficient	$\triangle V_{OUT} / $ $\triangle T_A * V_{OUT}$	$\begin{array}{c} V_{IN}\!\!=\!\!V_{OUT}\!\!+\!\!2.0V\\ I_{OUT}\!\!=\!\!10mA\\ -\!40^{\circ}C\!\!\leq\!\!T_{A}\!\!\leq\!\!125^{\circ}C \end{array}$	_	±100	—	ppm/°C
Discharge Resistor	$R_{ m DIS}^{^{\circledcirc}}$	VCE<0.5V		300		Ω

Note: When $V_{IN}=V_{OUT}+2.0V$, as the output voltage declined 2%, the $V_{DIF}=V_{IN}-V_{OUT}$. Output active discharge resistor R_{DIS} , As the input voltage increases, it decreases.

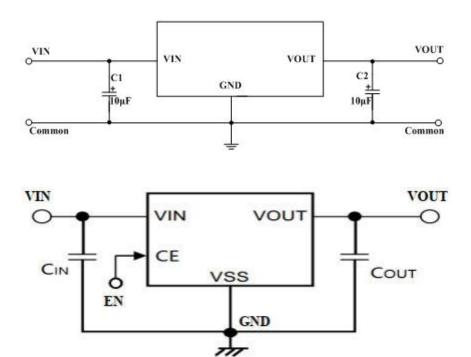


FUNCTIONAL DESCRIPTION

HT78XX-MS series are linear voltage regulator ICs withstanding 14V voltage. The series ICconsists of a voltage reference, an error amplifier, a current limiter and a phase compensation circuit plus a driver transistor. The output stabilization capacitor is also compatible with low ESR ceramic capacitors.

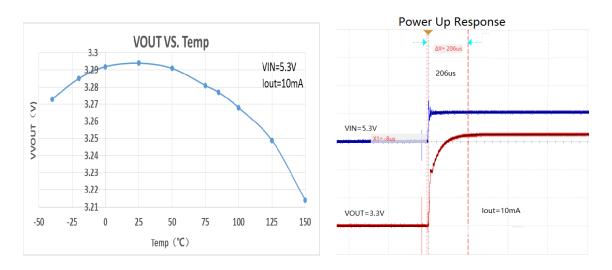
The over current protection circuit and the over voltage protection circuit are built-in. The protection circuit will operate when the output current or input voltage reaches limit level.

TYPICAL APPLICATION CIRCUIT



- 1. CIN is used to stabilize the input capacitor
- 2. CIN eramic capacitors greater than or equal to 1pF can be used for COUT.

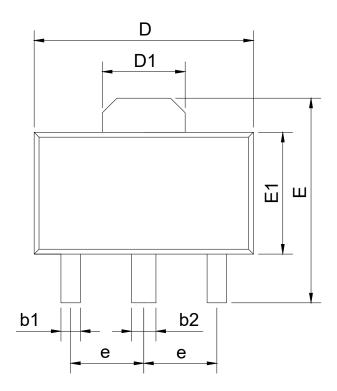
TYPICAL CHARACTERISTICS

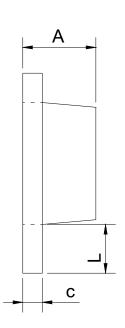




PACKAGEINFORMATION

SOT89





SYMBOL	mm		
STINDOL	min	max	
Α	1.40	1.60	
b1	0.35	0.50	
b2	0.45	0.60	
С	0.36	0.46	
D	4.30	4.70	
D1	1.40	1.80	
Е	4.00	4.40	
E1	2.30	2.70	
е	1.50BSC		
L	0.80	1.20	



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