MSKSEMI 美森科













ESD

Т

TSS

MOV

GDT

PIFD

LMV331-MS\LMV393-MS\LMV339-MS

Product specification





GENERAL DESCRIPTION

The LMV331-MS and LMV393-MS is the single and dual comparator version, the LMV339-MS is quad comparator version, and both are open-drain output comparators for maximum flexibility. It can operate from 2.1V to 5.5V, and have low power consuming 50µA (TYP) per channel at output low.

The LMV331-MS, LMV393-MS and LMV339-MS are themost cost-effective solutions for applications where low voltage operation, low power and space saving are the primary specifications in circuit design for portable consumer products. The LMV331-MS, LMV393-MS and LMV339-MS areavailable in Green SOT-23-5, SOP-8, SOP-14 packages. It operates over an ambient temperature range of -40°C to +85°C.

FEATURES

- Supply Range: +2.1V to +5.5V
- Low Supply Current
- 50µA (TYP) per channel at VS = 5V and output low
- Input Common-Mode Voltage Range Includes Ground
- Low Output Saturation Voltage 100mV Typical
- Open-Drain Output for Maximum Flexibility
- SPECIFIED UP TO +125°C
- Micro SIZE PACKAGES: SOT23-5

APPLICATIONS

- Hysteresis Comparators
- Oscillators
- Window Comparators
- Industrial Equipment
- Test and Measurement

Reference News

PACKAGE OUTLINE	Marking
	V331
LMV331-I	MS
	MSKSEMI LMV393 MS ***
LMV393-1	MS
	MSKSEMI LMV339 MS ***
LMV339-1	MS

SIMPLIFIED SCHEMATIC

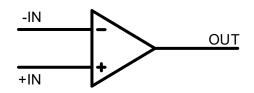


Figure 1. Simplified Schematic



Absolute Maximum Ratings(1)

		MIN	MAX	UNIT
	Supply, V _s =(V+) - (V-)		7	V
Voltage	Input pin (IN+, IN-) (2)	(V-) - 0.3	(V+) + 0.3	V
	Signal output pin (3)	(V-) - 0.3	(V+) + 0.3	V
Current	Signal Input pin (IN+, IN-) (2)	-10	10	mA
Current	Signal output pin (3)	-55	55	mA
	Operating Range	-40	85	°C
Temperature	Storage	-65	150	°C
	Junction		150	°C

⁽¹⁾ Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

ESD Ratings

			VALUE	UNIT
		Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins (1)	±2000	V
V _(ESD)	Electrostatic discharge	Charged device model (CDM), per JEDEC specification JESD22-C101, all pins (2)	±1000	V

⁽¹⁾ JEDEC document JEP155 states that 500V HBM allows safe manufacturing with a standard ESD control process.

Recommended Operating Conditions

		MIN	MAX	UNIT
Supply voltage, Vs= (V+) - (V-)	Single-supply	2.1	5.5	V
Supply voltage, vs= (v1)- (v-)	Dual-supply	±0.9	±2.75	V

⁽²⁾ Output terminals are diode-clamped to the power-supply rails. Output signals that can swing more than 0.5V beyond the supply rails should be current-limited to ±55mA or less.

⁽³⁾ Short-circuit from output to VCC can cause excessive heating and eventual destruction.

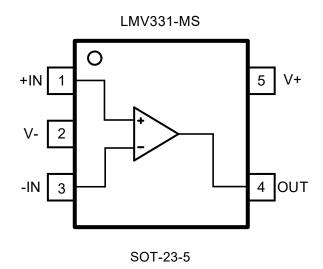
⁽²⁾ JEDEC document JEP157 states that 250V CDM allows safe manufacturing with a standard ESD control process.



PACKAGE/ORDER INFORMATION

MODEL	OPERATING TEMPERATURE RANGE	PACKAE DESCRIPTION	QTY
LMV331-MS	-40°C~85°C	SOT23-5	3000
LMV393-MS	-40°C~85°C	SOP-8	2500
LMV339-MS	-40°C~85°C	SOP-14	2500

Pin Configuration and Functions (Top View)



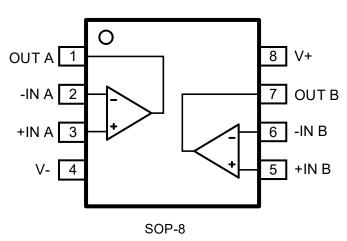
Pin Description

Pin Name	Pin Number SOT23-5	I/O	Description
+IN	1	l	Noninverting input
V-	2	- Negative(lowest) power supply	
-IN	3	I	Inverting input
OUT	4	0	Output
V+	5	-	Positive (highest) power supply



Pin Configuration and Functions (Top View)

LMV393-MS

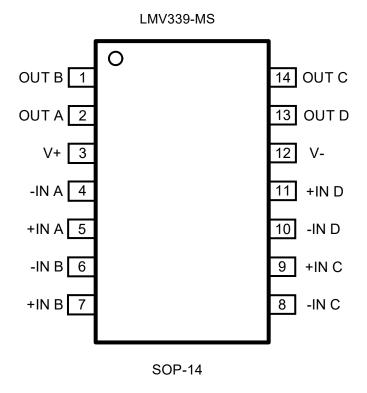


Pin Description

Pin Name	Pin Number SOP-8	1/0	Description
OUTA	1	0	Output, channel A
-INA	2	I	Inverting input, channel A
+INA	3	I	Noninverting input, channel A
V-	4	-	Negative(lowest) power supply
+INB	5	I	Noninverting input, channel B
-INB	6	I	Inverting input, channel B
OUTB	7	0	Output, channel B
V+	8	-	Positive (highest) power supply



Pin Configuration and Functions (Top View)



Pin Description

Pin	Pin Number	I/O	Description
Name	SOP-14] "/0	Description
OUTB	1	0	Output, channel B
OUTA	2	0	Output, channel A
V+	3	-	Positive (highest) power supply
-INA	4	I	Inverting input, channel A
+INA	5	I	Noninverting input, channel A
-INB	6	I	Inverting input, channel B
+INB	7	I	Noninverting input, channel B
-INC	8	I	Inverting input, channel C
+INC	9	I	Noninverting input, channel C
-IND	10	I	Inverting input, channel D
+IND	11	I	Noninverting input, channel D
V-	12	_	Negative(lowest) power supply
OUTD	13	0	Output, channel D
OUTC	14	0	Output, channel C



ELECTRICAL CHARACTERISTICS(Vs = 5.0V)

At T_A = 25°C, V_{CM} = V_S/2, unless otherwise noted.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
POWER SUPPLY					
Operating Voltage Range		2.1		5.5	V
Quiescent Current/per channel (Output High)			29		μA
Quiescent Current/per channel (Output Low)			50		μA
Power Supply Rejection Ratio	Vs = 2.1V to 5. 5V,V _{CM} = Vs/2		70		dB
INPUT					
Input offset voltage		-4	±0.8	4	mV
Input Offset Voltage Drift			0.8		μV/°C
Common-Mode Voltage Range		(V-)-0.1		4.5	V
Common-mode Rejection Ratio	V _{CM} = -0.1V to 4.5V		70		dB
Input Bias Current			2		рА
Input Offset Current			1		pA
OUTPUT			1	I	
Saturation Voltage	lo≤4mA		100		mV
Output Pull-up Voltage Range				5.6	V
Output Current(sinking)	V ₀ ≤1.5V		50		mA
SWITCHING					
Drama nation Dalay II Tall	R_{PU} =5.1 $K\Omega$, Overdrive =10 mV		460		
Propagation Delay H To L	R_{PU} =5.1 $K\Omega$, Overdrive =100 mV		400)	
	R_{PU} =5.1 $K\Omega$, Overdrive =10 mV		950		ns
Propagation Delay L To H	R_{PU} =5.1 $K\Omega$, Overdrive =100 mV		850		
Fall Time	R_{PU} =5.1KΩ, Overdrive =100mV		36		ns



ELECTRICAL CHARACTERISTICS(Vs = 2.7V)

 $A_t T_A = 25$ °C, $V_{CM} = V_S/2$, unless otherwise noted.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
POWER SUPPLY					
Operating Voltage Range		2.1		5.5	V
Quiescent Current/per channel (Output High)			17		μА
Quiescent Current/per channel (Output Low)			30		μA
Power Supply Rejection Ratio	Vs = 2.1V to 5.5V, V _{CM} = V _S /2		70		dB
INPUT					
Input offset voltage		-4	±0.8	4	mV
Input Offset Voltage Drift			0.8		μV/°C
Common-Mode Voltage Range		(V-)-0.1		2.2	V
Common-mode Rejection Ratio	V _{CM} = -0.1V to 2.2V		70		dB
Input Bias Current			2		рА
Input Offset Current			1		pA
OUTPUT					
Saturation Voltage	l ₀ ≤4mA		82		mV
Output Pull-up Voltage Range				5.6	V
Output Current(sinking)	Vo≤1.5V		20		mA
SWITCHING	,				
Propagation Delay H To L	$R_{PU}=5.1K\Omega$, Overdrive =10mV		420		
Tropagation Bolay 11 To E	R_{PU} =5.1 $K\Omega$, Overdrive =100 mV		380		ns
Propagation Delay L To H	R _{PU} =5.1KΩ, Overdrive =10mV		900		
	R_{PU} =5.1K Ω , Overdrive =100 mV		880		
Fall Time	R _{PU} =5.1KΩ, Overdrive =100 mV		36		ns



ELECTRICAL CHARACTERISTICS(VS = 2.1V)

 $A_t T_A = 25$ °C, $V_{CM} = V_S/2$, unless otherwise noted.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
POWER SUPPLY					'
Operating Voltage Range		2.1		5.5	V
Quiescent Current/per channel (Output High)			15		μA
Quiescent Current/per channel (Output Low)			26		μА
Power Supply Rejection Ratio	Vs = 2.1V to 5.5V, V _{CM} = V _S /2		70		dB
INPUT					
Input offset voltage		-4	±0.8	4	mV
Input Offset Voltage Drift			0.8		μV/°C
Common-Mode Voltage Range		(V-)-0.1		1.3	V
Common-mode Rejection Ratio	V _{CM} = -0.1V to 1.3V		70		dB
Input Bias Current			2		рА
Input Offset Current			1		pA
ОИТРИТ					
Saturation Voltage	lo≤4mA		96		mV
Output Pull-up Voltage Range				5.6	V
Output Current(sinking)	V ₀ ≤1.5V		7		mA
SWITCHING				1	
Propagation Delay H To L	R_{PU} =5.1 $K\Omega$, Overdrive =10 mV		480		
Tropagation Bolay IT To E	R_{PU} =5.1 $K\Omega$, Overdrive =100 mV		430		ns
Propagation Delay L To H	R_{PU} =5.1 $K\Omega$, Overdrive =10 mV		820		
. Topagation Bolay E 10 11	R_{PU} =5.1 $K\Omega$, Overdrive =100 mV		800		
Fall Time	R _{PU} =5.1KΩ, Overdrive =100mV		38		ns



TYPICAL CHARACTERISTICS

At $T_A = 25$ °C, $V_S = +5V$, $V_{CM} = V_S/2$, unless otherwise noted.

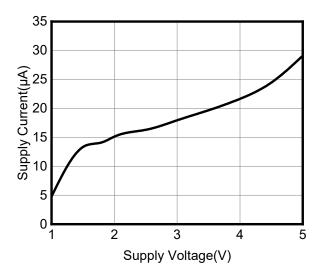


Figure 2. Supply Current vs Supply Voltage
Output High

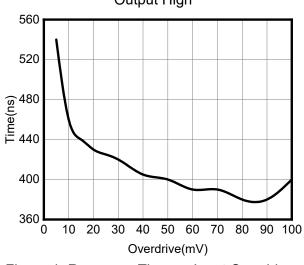


Figure 4. Response Time vs Input Overdrives Negative Transition(V_{CC}=5V)

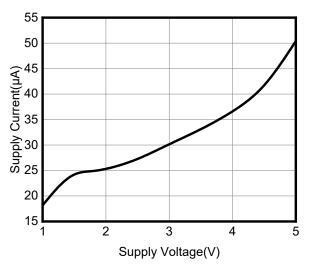


Figure 3. Supply Current vs Supply Voltage
Output Low

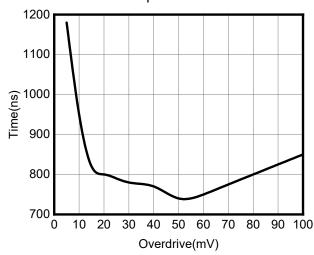
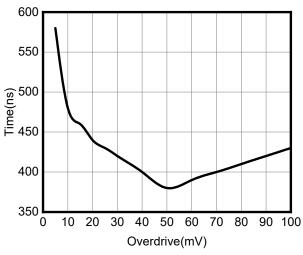


Figure 5. Response Time vs Input Overdrives Positive Transition(V_{CC}=5V)





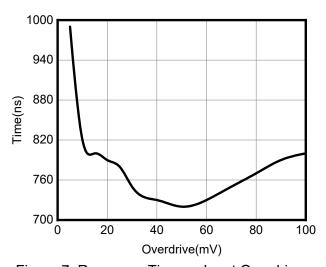


Figure 6. Response Time vs Input Overdrives Negative Transition(V_{CC}=2.1V)

Figure 7. Response Time vs Input Overdrives Positive Transition(V_{CC}=2.1V)

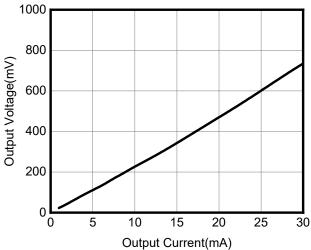


Figure 8. Output Voltage vs Output Current



DETAILED DESCRIPTION

Overview

The LMV331-MS, LMV393-MS and LMV339-MS family of comparators can operate up to 5. 5V on the supply pin.

This standard device has proven ubiquity and versatility across a wide range of applications.

This is due to its low power and high speed. The opendrain output allows the user to configure the output's logic low voltage (V_{OL}) and can be utilized to enable the comparator to be used in AND functionality.

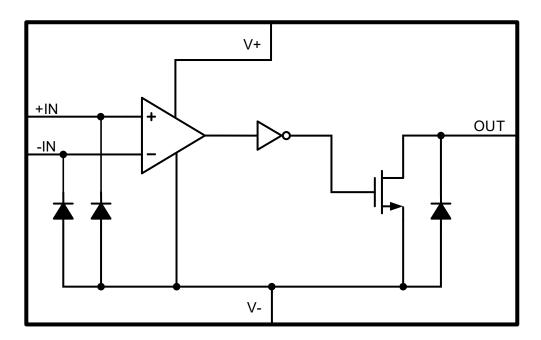


Figure 9. Functional Block Diagram



APPLICATION and IMPLEMENTATION

Application Information

LMV331-MS, LMV393-MS and LMV339-MS will typically be used to compare a single signal to a reference or two signals against each other. Many users take advantage of the open drain output (logic high with pull-up) to drive the comparison logic output to a logic voltage level to an MCU or logic device. **Typical Application**

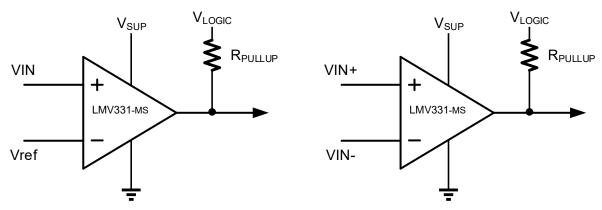


Figure 10. Typical Application Schematic

Power Supply Recommendations

For fast response and comparison applications with noisy or AC inputs, it is recommended to use a bypass capacitor on the supply pin to reject any variation on the supply voltage. This variation causes temporary fluctuations in the comparator's input common mode range and create an inaccurate comparison.

Layout

Layout Guidelines

For accurate comparator applications without hysteresis it is important maintain a stable power supply with minimized noise and glitches, which can affect the high-level input common mode voltage range. In order to achieve this, it is best to add a bypass capacitor between the supply voltage and ground. This should be implemented on the positive power supply and negative supply (if available). If a negative supply is not being used, do not put a capacitor between the IC's GND pin and system ground.

Layout Example

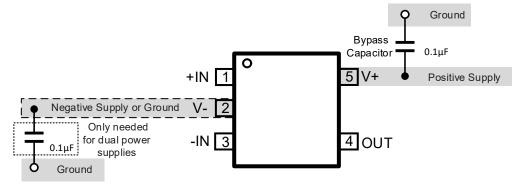
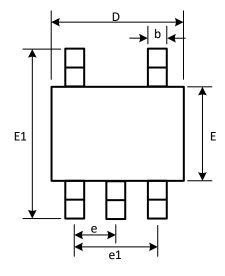


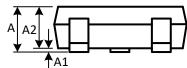
Figure 11. LMV331-MS Layout Example

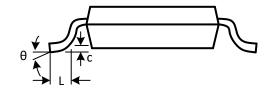


PACKAGE DESCRIPTION

SOT-23-5





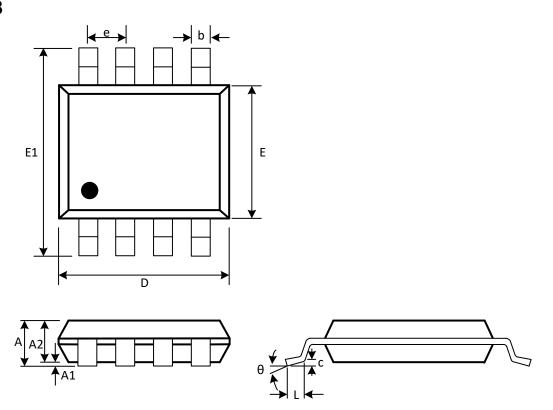


(Unit: mm)

Symbol	Min	Max
А	1.050	1.250
A1	0.000	0.100
A2	1.050	1.150
b	0.300	0.500
С	0.100	0.200
D	2.820	3.020
е	0.950	(BSC)
e1	1.800	2.000
E	1.500	1.700
E1	2.650	2.950
L	0.300	0.600
θ	0°	8°



SOP-8

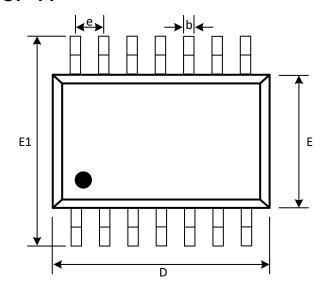


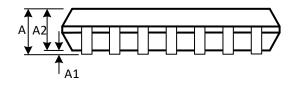
(Unit: mm)

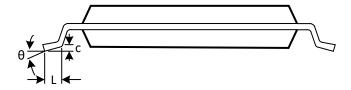
Symbol	Min	Max
A	1.350	1.750
A1	0.100	0.250
A2	1.350	1.550
b	0.330	0.510
С	0.170	0.250
D	4.800	5.000
е	1.270(BSC)	
E	3.800	4.000
E1	5.800	6.200
L	0.400	1.270
θ	0°	8°



SOP-14







(Unit: mm)

Symbol	Min	Max
А	1.350	1.750
A1	0.100	0.250
A2	1.350	1.550
b	0.310	0.510
С	0.100	0.250
D	8.450	8.850
е	1.270(BSC)	
E	5.800	6.200
E1	3.800	4.000
L	0.400	1.270
θ	0°	8°



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TLV3601QDBVRQ1 LM2901BQPWRQ1 TLV3603DCKR TLV3603DCKT LMV331IDBVR(MS) LMV331TP-MS LMV331W5-7(MS)
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