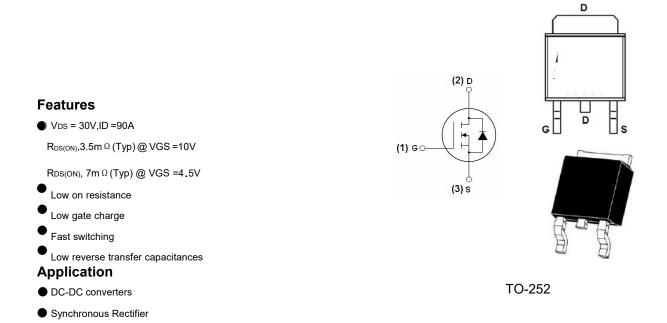


Product data sheet

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Schematic diagram



Absolute Maximum Ratings(TA=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	±20	V
Drain Current-Continuous ^{Note3}	TC=25 ℃		90	Α
Drain Current-Continuous	TC=100℃	- I _D	63	Α
Drain Current-Pulsed ^{Note1}	I _{DM}	200	Α	
Avalanche Energy ^{Note4}		E _{AS}	280	mJ
Avalanche Current		las	33	Α
Maximum Power Dissipation TC=25°C		PD	105	W
Storage Temperature Range	T _{STG}	-55 to +150	°C	
Operating Junction Temperature Range		TJ	-55 to +150	°C

Thermal Resistance

Parameter	Symbol	Min.	Тур.	Max	Unit
Thermal Resistance,Junction-to-Case	Rejc	-	3.3	-	°C/W



Electrical Characteristics(TJ=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _{DS} =250uA	30	-	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =30V,V _{GS} =0V	-	-	1	uA
Gate-Body Leakage	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Gate Threshold Voltage	VGS(TH)	$V_{DS}=V_{GS}$, $I_{DS}=250$ uA	1.0	1.7	2.5	V
Drain-Source On-State Resistance	RDS(ON)	V _{GS} =10V,I _{DS} =30A	-	3.5	5.5	m Ω
		V _{GS} =4.5V,I _{DS} =20A	-	7	8.9	

DYNAMIC CHARACTERISTICS							
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input Capacitance	Ciss		-	1963	-		
Output Capacitance	Coss	VDS =15V, VGS = 0V, f=1MHz	-	248	-	pF	
Reverse Transfer Capacitance	C _{rss}	I=IMH2	-	221	-		
Gate Resisitance	Da	VDD=0V,VGS=1V,		1.43		Ω	
	Rg	F=1MHz	-	1.43	-	Ω	

SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Turn-On Delay Time	T _{d(on)}		-	55	-	
Rise Time	tr	V_{GS} =10V, V_{DS} =15V,	-	36.4	-	
Turn-Off Delay Time	T _{d(off)}	$R_{GEN}=3\Omega I_D=20A$	-	37.5	-	ns
Fall Time	t _f		-	14	-	
Total Gate Charge at 10V	Qg		-	41	-	
Gate to Source Gate Charge	Q _{gs}	V _{DS} =15V,I _{DS} =45A, V _{GS} =10V	-	6.4	-	nC
Gate to Drain"Miller"Charge	Q _{gd}	VGS=10V	-	11	-	1

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _{DS} =20A	-	-	1.2	V	
Reverse Recovery Time	trr	TJ=25℃,IF=20A	-	21.7	-	nS	
Reverse Recovery Charge	Qrr	di/dt=100A/us	-	7.2	-	nC	

Notes:

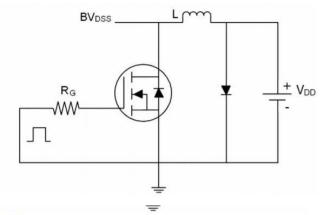
- 1: Repetitive rating, pulse width limited by maximum junction temperature.
- 2: Surface mounted on FR4 Board, t≤10sec.
- 3: Pulse width \leq 300µs, duty cycle \leq 2%.
- 4: EAS condition: L=0.5mH,VDD=15V,VG=10V,V_{GATE}=30V,Start TJ=25 $^\circ\!\mathrm{C}.$



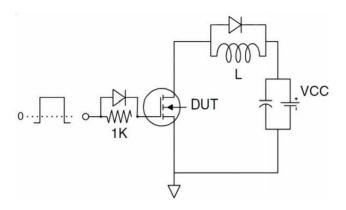


Test Circuit

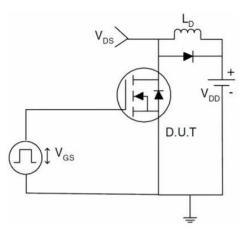
1) EAS Test Circuit



2) Gate Charge Test Circuit



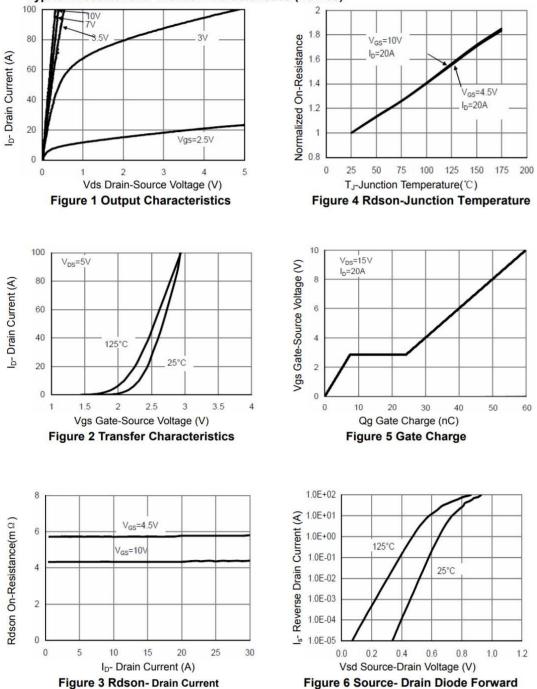
3) Switch Time Test Circuit







Typical Electrical and Thermal Characteristics (Curves)





4000

MS100N03 HF Semiconductor Compiance

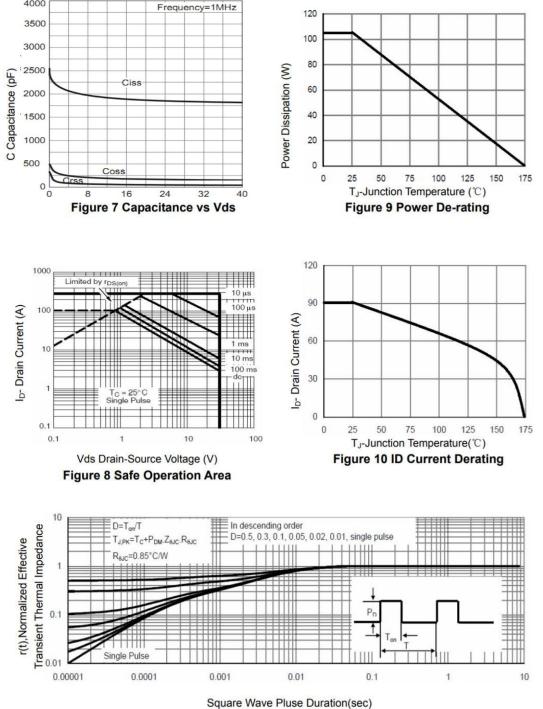
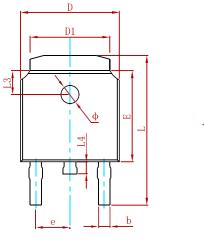


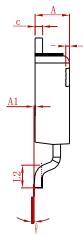
Figure 11 Normalized Maximum Transient Thermal Impedance



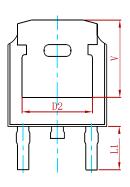


PACKAGE MECHANICAL DATA



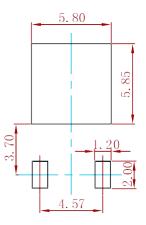


h



0. mahal	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830	REF.	0.190	REF.
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900	REF.	0.114	REF.
L2	1.400	1.700	0.055	0.067
L3	1.600	REF.	0.063	REF.
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250	REF.	0.207	REF.

Suggested Pad Layout



Note:

1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm

3. The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MS100N03	TO-252	2500



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