# MSKSEMI 美森科







TSS



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GDT



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## **MS10N10**

**Product specification** 





#### **General Description**

• These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. advanced technology has This been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

#### **General Features**

- 100V,12A, RDS(ON)=115mΩ@VGS=
- 10V Improved dv/dt capability
- Fast switching
- Green Device Available

## **Application**

- Networking
- Load Switch
- LED applications

#### **Reference News**

PACKAGE OUTLINE	Pin Configuration	Marking
	G	MSKSEMI 10N10 MS10N
TO-252	s	



## AbsoluteMaximumRatingsTc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	100	V
Vgs	Gate-Source Voltage	±20	V
<b>l</b> D	Drain Current – Continuous (Tc=25°C)	12	А
	Drain Current – Continuous (Tc=100°C)	7.6	А
Ірм	Drain Current – Pulsed <sup>1</sup>	48	А
EAS	Single Pulse Avalanche Energy <sup>2</sup>	6	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	11	А
Po	Power Dissipation (Tc=25°C)	34.7	W
	Power Dissipation – Derate above 25℃	0.27	W/°C
Тѕтс	Storage Temperature Range	-55 to 150	℃
TJ	Operating Junction Temperature Range	-55 to 150	℃

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		62	°C/W
Reuc	Thermal Resistance Junction to Case		3.1	°C/W

## Electrical Characteristics (TJ=25 $^{\circ}$ C , unless otherwise noted)

#### Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	100			٧
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient Reference to 25°C , I <sub>D</sub> =1mA			0.09		V/°C
lana	Drain Course Leakage Current	V <sub>DS</sub> =100V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =80V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			10	uA
Igss	Gate-Source Leakage Current	V <sub>GS=</sub> ±20V , V <sub>DS</sub> =0V			±100	nA



#### On Characteristics

Rds(on)	Static Drain-Source On-Resistance	Vgs=10V , Ip=10A		115	130	mΩ
TADS(ON)	Static Brain Course On Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =8A		120	150	mΩ
VGS(th)	Gate Threshold Voltage	Vgs=Vps . Ip =250uA	1.0	1.6	2.5	V
△VGS(th)	V <sub>GS(th)</sub> Temperature Coefficient	V65-V65, 16-2000/1		-5		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =2A		8.7		S

#### Dynamic and switching Characteristics

Dynamic	and Switching On	aracteristics		
Qg	Total Gate Charge <sup>3,4</sup>		 20	
Qgs	Gate-Source Charge <sup>3,4</sup>	V <sub>DS</sub> =50V , V <sub>GS</sub> =10V , I <sub>D</sub> =2A	 3.2	 nC
$\mathbf{Q}_{gd}$	Gate-Drain Charge <sup>3,4</sup>		 3.6	
Td(on)	Turn-On Delay Time <sup>3,4</sup>		 18	
Tr	Rise Time <sup>3,4</sup>	V <sub>DD</sub> =50V , V <sub>GS</sub> =10V , R <sub>G</sub> =3.3Ω	 4	 no
Td(off)	Turn-Off Delay Time <sup>3,4</sup>	lo=1A	 40	 ns
Tf	Fall Time <sup>3,4</sup>		 3	
Ciss	Input Capacitance		 1400	
Coss	Output Capacitance V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , F=1		 60	 pF
Crss	Reverse Transfer Capacitance		 35	
Rg	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	 2	 Ω

## Drain- Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			12	Α
Isм	Pulsed Source Current	TVG-VD-0V, Force Current			24	Α
VsD	Diode Forward Voltage	V <sub>G</sub> s=0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1.2	V
trr	Reverse Recovery Time <sup>3</sup>	la=4 A d1/dt=400 A /u.o		38		ns
Qrr	Reverse Recovery Charge <sup>3</sup>	Is=1A , dI/dt=100A/μs Τ <sub>J</sub> =25°C		27		nC

#### Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0. 1mH, $I_{AS}$ =11A., $R_G$ =25 $\Omega$ ,Starting  $T_J$ =25 $^{\circ}$ C .
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 4. Essentially independent of operating temperature.

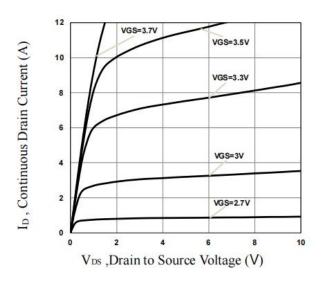


Fig.1 Typical Output Characteristics

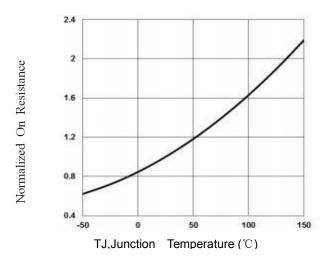


Fig. 3 Normalized RDSON vs. T<sub>J</sub>

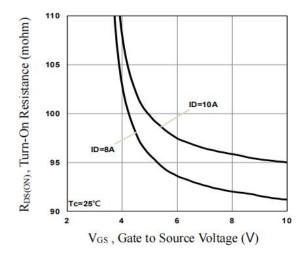


Fig. 5 Turn-On Resistance vs. V<sub>GS</sub>

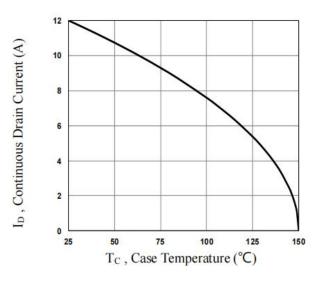


Fig.2 Continuous Drain Current vs. Tc

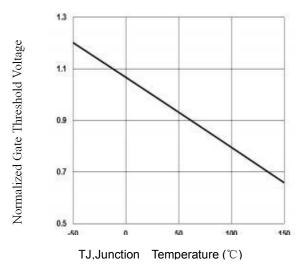


Fig. 4 Normalized V<sub>th</sub> vs. T<sub>J</sub>

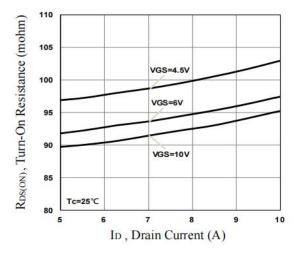
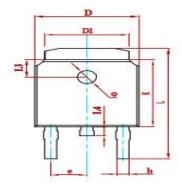


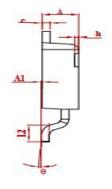
Fig.6 Turn-On Resistance vs. ID

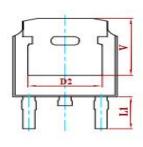




## PACKAGE MECHANICAL DATA

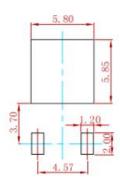






Symbol	Dimensions	In Millimeters	Dimensions	In Inches
Syllibol	Min.	Max.	Min.	Max.
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830	REF.	0.190	REF.
E	6.000	6.200	0.236	0.244
е	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114	REF.
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063	REF.
L4	0.600	1.000	0.024	0.039
Ф	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250	REF.	0.207	REF.

# **Suggested Pad Layout**



#### Note:

- 1 Controlling dimension in millimeters.
- 2.General tolerance:±0.05mm.
- 3. The pad layout is for reference purposes only.

#### **REEL SPECIFICATION**

P/N	PKG	QTY
MS10N10	TO-252	2500



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DMN1017UCP3-7 EFC2J004NUZTDG P85W28HP2F-7071 DMN1053UCP4-7 NTE2384 DMC2700UDMQ-7 DMN2080UCB4-7
DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B IPS60R3K4CEAKMA1 DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 IPS60R360PFD7SAKMA1
DMN2990UFB-7B SSM3K35CT,L3F IPLK60R1K0PFD7ATMA1 2N7002W-G MCAC30N06Y-TP IPWS65R035CFD7AXKSA1
MCQ7328-TP SSM3J143TU,LXHF DMN12M3UCA6-7 PJMF280N65E1\_T0\_00201 PJMF380N65E1\_T0\_00201
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