

ESD



TVS



TSS



MOV



GDT



PLED

---

**MS18N50P/F**  
Product specification

---

## Description

The MS18N50P/F can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-220/ TO-220F, which accords with the RoHS standard

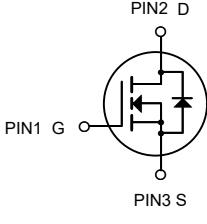
## General Features

- $V_{DS}=500V, I_D=18A$
- $R_{DS(ON)} < 0.36\Omega @ V_{GS}=10V$

## Application

- Power switch circuit of adaptor and charger

## Reference News

PACKAGE OUTLINE		N-Channel MOSFET	Marking	
		 PIN1 G    PIN2 D PIN3 S	<b>MSKSEMI 18N50 MS ***</b>	<b>MSKSEMI 18N50 MS ***</b>
<b>TO-220</b>	<b>TO-220F</b>		<b>MS18N50P</b>	<b>MS18N50F</b>

Note : \*\*\*\*Representative production cycle

## Absolute Maximum Ratings@T=25°C(unless otherwise specified)

Symbol	Parameter	MS18N50P	MS18N50F	Unit
$V_{DSS}$	Drain-to-Source Voltage <sup>[1]</sup>	500		V
$V_{GSS}$	Gate-to-Source Voltage	$\pm 30$		
$I_D$	Continuous Drain Current	17		A
$I_D @ T_c=100^\circ C$	Continuous Drain Current @ $T_c=100^\circ C$	Figure 3		
$I_{DM}$	Pulsed Drain Current at $V_{GS}=10V$ <sup>[2]</sup>	Figure 6		
$E_{AS}$	Single Pulse Avalanche Energy	1000		mJ
$dv/dt$	Peak Diode Recovery $dv/dt$ <sup>[3]</sup>	5.0		V/ns
$P_D$	Power Dissipation	150	45	W
$T_L$ TPAK	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10 seconds, Package Body for 10 seconds	300 260		$^\circ C$
$T_J$ & $T_{STG}$	Operating and Storage Temperature Range	-55 to 150		
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.84	2.78	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62	100	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

**Electrical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	500	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$
$\text{I}_{\text{DSS}}$	Drain-to-Source Leakage Current	--	--	1	uA	$\text{V}_{\text{DS}}=500\text{V}, \text{V}_{\text{GS}}=0\text{V}$
		--	--	100		$\text{V}_{\text{DS}}=400\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=125^\circ\text{C}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Current	--	--	+100	nA	$\text{V}_{\text{GS}}=+30\text{V}, \text{V}_{\text{DS}}=0\text{V}$
		--	--	-100		$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$
$\text{R}_{\text{DS(ON)}}$	Static Drain-to-Source On-Resistance <sup>[4]</sup>	--	0.3	0.36	$\Omega$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=8.5\text{A}$
$\text{V}_{\text{GS(TH)}}$	Gate Threshold Voltage	2.0	--	4.0	V	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$
$\text{g}_{\text{fs}}$	Forward Transconductance <sup>[4]</sup>	--	15	--	S	$\text{V}_{\text{DS}}=30\text{V}, \text{I}_D=17\text{A}$
$\text{C}_{\text{iss}}$	Input Capacitance	--	2500	--	pF	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}, \text{f}=1.0\text{MHz}$
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance	--	280	--		
$\text{C}_{\text{oss}}$	Output Capacitance	--	800	--		
$\text{Q}_{\text{g}}$	Total Gate Charge	--	45	--	nC	$\text{V}_{\text{DD}}=250\text{V}, \text{I}_D=17\text{A}, \text{V}_{\text{GS}}=0 \text{ to } 10\text{V}$
$\text{Q}_{\text{gs}}$	Gate-to-Source Charge	--	10	--		
$\text{Q}_{\text{gd}}$	Gate-to-Drain (Miller) Charge	--	18	--		
$\text{t}_{\text{d(ON)}}$	Turn-on Delay Time	--	15	--	nS	$\text{V}_{\text{DD}}=250\text{V}, \text{I}_D=17\text{A}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_G=6.1\Omega$
$\text{t}_{\text{rise}}$	Rise Time	--	35	--		
$\text{t}_{\text{d(OFF)}}$	Turn-Off Delay Time	--	52	--		
$\text{t}_{\text{fall}}$	Fall Time	--	40	--		
$\text{I}_{\text{SD}}$	Continuous Source Current <sup>[4]</sup>	--	--	18	A	Integral PN-diode in MOSFET
$\text{I}_{\text{SM}}$	Pulsed Source Current <sup>[4]</sup>	--	--	68		
$\text{V}_{\text{SD}}$	Diode Forward Voltage	--	--	1.5	V	$\text{I}_S=17\text{A}, \text{V}_{\text{GS}}=0\text{V}$
$\text{trr}$	Reverse recovery time	--	220	--	ns	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_F=17\text{A}, \text{diF/dt}=100\text{A}/\mu\text{s}$
$\text{Qrr}$	Reverse recovery charge	--	2.5	--	uC	

**Note:**

[1]  $\text{T}_J=+25^\circ\text{C}$  to  $+150^\circ\text{C}$

[2] Repetitive rating; pulse width limited by maximum junction temperature.

[3]  $\text{I}_{\text{SD}}=17\text{A}$   $\text{di/dt} < 100 \text{ A}/\mu\text{s}$ ,  $\text{V}_{\text{DD}} < \text{BV}_{\text{DSS}}$ ,  $\text{T}_J=+150^\circ\text{C}$ .

[4] Pulse width  $\leq 380\mu\text{s}$ ; duty cycle  $\leq 2\%$ .

## Typical Characteristics(Cont.)

Duty Factor

Figure 1. Maximum Effective Thermal Impedance, Junction-to-Case

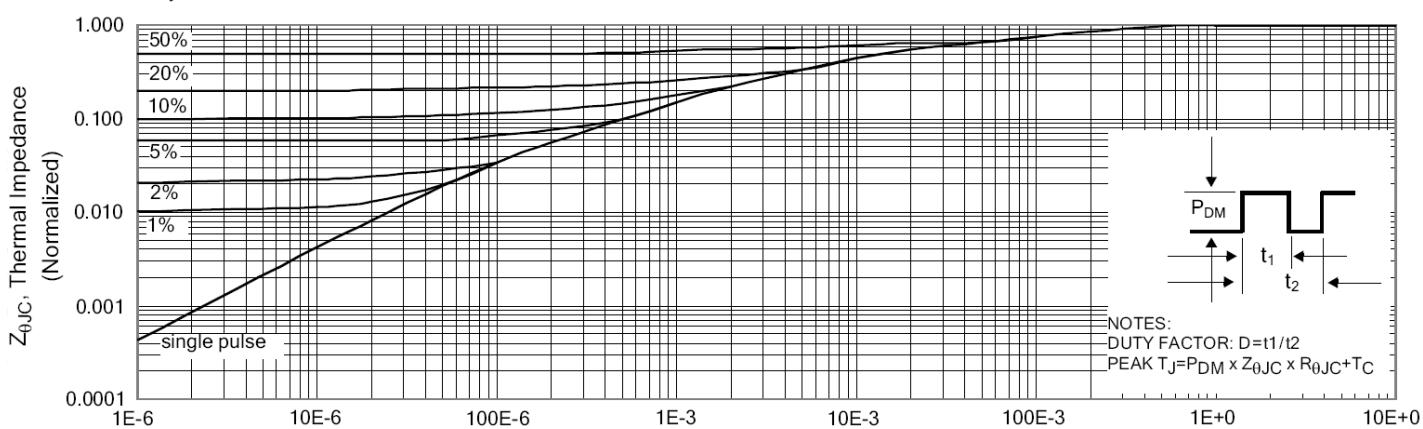


Figure 2. Maximum Power Dissipation vs Case Temperature

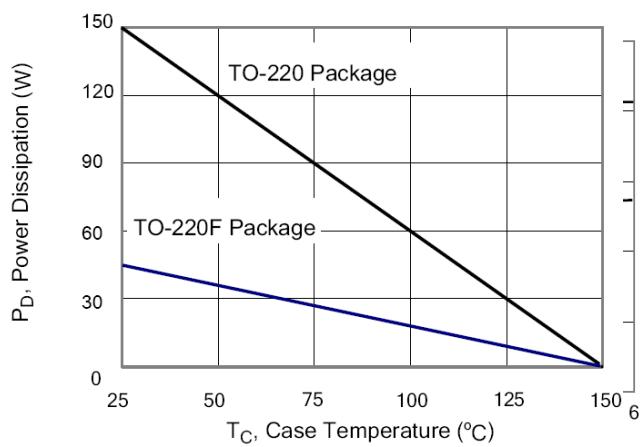


Figure 4. Typical Output Characteristics

$t_p$ , Rectangular Pulse Duration (s)  
 $t_p$ , Pulse Width (s)

Figure 3. Maximum Continuous Drain Current vs Case Temperature

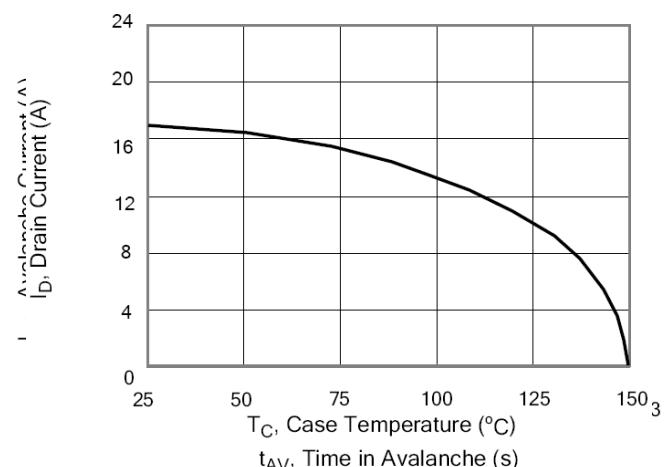
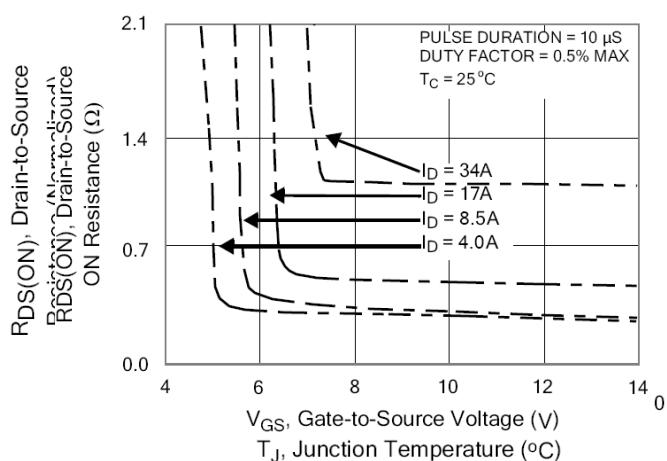
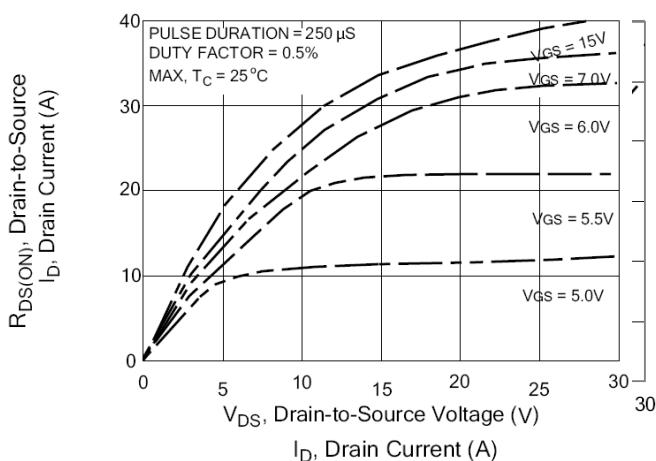


Figure 5. Typical Drain-to-Source ON Resistance vs Gate Voltage and Drain Current



## Typical Characteristics(Cont.)

Figure 6. Maximum Peak Current Capability

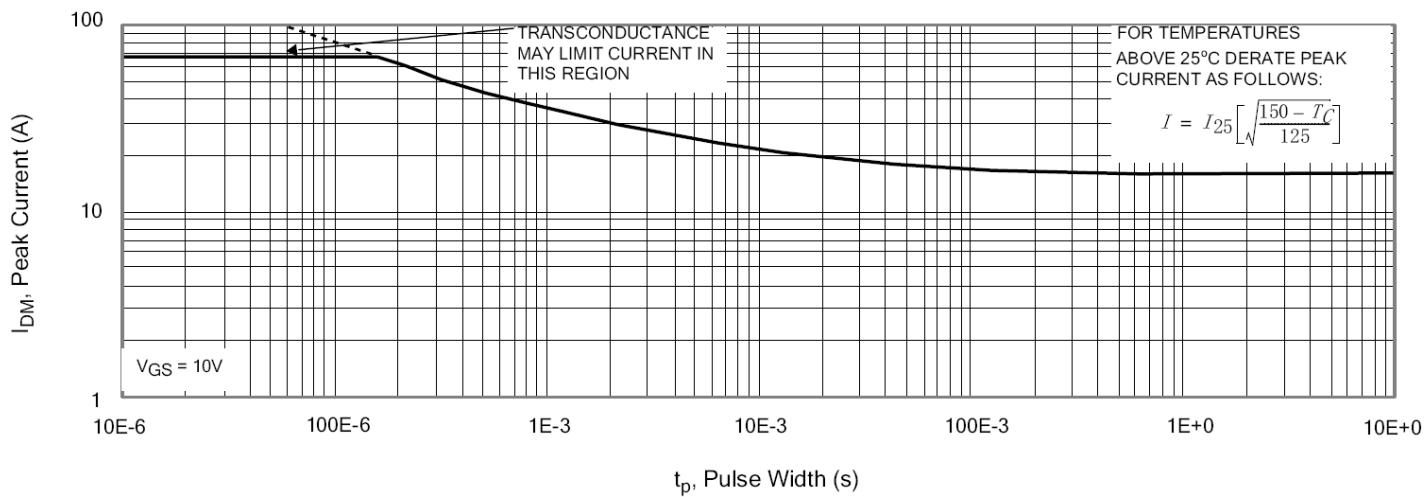


Figure 7. Typical Transfer Characteristics

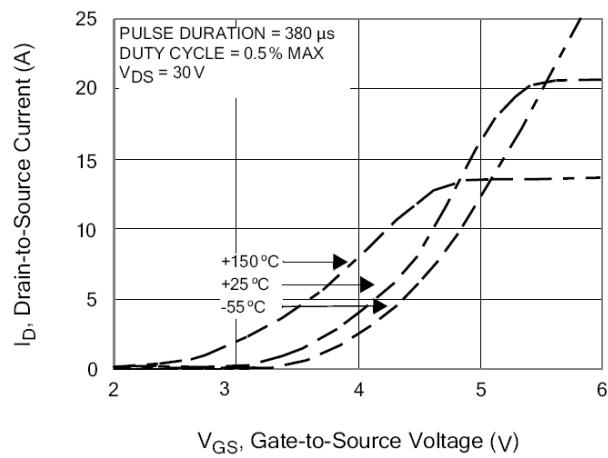


Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current

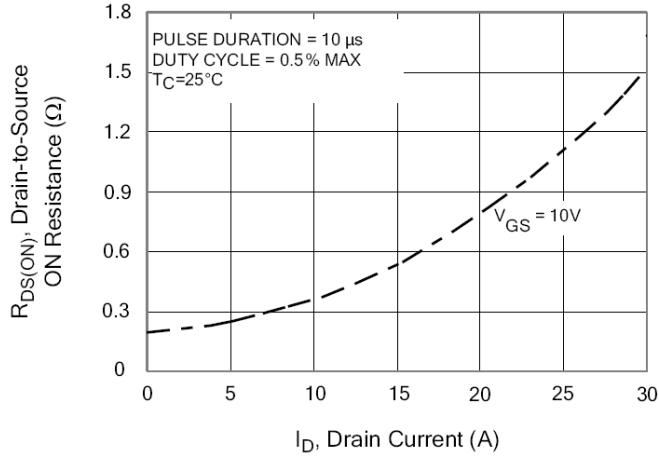


Figure 8. Unclamped Inductive Switching Capability

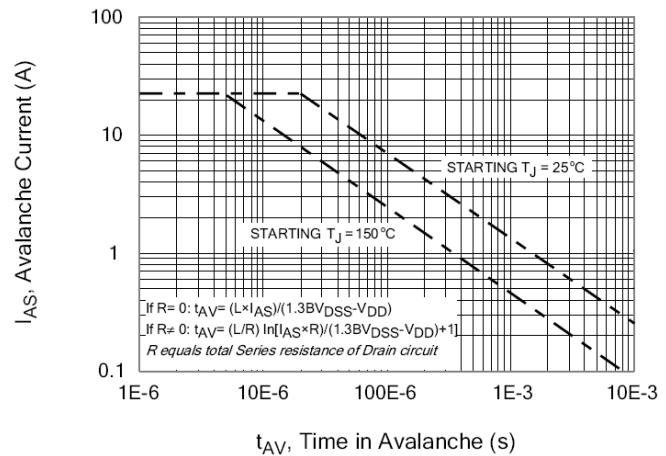
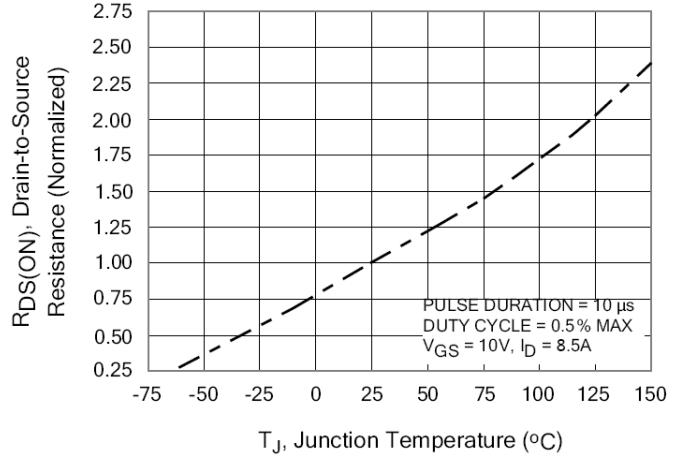


Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature



## Typical Characteristics(Cont.)

Figure 11. Typical Breakdown Voltage vs Junction Temperature

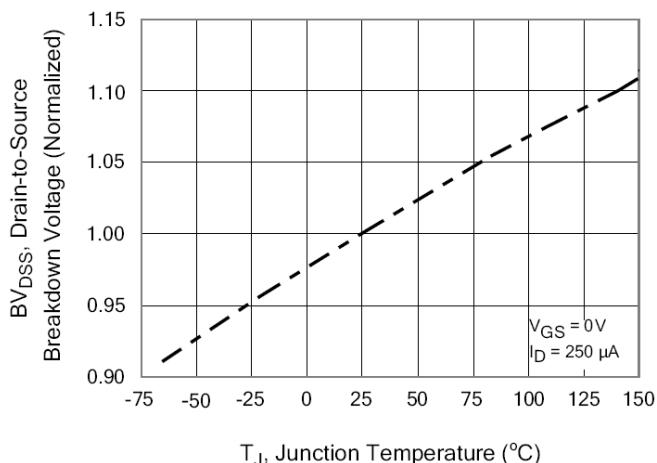


Figure 13. Maximum Forward Bias Safe Operating Area

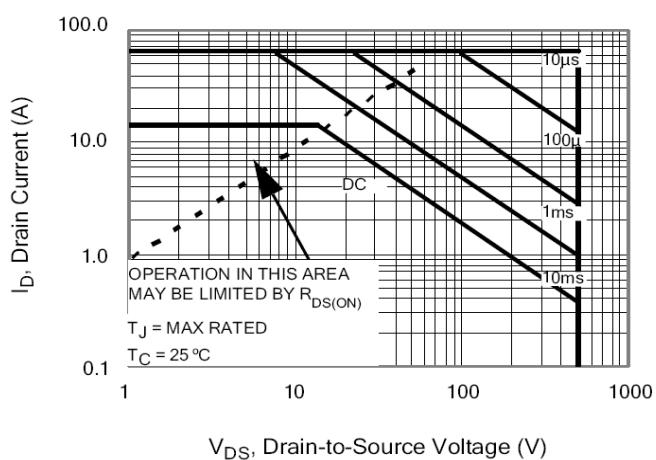


Figure 15. Typical Gate Charge vs Gate-to-Source Voltage

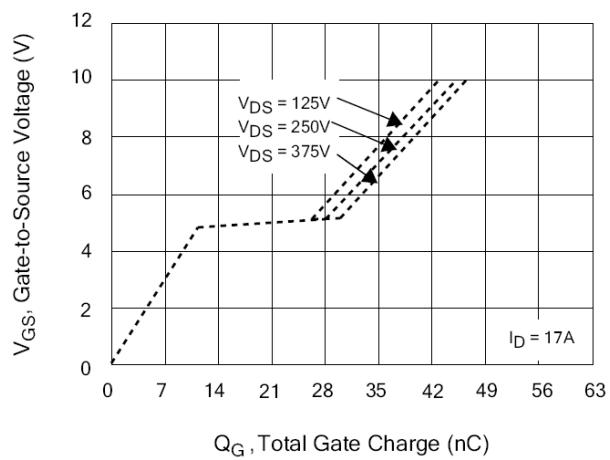


Figure 12. Typical Threshold Voltage vs Junction Temperature

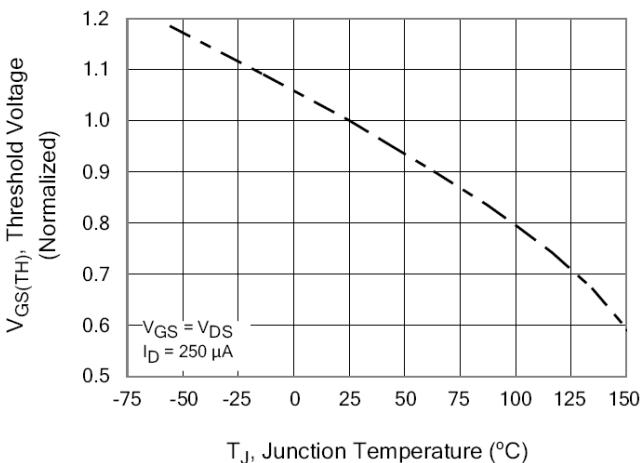


Figure 14. Typical Capacitance vs Drain-to-Source Voltage

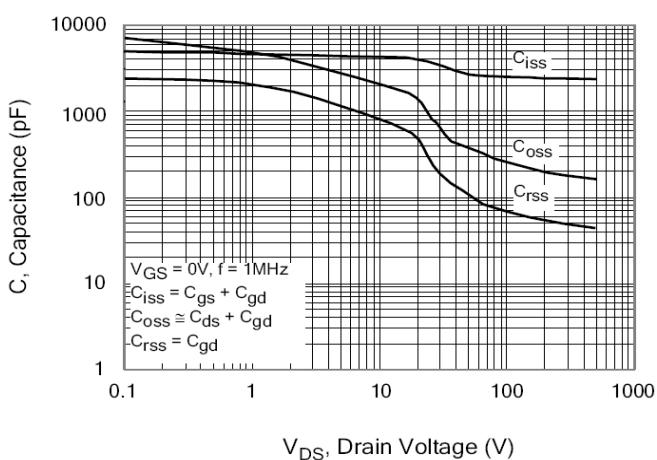
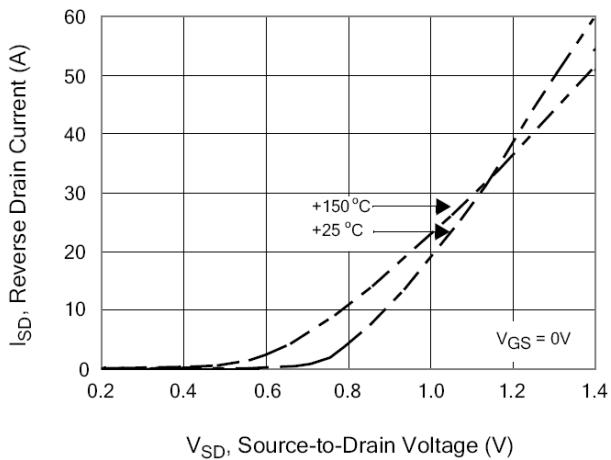


Figure 16. Typical Body Diode Transfer Characteristics



## Test Circuits and Waveforms

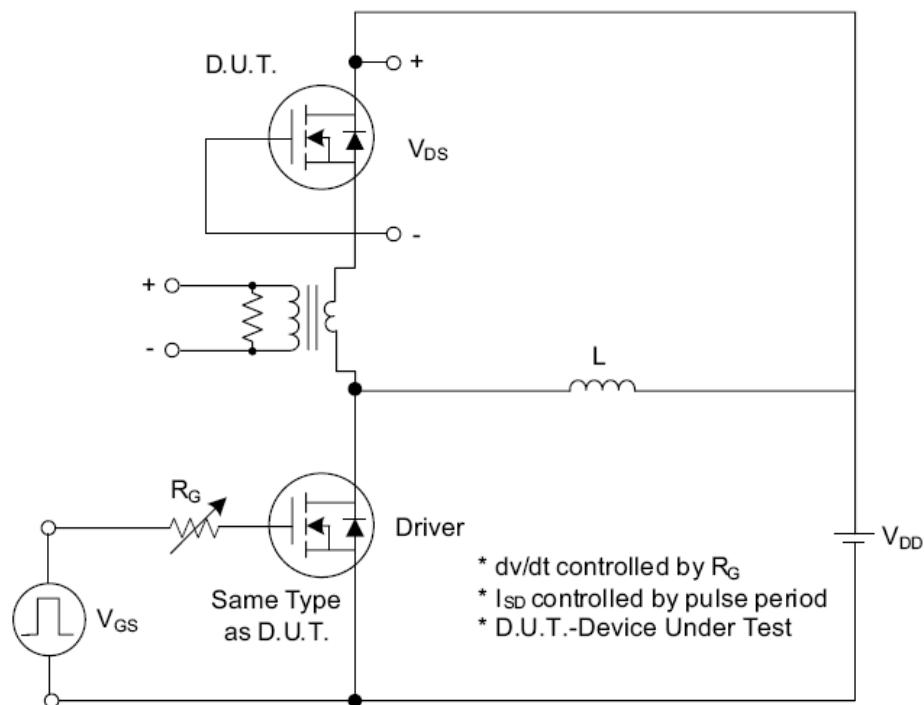


Fig. 1.1 Peak Diode Recovery dv/dt Test Circuit

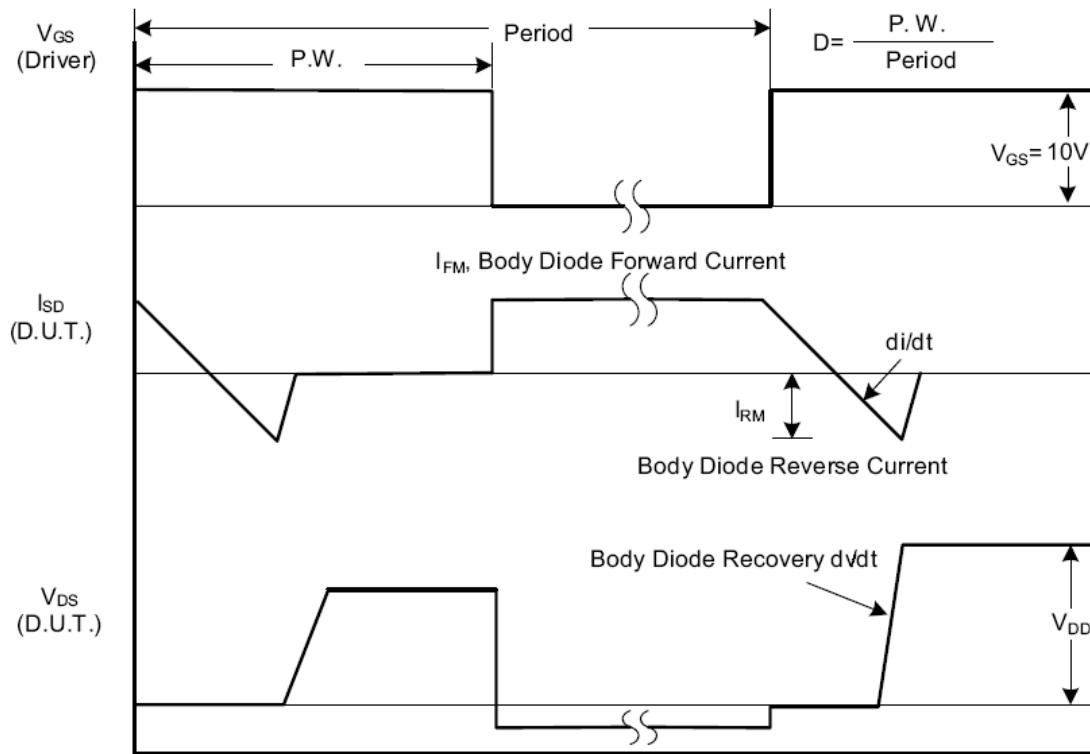


Fig. 1.2 Peak Diode Recovery dv/dt Waveforms

## Test Circuits and Waveforms (Cont.)

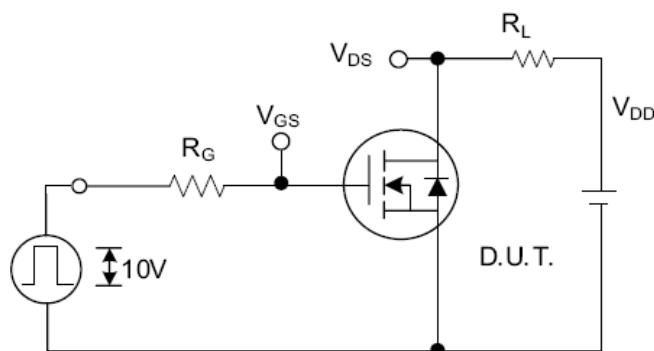


Fig. 2.1 Switching Test Circuit

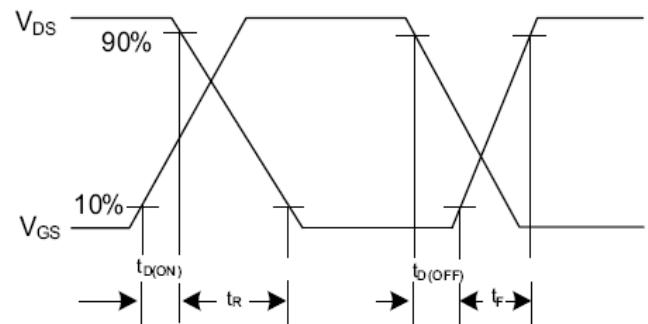


Fig. 2.2 Switching Waveforms

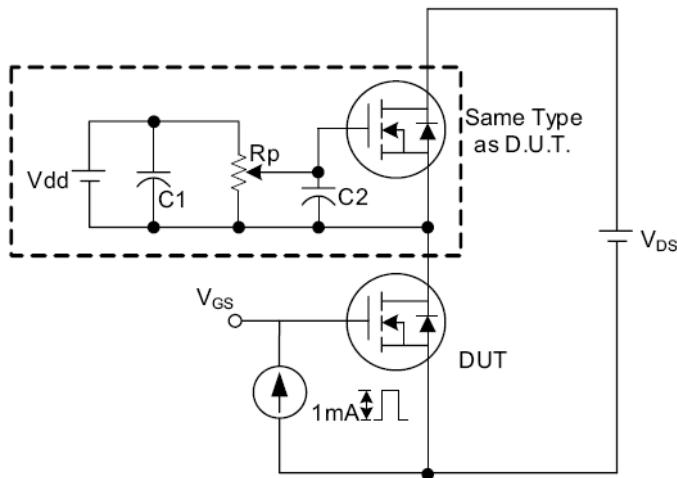


Fig. 3.1 Gate Charge Test Circuit

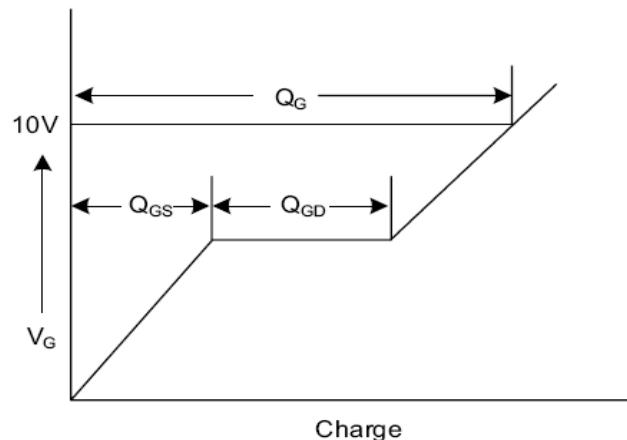


Fig. 3.2 Gate Charge Waveform

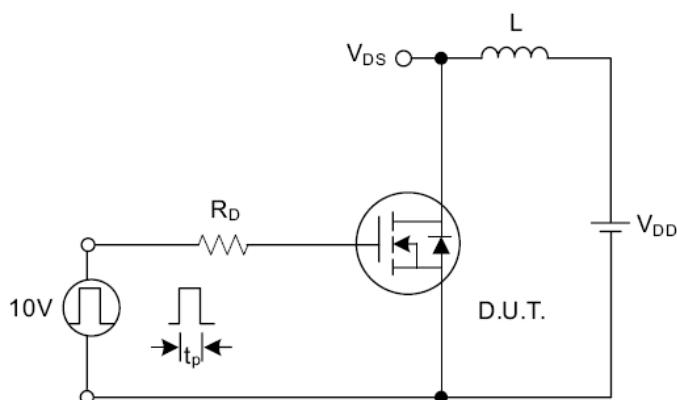


Fig. 4.1 Unclamped Inductive Switching Test Circuit

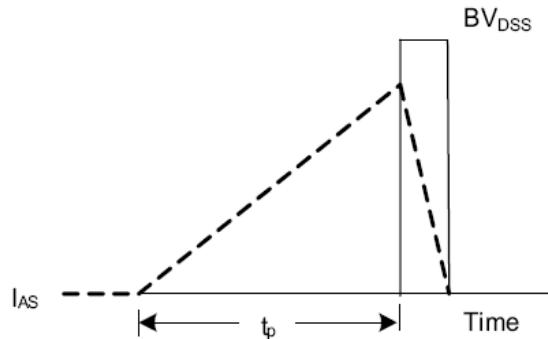
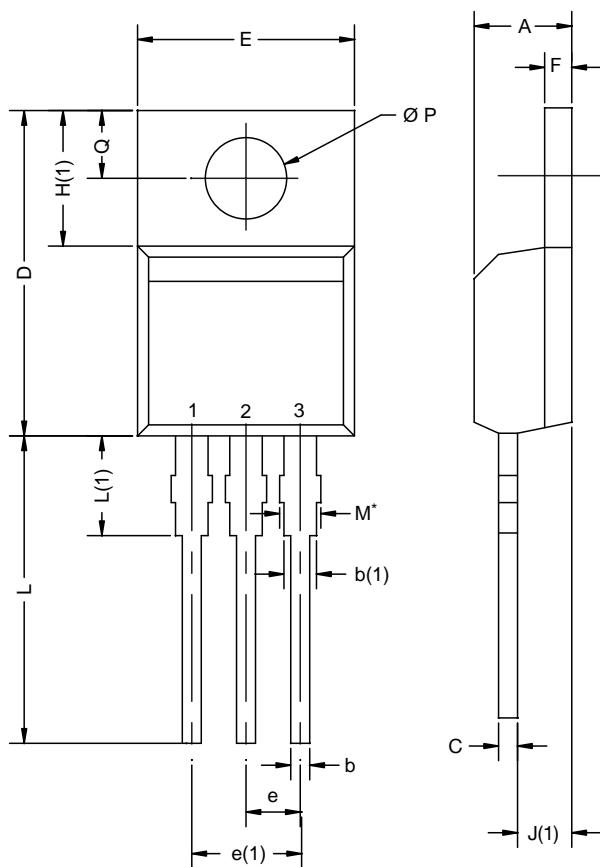


Fig. 4.2 Unclamped Inductive Switching Waveforms

## Package Dimension TO-220



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
c	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
E	10.04	10.51	0.395	0.414
e	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
Ø P	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118

ECN: X12-0208-Rev. N, 08-Oct-12  
DWG: 5471

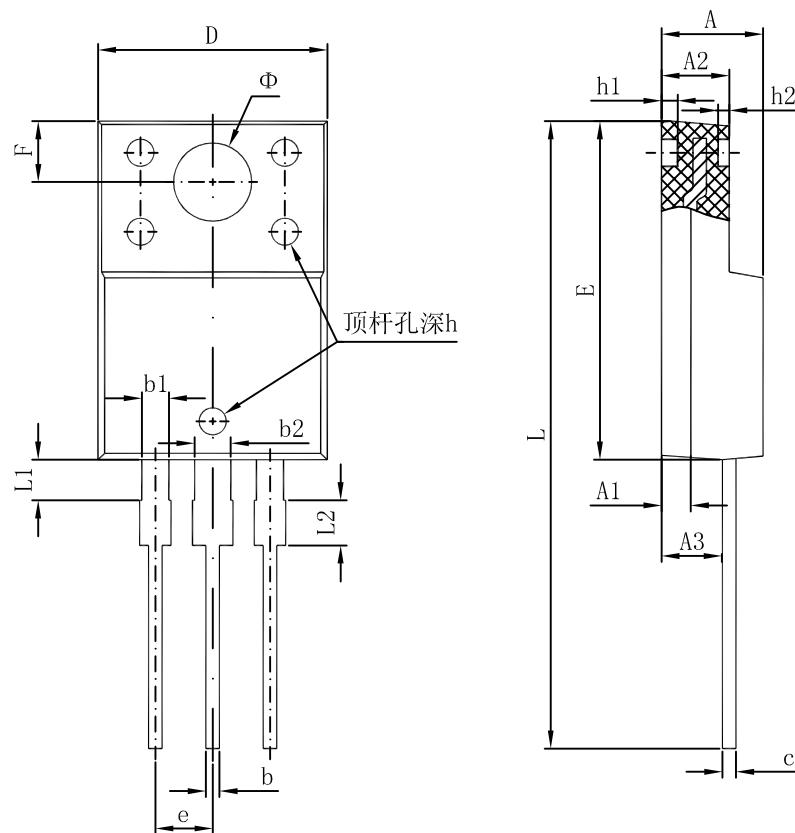
**Notes**

\* M = 1.32 mm to 1.62 mm (dimension including protrusion)  
Heatsink hole for HVM

## REEL SPECIFICATION

P/N	PKG	QTY
MS18N50P	TO-220	1 tube of 50pcs/1 box of 1000pcs

### Package Dimension TO-220F



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.300	4.700	0.169	0.185
A1	1.300 REF.		0.051 REF.	
A2	2.800	3.200	0.110	0.126
A3	2.500	2.900	0.098	0.114
b	0.500	0.750	0.020	0.030
b1	1.100	1.350	0.043	0.053
b2	1.500	1.750	0.059	0.069
c	0.500	0.750	0.020	0.030
D	9.960	10.360	0.392	0.408
E	14.800	15.200	0.583	0.598
e	2.540 TYP.		0.100 TYP.	
F	2.700 REF.		0.106 REF.	
Φ	3.500 REF.		0.138 REF.	
h	0.000	0.300	0.000	0.012
h1	0.800 REF.		0.031 REF.	
h2	0.500 REF.		0.020 REF.	
L	28.000	28.400	1.102	1.118
L1	1.700	1.900	0.067	0.075
L2	1.900	2.100	0.075	0.083

### REEL SPECIFICATION

P/N	PKG	QTY
MS18N50F	TO-220F	1 tube of 50pcs/1 box of 1000pcs

## **Attention**

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.

# X-ON Electronics

Largest Supplier of Electrical and Electronic Components

*Click to view similar products for MOSFET category:*

*Click to view products by MSKSEMI manufacturer:*

Other Similar products are found below :

[IRFD120](#) [JANTX2N5237](#) [BUK455-60A/B](#) [MIC4420CM-TR](#) [VN1206L](#) [NDP4060](#) [SI4482DY](#) [IPS70R2K0CEAKMA1](#) [SQD23N06-31L-GE3](#)  
[TK16J60W,S1VQ\(O](#) [2SK2614\(TE16L1,Q\)](#) [DMN1017UCP3-7](#) [DMN1053UCP4-7](#) [SQJ469EP-T1-GE3](#) [NTE2384](#) [DMC2700UDMQ-7](#)  
[DMN2080UCB4-7](#) [DMN61D9UWQ-13](#) [US6M2GTR](#) [DMN31D5UDJ-7](#) [DMP22D4UFO-7B](#) [DMN1006UCA6-7](#) [DMN16M9UCA6-7](#)  
[STF5N65M6](#) [IRF40H233XTMA1](#) [STU5N65M6](#) [DMN6022SSD-13](#) [DMN13M9UCA6-7](#) [DMTH10H4M6SPS-13](#) [DMN2990UFB-7B](#)  
[IPB80P04P405ATMA2](#) [2N7002W-G](#) [MCAC30N06Y-TP](#) [MCQ7328-TP](#) [NTMC083NP10M5L](#) [NVMFS2D3P04M8LT1G](#) [BXP7N65D](#)  
[BXP4N65F](#) [AOL1454G](#) [WMJ80N60C4](#) [BXP2N20L](#) [BXP2N65D](#) [BXT1150N10J](#) [BXT1700P06M](#) [TSM60NB380CP](#) [ROG](#) [RQ7L055BGTCR](#)  
[DMNH15H110SK3-13](#) [SLF10N65ABV2](#) [BSO203SP](#) [BSO211P](#)