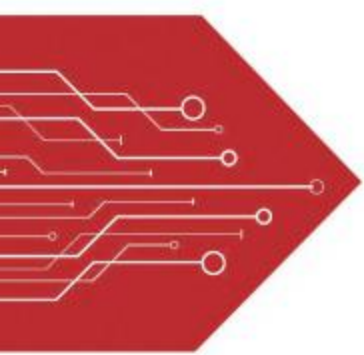


MSKSEMI

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



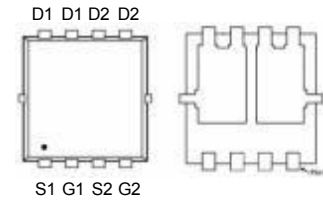
PLED

Product data sheet

Description

The MSK7804 is the high cell density trenched N-ch MOSFETs, which provide excellent R_{DS(on)} and gate charge for most of the synchronous buck converter applications.

The MSK7804 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

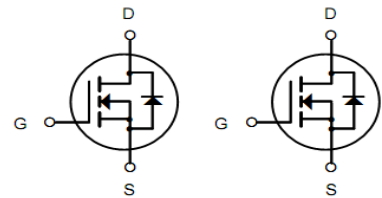


DFN3X3-8L

Product Summary

BVDSS	R _{DS(on)}	I _D
30V	12mΩ	30A

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology



Dual N-Channel MOSFET

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _{D@TA=25°C}	Continuous Drain Current, V _{GS} @ 10V ¹	30	A
I _{D@TA=70°C}	Continuous Drain Current, V _{GS} @ 10V ¹	18	A
I _{DM}	Pulsed Drain Current ²	50	A
EAS	Single Pulse Avalanche Energy ³	24.2	mJ
I _{AS}	Avalanche Current	22	A
P _{D@TA=25°C}	Total Power Dissipation ⁴	1.5	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	85	°C/W
R _{θJL}	Thermal Resistance Junction-Case ¹	---	25	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C , I _D =1mA	---	0.023	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =8A	---	10	12	mΩ
		V _{GS} =4.5V , I _D =6A	---	15	18	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	---	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5.08	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =25°C	---	---	1	uA
		V _{DS} =24V , V _{GS} =0V , T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V , I _D =8A	---	24	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	1.8	---	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =15V , V _{GS} =4.5V , I _D =8A	---	9.63	---	nC
Q _{gs}	Gate-Source Charge		---	3.88	---	
Q _{gd}	Gate-Drain Charge		---	3.44	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V , V _{GS} =10V , R _G =1.5Ω I _D =8A	---	4.2	---	ns
T _r	Rise Time		---	8.2	---	
T _{d(off)}	Turn-Off Delay Time		---	31	---	
T _f	Fall Time		---	4	---	
C _{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz	---	940	---	pF
C _{oss}	Output Capacitance		---	131	---	
C _{rss}	Reverse Transfer Capacitance		---	109	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current	---	---	9	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time	I _F =8A , di/dt=100A/μs , T _J =25°C	---	8	---	nS
Q _{rr}	Reverse Recovery Charge		---	2.9	---	nC

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=22A
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

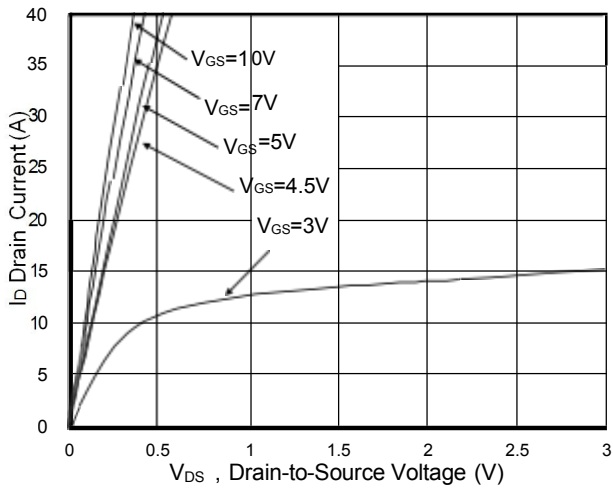


Fig.1 Typical Output Characteristics

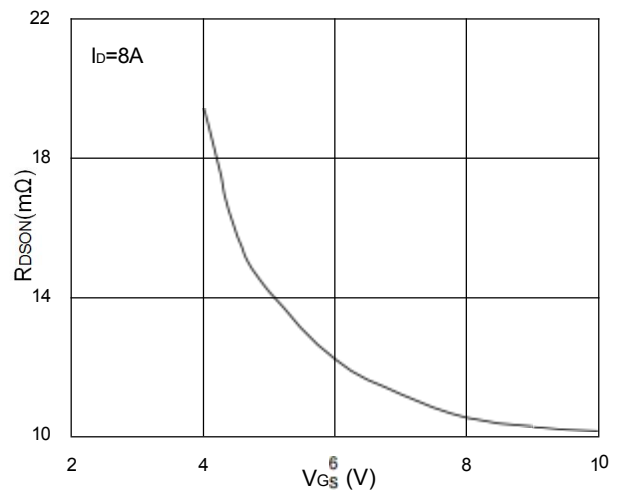


Fig.2 On-Resistance vs. G-S Voltage

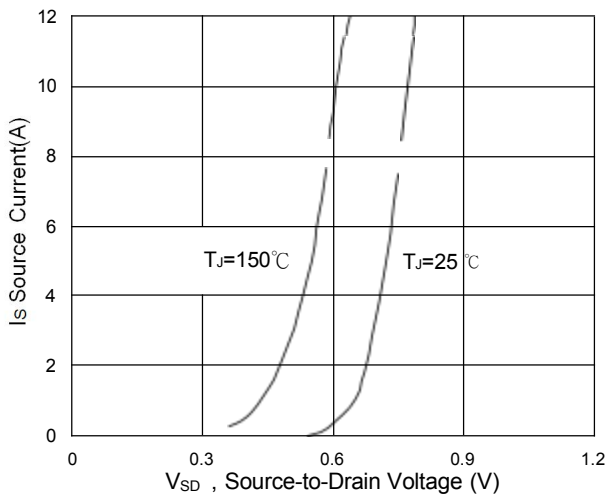


Fig.3 Source Drain Forward Characteristics

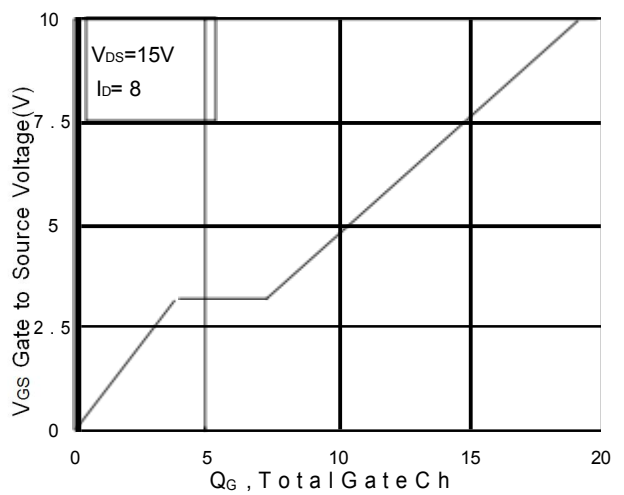


Fig.4 Gate-Charge Characteristics

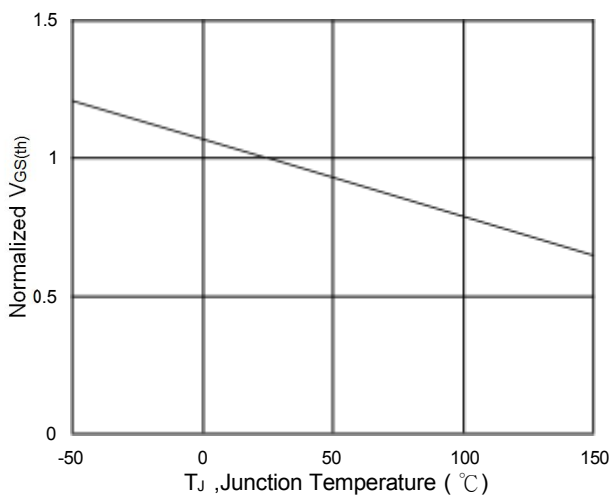


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

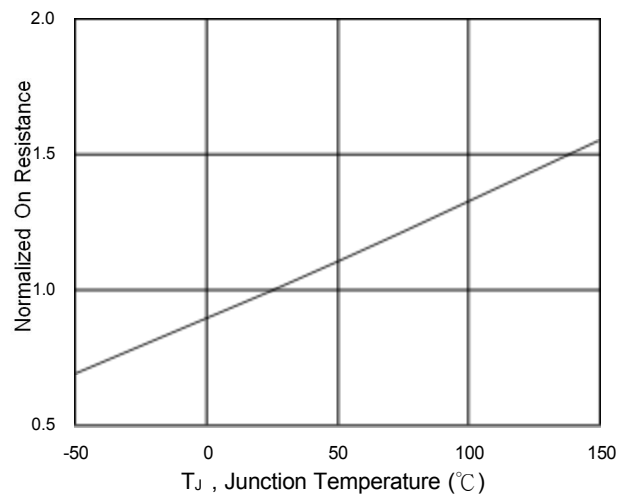


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

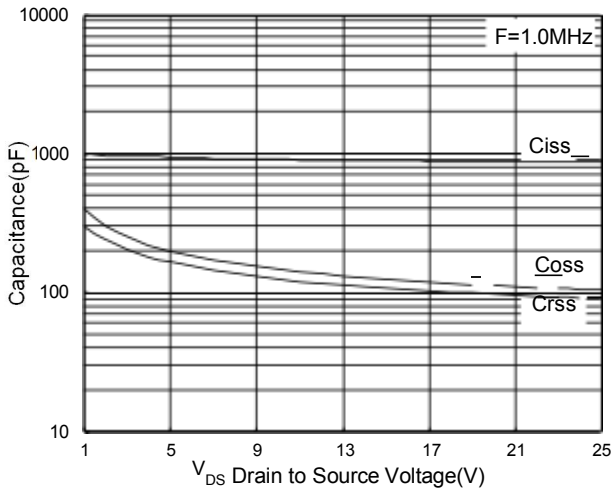


Fig.7 Capacitance

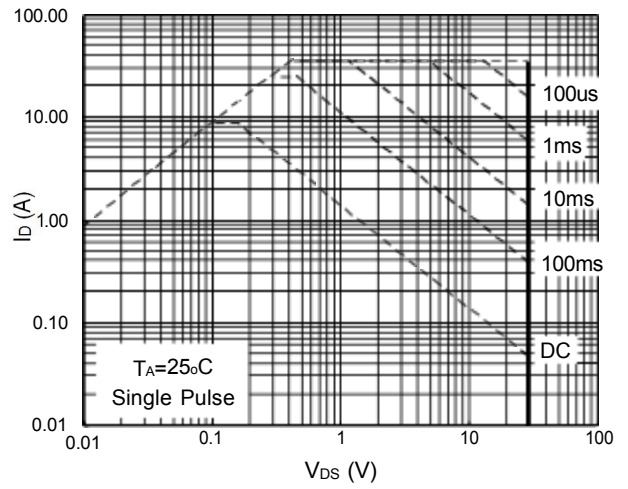


Fig.8 Safe Operating Area

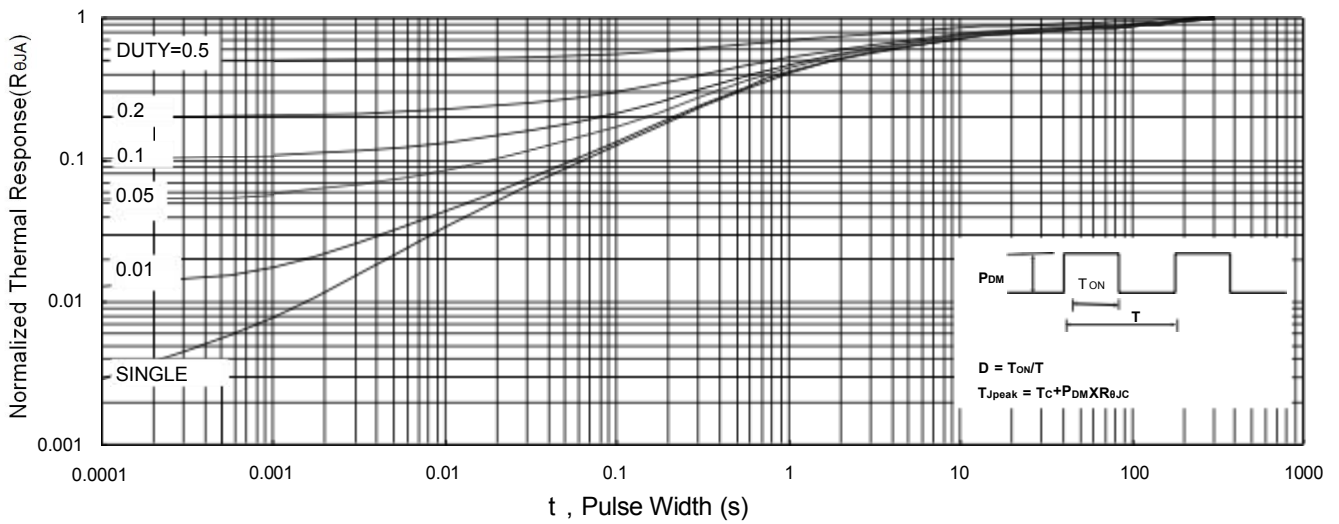
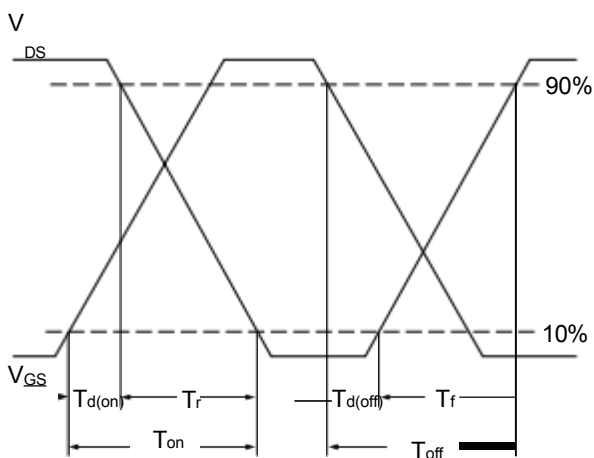
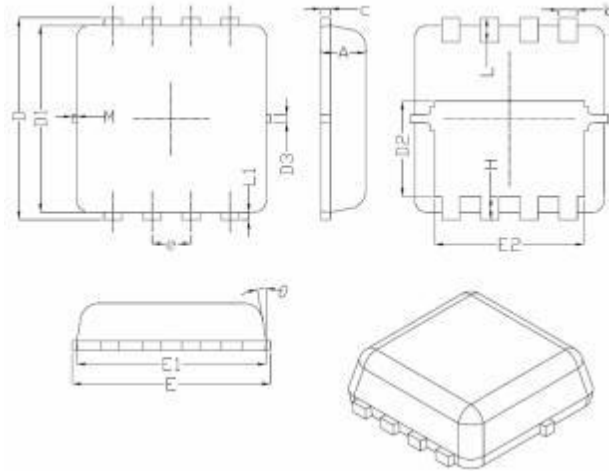


Fig.9 Normalized Maximum Transient Thermal Impedance



$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{V_{GS}}$$

DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12°

REEL SPECIFICATION

Product ID	Pack	Qty(PCS)
MSK7804	DFN3X3-8L	5000

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