# MSKSEMI 美森科
## SEMICONDUCTOR

ESD　　TVS　　TSS　　MOV　　GDT　　PLED

# PAM2804AAB010(MS)

**Product specification**

## GENERAL DESCRIPTION

The PAM2804AAB010(MS) is a constant frequency, current mode PWM step-down LED driver with 1A output current and 1.5MHz switch frequency. The device integrates a main switch and a synchronous rectifier for high efficiency without an external Schottky diode. This device offers two operation modes, PWM control and PFM Mode switching control, which allows a high efficiency over the wider range of the LED current. The feedback voltage is 0.1V typically. The device also integrates many protection features included input OVP, cycle-by-cycle current limit, over temperature protection.
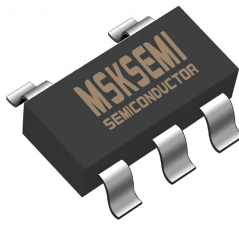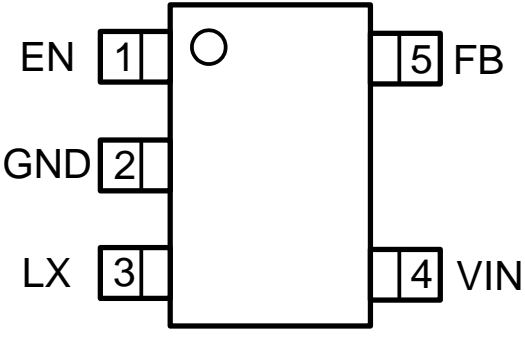
## FEATURES

- 1.5MHz Constant Frequency Operation
- 1.0A Output Current
- No Schottky Diode Required
- 2.5V to 5.5V Input Voltage Range
- Feedback Voltage as Low as 0.1V
- 100% Duty Cycle in Dropout
- Low Quiescent Current: 50μA
- Short Circuit Protection
- Thermal Fault Protection
- Input over voltage protection (OVP)
- <1μA Shutdown Current
- SOT-23-5 Package

## APPLICATIONS

- LED Drivers

## Pin Configuration and Functions

| PACKAGE OUTLINE | Pin Configuration | Circuit Diagram |
|---|---|---|
| MSKSEMI SEMICONDUCTOR<br><br>SOT-23-5 | EN 1    5 FB<br>GND 2<br>LX 3    4 VIN | 3AMC |

## TYPICAL APPILCATION
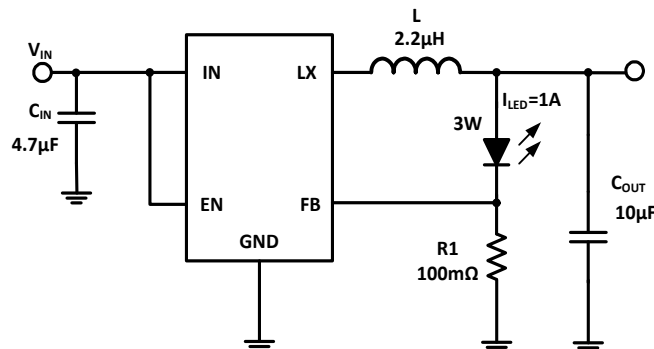


**Figure 1. Typical Application Circuits**

## PIN FUNCTIONS

| Pin | Name | Function |
|-----|------|----------|
| 1 | EN | Chip Enable Pin. Drive EN above 1.5V to turn on the part. Drive EN below 0.4V to turn it off. Do not leave EN floating. |
| 2 | GND | Ground Pin |
| 3 | LX | Power Switch Output. It is the switch node connection to Inductor. |
| 4 | VIN | Power Supply Input. Must be closely decoupled to GND with a 4.7µF or greater ceramic capacitor. |
| 5 | FB | Feedback Pin. A resistor connected between FB to GND is used to program LED current. |

## ESD RATING

| Items | Description | Value | Unit |
|-------|-------------|-------|------|
| $V_{ESD}$ | Human Body Model for all pins | ±2000 | V |

**JEDEC specification JS-001**

## RECOMMENDED OPERATING CONDITIONS

| Items | Description | Min | Max | Unit |
|-------|-------------|-----|-----|------|
| Voltage Range | IN | 2.5 | 5.5 | V |
| $T_J$ | Operating Junction Temperature Range | -40 | 125 | °C |

## THERMAL RESISITANCE (Note 3)

| Items | Description | Value | Unit |
|-------|-------------|-------|------|
| $\theta_{JA}$ | Junction-to-ambient thermal resistance | 200 | °C/W |

## ABSOLUTE MAXIMUM RATINGS (Note 1)

| Parameter | Value | Unit |
|-----------|-------|------|
| Input Supply Voltages | -0.3~6.5 | V |
| LX Voltages | -0.3~6.5 | V |
| EN, FB Voltage | -0.3~6.5 | V |
| Storage Temperature Range | -65~150 | °C |
| Junction Temperature (Note 2) | -40~150 | °C |
| Power Dissipation | 600 | mW |
| Lead Temperature Soldering, 10sec | 260 | °C |

## ELECTRICAL CHARACTERISTICS

($V_{IN}$=$V_{EN}$=5V, $T_A$ = 25°C, unless otherwise noted.)

| Parameter | Test Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|
| Input Voltage Range | | 2.5 | | 5.5 | V |
| OVP Threshold | | | 6.0 | | V |
| UVLO Threshold | | | 2.1 | | V |
| Quiescent Current | $V_{EN}$=2.0V, $I_{OUT}$=0，$V_{FB}$=$V_{REF}$*105% | | 50 | 65 | µA |
| Shutdown Current | $V_{EN}$ =0V | | 0.1 | 10 | µA |
| Regulated Feedback Voltage | $T_A$ = 25°C | | 100 | | mV |
| Oscillation Frequency | $V_{FB}$=100mV | | 1.5 | | MHz |
| | $V_{FB}$=0V | | 400 | | kHz |
| On Resistance of PMOS | $I_{LX}$=100mA | | 0.29 | | Ω |
| On Resistance of NMOS | $I_{LX}$=-100mA | | 0.18 | | Ω |
| Peak Current Limit | | 1.5 | | | A |
| EN Input Low Level Voltage | | | | 0.4 | V |
| EN Input High Level Voltage | | 1.5 | | | V |
| EN Leakage Current | | | ±0.01 | ±1.0 | µA |
| LX Leakage Current | $V_{EN}$=0V, $V_{IN}$=$V_{LX}$=5V | | ±0.01 | ±1.0 | µA |
| Thermal Shutdown Threshold (Note 4) | | | 155 | | °C |
| Thermal Shutdown Hysteresis (Note 4) | | | 20 | | °C |

**Note 1:** Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

**Note 2:** $T_J$ is calculated from the ambient temperature $T_A$ and power dissipation $P_D$ according to the following formula: $T_J$ = $T_A$ + ($P_D$) x $\theta_{JA}$.

**Note 3**: Measured on JESD51-7, 4-layer PCB.

**Note 4:** Thermal shutdown threshold and hysteresis are guaranteed by design.

## FUNCTION DESCRIPTION

The PAM2804AAB010(MS) is a high performance 1.0A, 1.5MHz monolithic step-down LED driver. The PAM2804AAB010(MS) requires only three external power components ($C_{in}$, $C_{out}$ and L). The LED driver current can be programmed with external feedback resistance between FB and GND pin up to 1.0A.

At dropout, the converter duty cycle increases to 100% and the output voltage tracks the input voltage minus the $R_{DS(ON)}$ drop of the high-side MOSFET.

The internal error amplifier and compensation provides excellent load, and line regulation. he device also integrates many protection features such as input OVP, cycle-by-cycle current limit, over temperature protection.
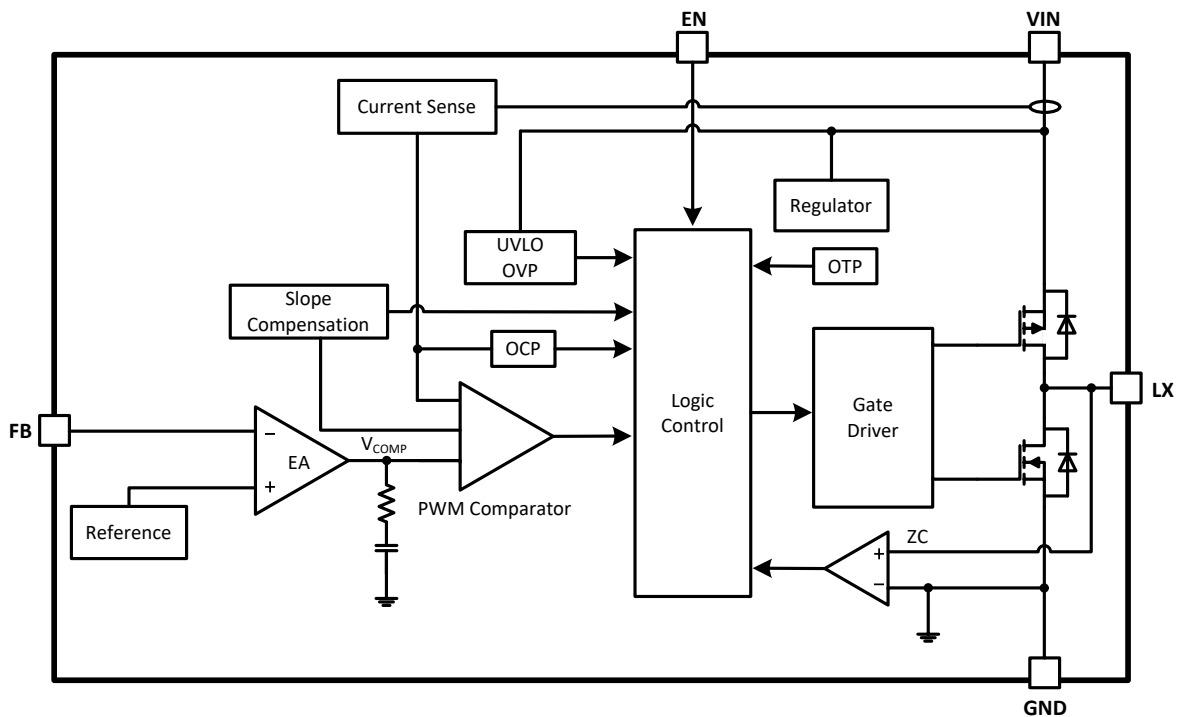
## FUNCTIONAL BLOCK DIAGRAM



**Figure 2.** PAM2804AAB010(MS) **Block Diagram**

## APPLICATION INFORMATION

### Setting the Output Voltage

Figure 1 shows the basic application circuit for the PAM2804AAB010(MS). LED drive current of the PAM2804AAB010(MS) can be externally programmed by resistor R1. The external resistor sets the LED current according to the following equation:

$$I_{LED} = \frac{V_{FB}}{R1}$$

### Inductor Selection

For most designs, 2.2µH inductance can satisfy most application conditions. Inductance value is related to inductor ripple current value, input voltage, output voltage and switching frequency. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where $\Delta I_L$ is inductor ripple current. Large value inductors result in lower ripple current and small value inductors result in high ripple current, so inductor value has effect on output voltage ripple value. DC resistance of inductor which has impact on efficiency of DC/DC converter should be taken into account when selecting the inductor.

### Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency should be less than input source impedance to prevent high frequency switching current passing to the input.

A low ESR input capacitor sized for maximum RMS current must be used. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients.

A 4.7µF ceramic capacitor for most applications is sufficient. A large value may be used for improved input voltage filtering.

### Output Capacitor Selection

The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current ratings. The output ripple $V_{OUT}$ is determined by:

$$\Delta V_{OUT} = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times L \times f_{OSC}} \times (ESR + \frac{1}{8 \times f_{osc} \times C_{OUT}})$$

A 10µF ceramic can satisfy most applications.

### Layout Consideration

When laying out the printed circuit board, the Following checking should be used to ensure proper operation of the PAM2804AAB010(MS). Check the following in your layout:

1. The power traces, consisting of the GND trace, the LX trace and the VIN trace should be kept short, direct and wide.

2. Does the (+) plates of Cin connect to Vin as closely as possible. This capacitor provides the AC current to the internal power MOSFETs.

3. Keep the switching node, LX, away from the sensitive VOUT node.

4. Keep the (-) plates of Cin and Cout as close as possible

## MSKSEMI
### SEMICONDUCTOR

**SOT-23-5 Package Outline Dimensions**
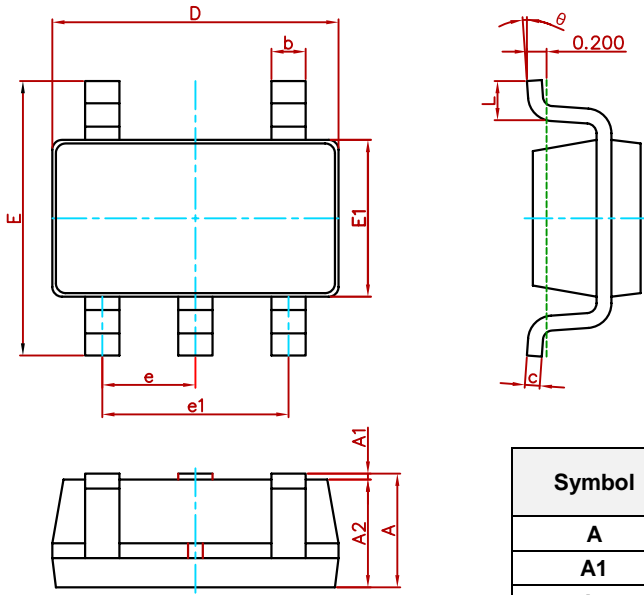


0.200

| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|:---:|:---:|:---:|:---:|:---:|
| | **Min.** | **Max.** | **Min.** | **Max.** |
| **A** | 1.050 | 1.250 | 0.041 | 0.049 |
| **A1** | 0.000 | 0.100 | 0.000 | 0.004 |
| **A2** | 1.050 | 1.150 | 0.041 | 0.045 |
| **b** | 0.300 | 0.500 | 0.012 | 0.020 |
| **c** | 0.100 | 0.200 | 0.004 | 0.008 |
| **D** | 2.820 | 3.020 | 0.111 | 0.119 |
| **E** | 2.650 | 2.950 | 0.104 | 0.116 |
| **E1** | 1.500 | 1.700 | 0.059 | 0.067 |
| **e** | 0.950(BSC) | | 0.037(BSC) | |
| **e1** | 1.800 | 2.000 | 0.071 | 0.079 |
| **L** | 0.300 | 0.600 | 0.012 | 0.024 |
| **θ** | 0° | 8° | 0° | 8° |

**SOT-23-5 Suggested Pad Layout**
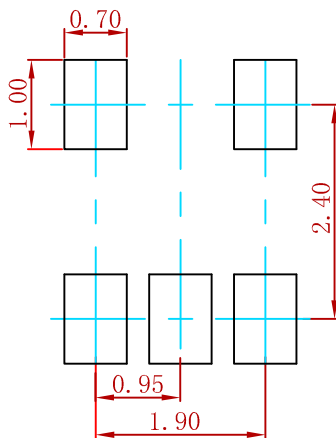


Note:
1.Controlling dimension:in millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.

### REEL SPECIFICATION

| P/N | PKG | QTY |
|:---:|:---:|:---:|
| PAM2804AAB010(MS) | SOT-23-5 | 3000 |

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