



# Product data sheet

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**SOT-23** 

#### Features

- **300** Watts peak pulse power (tp =  $8/20\mu s$ )
- Bidirectional configurations
- Solid-state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- IEC 61000-4-2 ±30kV contact ±30kV air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 8A (8/20µs)

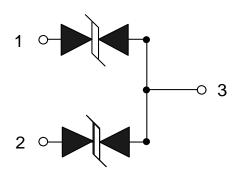
## Applications

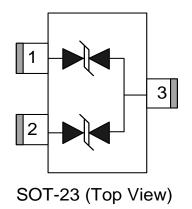
- Dataline
- Automatic Teller Machines
- Net works
- Power line

## **Mechanical Data**

- SOT-23 package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- RoHS/WEEE Compliant

## Schematic & PIN Configuration









# Absolute Maximum Rating

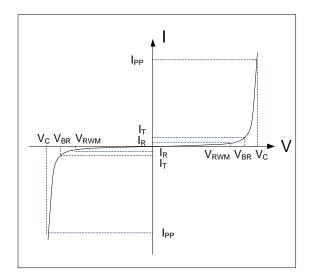
Rating	Symbol	Value	Units
Peak Pulse Power ( $t_p = 8/20 \mu s$ )	P <sub>PP</sub>	300	Watts
Peak Pulse Current ( $t_p = 8/20 \mu s$ ) (note1)	I <sub>pp</sub>	8	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>	30 30	kV
Lead Soldering Temperature	$T_L$	260(10seconds)	°C
Junction Temperature	TJ	-55 to + 125	°C
Storage Temperature	T <sub>stg</sub>	-55 to + 125	°C

## **Electrical Characteristics**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>				24.0	v
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>T</sub> =1mA	25.5			v
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =24V,T=25°C		0.1	0.5	μΑ
Peak Pulse Current	$I_{PP}$	$t_p = 8/20 \mu s$			8	А
Clamping Voltage	V <sub>C</sub>	Ipp=8A,tp=8/20µs		35		V
Junction Capacitance	C <sub>j</sub>	$V_R = 0V, f = 1MHz$		16		pF

# Electrical Parameters (TA = 25 °C unless otherwise noted)

Symbol	Parameter
Ipp	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
VRWM	Working Peak Reverse Voltage
IR	Maximum Reverse Leakage Current @ Vrwм
VBR	Breakdown Voltage @ IT
IT	Test Current



Note:. 8/20µs pulse waveform.





Ambient Temperature -  $T_A$  (°C)

## **Typical Characteristics**

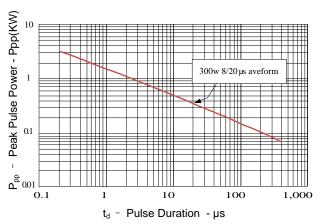
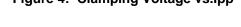
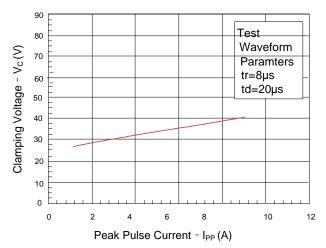


Figure 1: Peak Pulse Power vs. Pulse Time

Figure3: Pulse Waveform







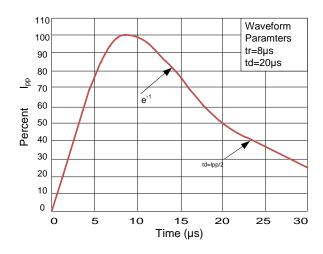


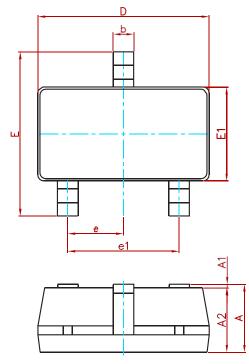
Figure 2: Power Derating Curve

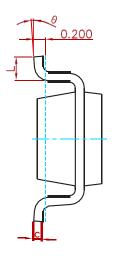
Percent of Rated Power for Ipp



#### ESD1CAN-MS Roms Semiconductor Compiance

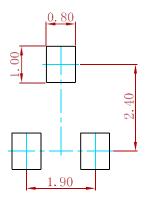
#### PACKAGE MECHANICAL DATA





Symbol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
е	0.950(	BSC)	0.037	(BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
0	0°	8°	0°	8°

## Suggested Pad Layout



Note:

Controlling dimension:in millimeters.
General tolerance:± 0.05mm.
The pad layout is for reference purposes only.

## **REEL SPECIFICATION**

P/N	PKG	QTY
ESD1CAN-MS	SOT-23	3000





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