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SEMICONDUCTOR



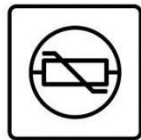
ESD



TVS



TSS



MOV



GDT



PLED

RCLAMP7534P-MS

Product specification

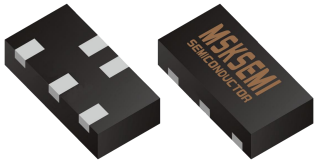
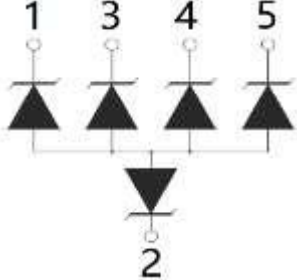

Features

- 150Watts peak pulse power (tp = 8/20μs)
- Transient protection for high speed data lines to IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns)
- Working voltages : 5V
- Protects five/I/O lines
- Low operating and clamping voltages
- Solid-state silicon avalanche technology

Applications

- Notebooks, Desktops, Servers and Video Graphics Cards
- USB Power & Data Line Protection
- Monitors and Flat Panel Displays
- I²C Bus Protection
- Portable Instrumentation
- Set Top Box

Reference News

Outline	Circuit Diagram	Marking
 <p data-bbox="264 1272 419 1303">DFN2010-5L</p>		

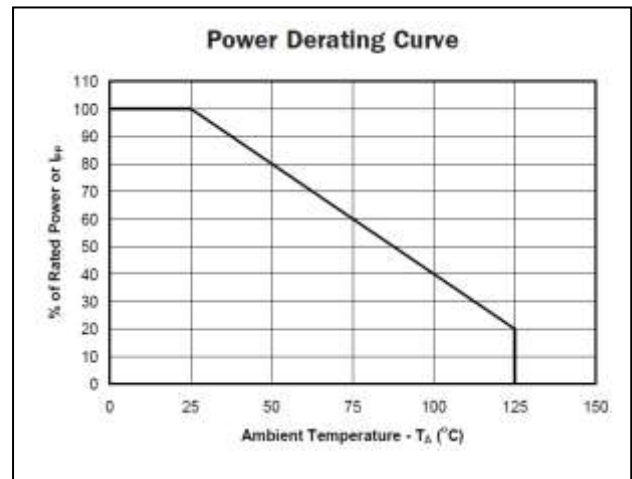
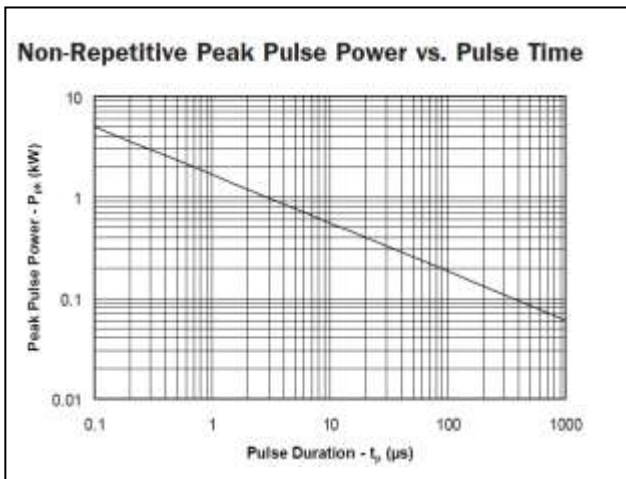
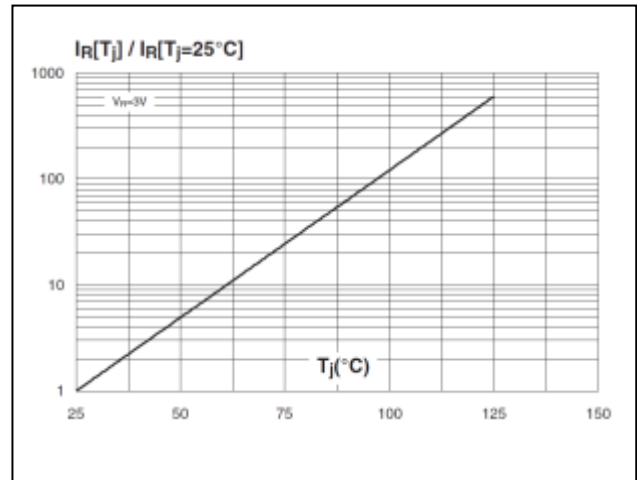
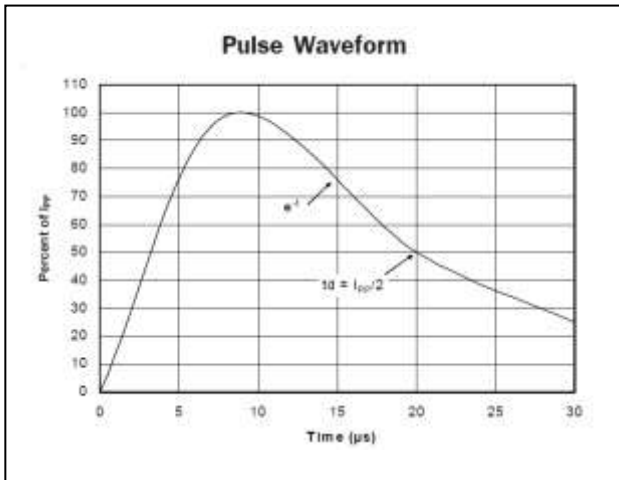
Maximum Rating @ Ta=25°C unless otherwise specified

Symbol	Parameter	Ratings	Units
P _{PK}	Peak Pulse Power (tp = 8/20μs)	150	Watts
T _L	Lead Soldering Temperature	260(10sec.)	°C
T _J	Operating Temperature	-55 to +125	°C
T _{STG}	Storage Temperature	-55 to +150	°C

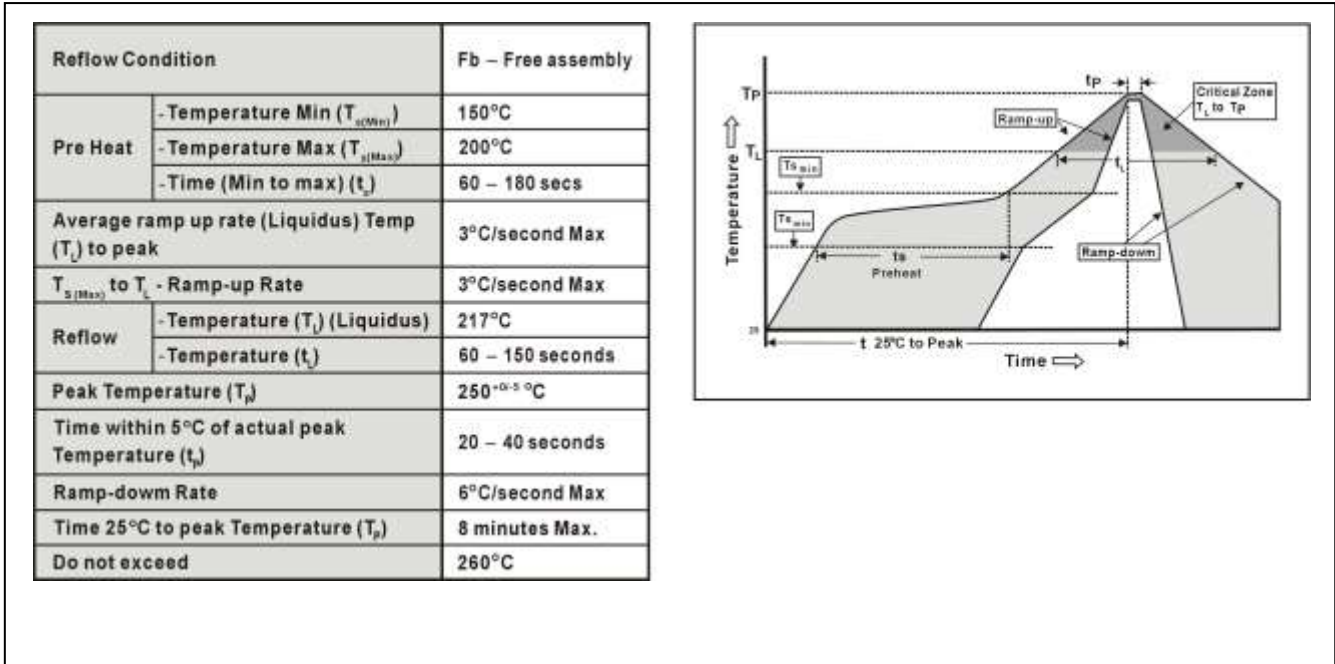
Electrical Characteristics @ Ta=25°C unless otherwise

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
V _{RWM}	Reverse Working Voltage	Any I/O to Ground			5.0	V
V _{BR}	Reverse Breakdown Voltage	I _r = 1mA, Any I/O to Ground	6.0			V
I _R	Reverse Leakage Current	V _{RWM} = 5V, Any I/O to Ground			1	μA
V _C	Clamping Voltage	I _{PP} = 1A, t _p = 8/20μs, any I/O pin to Ground			9.8	V
		I _{PP} = 3.5A, t _p = 8/20μs, any I/O pin to Ground			15	V
C _J	Junction Capacitance	V _R = 0V, f = 1MHz, between I/O pins		0.3	0.5	pF
		V _R = 0V, f = 1MHz, any I/O pin to Ground		0.5	0.8	pF

Typical Characteristics @ $T_a=25^\circ\text{C}$ unless otherwise specified



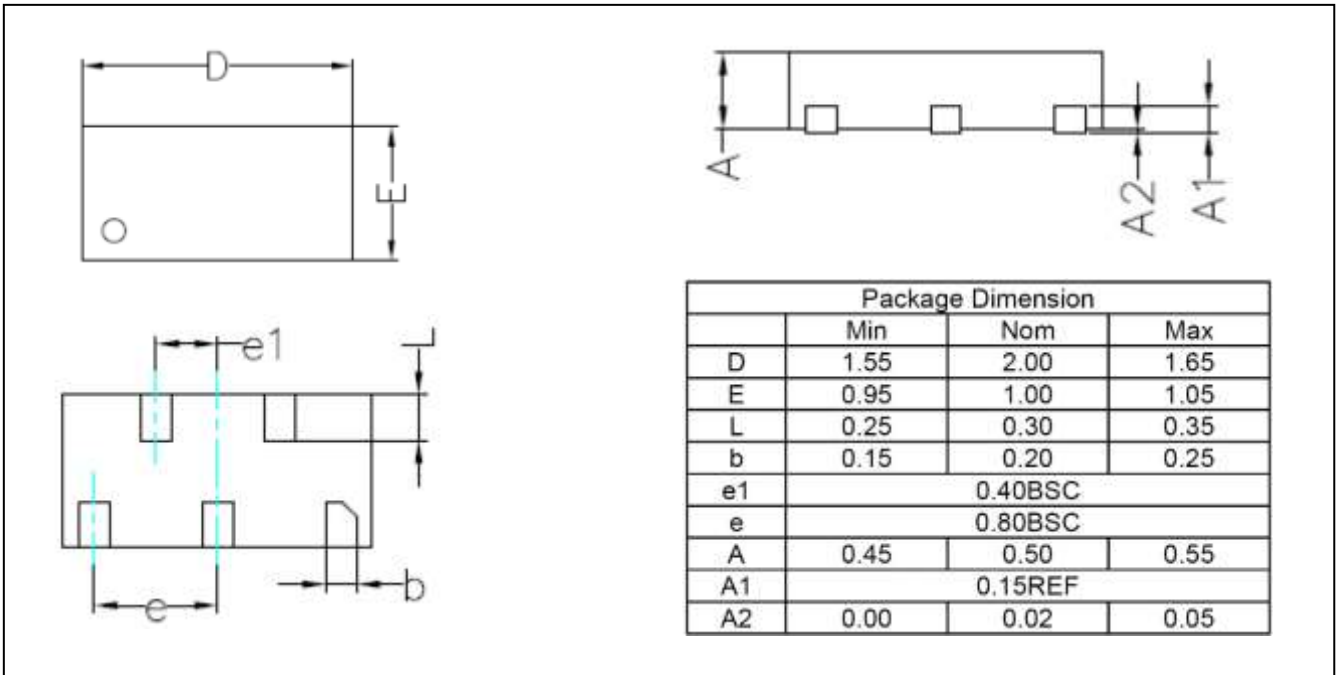
Soldering Parameters



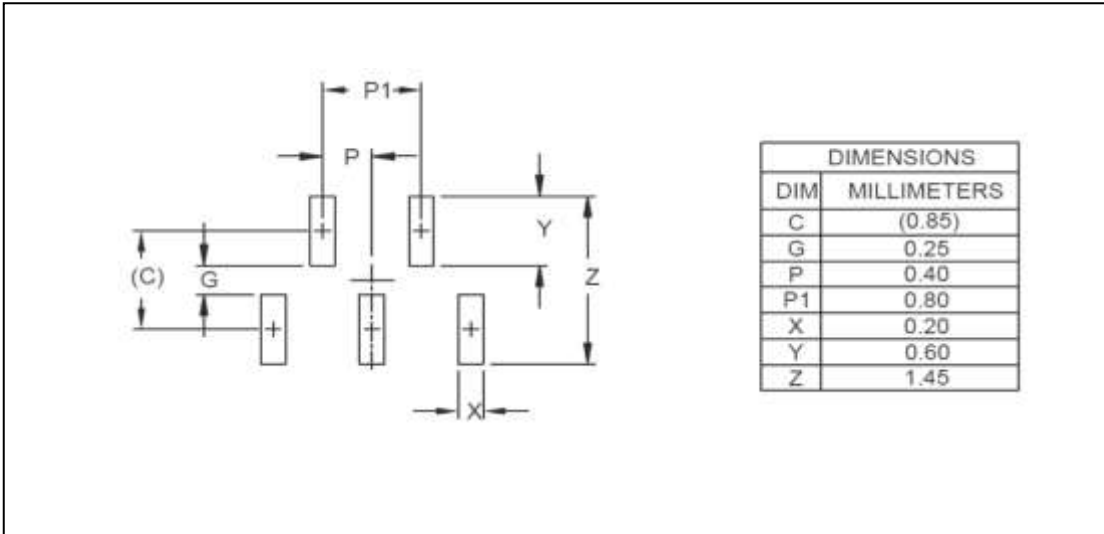
Package Outline

Plastic surface mounted package

DFN2010-5L



Soldering Footprint



REEL SPECIFICATION

P/N	PKG	QTY
RCLAMP7534P-MS	DFN2010-5L	3000

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