

Product data sheet

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Feature

Ultra Small mold type. (SOD-323) Low I_R High reliability.

Applications

Low current rectification

Construction

Silicon epitaxial planar

Mechanical Characteristics

Lead finish:100% matte Sn(Tin) Mounting position: Any Qualified max reflow temperature:260 ℃ Pure tin plating: 7 ~ 17 um Pin flatness:≤3mil







VF	-	0.5	0.55	V	I _F =1A
VF	-	0.43	0.48	V	I⊧=0.5A
I _R	-	-	0.1	mA	V _R =40V
Cj	-	120	-	pF	V _R =0V f =1MHz
	VF IR Cj	VF -	VF - 0.43	VF - 0.43 0.48 IR - 0.1	V _F _ 0.43 0.48 V I _R 0.1 mA

Absolute maximum rating@25℃

Parameter	Symbol	limits	Unit
Reverse voltage(repetitive peak)	V _{RM}	45	V
Reverse voltage (DC)	V _R	40	V
Average rectified forward current	lo	1	A

Electrical characteristics per line@25℃



Parameter	Symbol	limits	Unit
Peak forward current(Pulse Width=1us, Single Pulse)	Ірк	25	A
Peak forward surge current(8.33ms 1/2sine waveform with single pulse)	IFSM	8	А
Power Dissipation	PD	500	mW
Operating Junction temperature Range	Tj	-55 to 125	°C
Storage temperature	T _{stg}	-55 to 125	°C

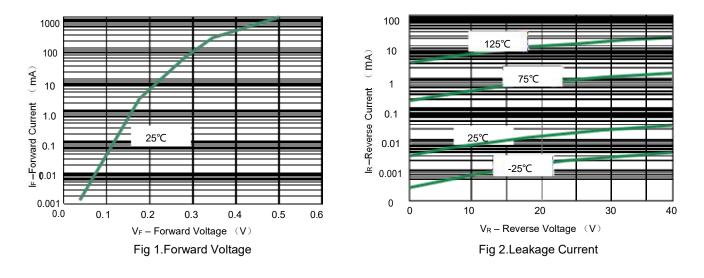
S7116-300A

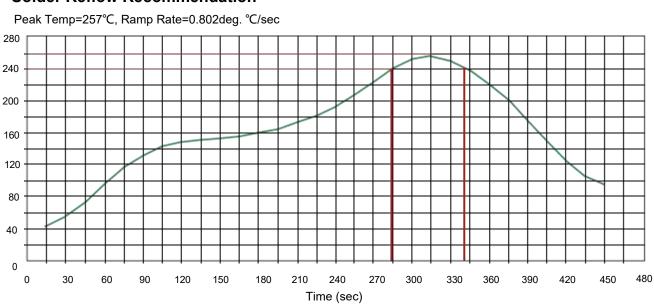
Semiconductor

HF

Compiance

Typical Characteristics





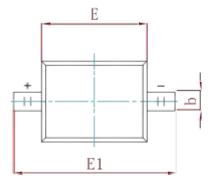
Solder Reflow Recommendation

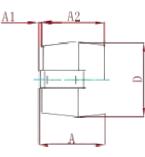
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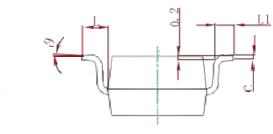




PACKAGE MECHANICAL DATA

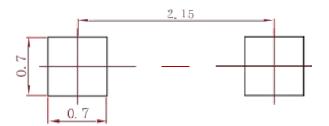






Complete L	Dimensions	In Millimeters	Dimensions In Inche	
Symbol	Min.	Max.	Min.	Max.
A		1.000		0.039
A1	0.000	0.100	0.000	0.004
A2	0.800	0.900	0.031	0.035
b	0.250	0.350	0.010	0.014
c	0.080	0.150	0.003	0.006
D	1.200	1.400	0.047	0.055
E	1.600	1.800	0.063	0.071
E1	2.550	2.750	0.100	0.108
L	0.475	REF.	0.019	REF.
L1	0.250	0.400	0.010	0.016
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:

Controlling dimension:in millimeters.
General tolerance:±0.05mm.
The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
S7116-300A	SOD-323	3000





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