## MSKSEMI















**ESD** 

TVS

TSS

MOV

GDT

**PLED** 

# Broduct data sheet



#### **Features**

- 350Watts peak pulse power (tp =  $8/20\mu$ s)
- Low clamping voltage
- Low leakage current
- Protection one power line
- Low Capacitance: 1.0 pF Typical
- IEC 61000-4-2 ±20kV contact ±20kV air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 20A (8/20µs)



SOD-323

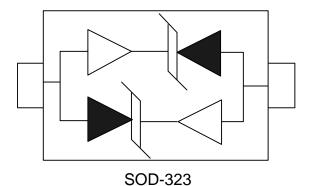
## **Applications**

- Ethernet 10/100/1000 Base T
- Cellular Phones
- Handheld Wireless Systems
- Personal Digital Assistant (PDA)
- **USB** Interface

#### **Mechanical Data**

- SOD-323 package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- RoHS/WEEE Compliant

## Schematic & PIN Configuration





**Absolute Maximum Rating** 

Rating	Symbol	Value	Units
Peak Pulse Power ( t <sub>p</sub> =8/20μs )	P <sub>PP</sub>	350	Watts
Peak Pulse Current ( t <sub>p</sub> =8/20μs ) (note1)	I <sub>pp</sub>	20	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>	20 20	kV
Lead Soldering Temperature	T∟	260(10seconds)	$^{\circ}$ C
Junction Temperature	TJ	-55 to + 150	$^{\circ}$ C
Storage Temperature	T <sub>stg</sub>	-55 to + 150	$^{\circ}$ C

#### **Electrical Characteristics**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{RWM}$				3.3	٧
Reverse Breakdown Voltage	$V_{BR}$	I <sub>T</sub> =1mA	4.0			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =3.0V,T=25℃			1	uA
Clamping Voltage	V <sub>C</sub>	I <sub>PP</sub> =20A,t <sub>p</sub> =8/20μs			18	V
Junction Capacitance	C <sub>j</sub>	$V_R = 0V$ , $f = 1MHz$		1.0	1.5	pF

## **Electrical Parameters (TA = 25°C unless otherwise noted)**

Symbol	Parameter
<b>I</b> PP	Maximum Reverse Peak Pulse Current
Vc	Clamping Voltage @ IPP
VRWM	Working Peak Reverse Voltage
lr	Maximum Reverse Leakage Current @ VRWM
V <sub>BR</sub>	Breakdown Voltage @ I <sub>T</sub>
lτ	Test Current

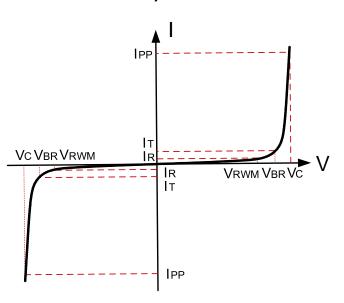




Figure 1: Peak Pulse Power vs. Pulse Time

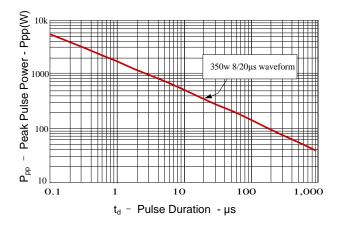


Figure 2: Power Derating Curve

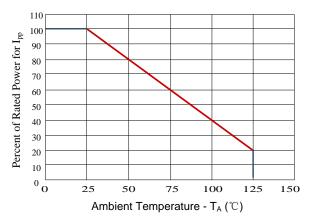


Figure3: Pulse Waveform

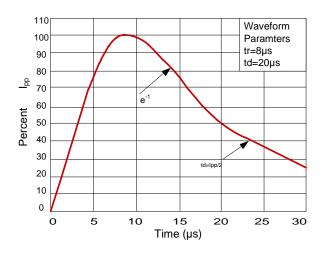
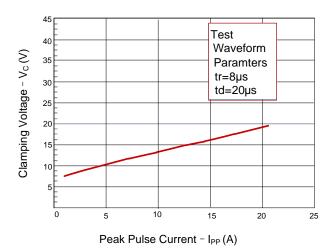


Figure 4: Clamping Voltage vs.lpp

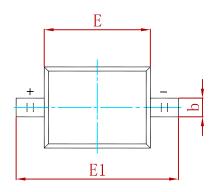


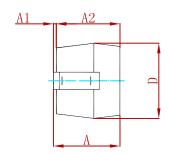


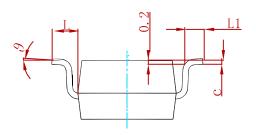
Semiconductor



## PACKAGE MECHANICAL DATA

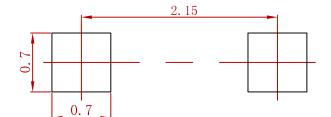






Cumbal	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
Α		1.000		0.039
A 1	0.000	0.100	0.000	0.004
A2	0.800	0.900	0.031	0.035
b	0.250	0.350	0.010	0.014
С	0.080	0.150	0.003	0.006
D	1.200	1.400	0.047	0.055
E	1.600	1.800	0.063	0.071
E1	2.550	2.750	0.100	0.108
L	0.475 REF.		0.019 REF.	
L1	0.250	0.400	0.010	0.016
θ	0°	8°	0°	8°

## **Suggested Pad Layout**



#### Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

#### **REEL SPECIFICATION**

P/N	PKG	QTY
SP4020-01FTG-C-MS	SOD-323	3000



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