Milestone Semiconductor Inc.

# Description

The MST56XXB series is a high input voltage (60V), low quiescent current, High PSRR linear regulator (LDO) able to provide 150mA load current.

The LDO features very fast response against line voltage transient and load current transient, and ensures no overshoot voltage during the LDO start up and short circuit recovery.

The device features integrated short-circuit and thermal shutdown protection.

The device is available with fixed output voltages of 3.0V, 3.3V, 3.6V and 5.0V, and available in SOT23 and SOT89 packages.

## Application

- Battery-powered equipment
- Smoke detector and sensor
- Micro controller Applications
- Home Appliance

#### Features

- Low Quiescent Current: 2uA
- High Input Voltage Rating: Up to 80V
- Output Current: 150mA
- High PSRR: 70dB at 1Khz
- Dropout Voltage: 70mV@10mA

C

700mV@100mA

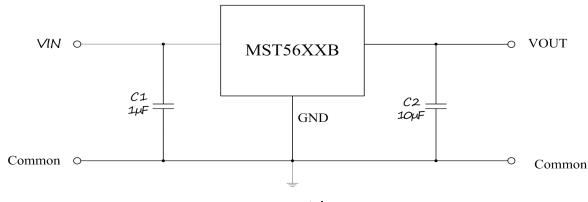
Fixed Output Voltages:

3.0V, 3.3V, 3.6V and 5.0V

- High-accuracy Output Voltage: ±2%
- Good Transient Response
- Integrated Short-Circuit Protection
- Integrated Thermal Protection
- Available Packages:

MST56XXBTE	
MST56XXBTE-A	SOT23-3
MST56XXBTE-B	
MST56XXBTE-C	
MST56XXBTS	
MST56XXBTS-A	SOT89-3
MST56XXBTS-B	
MST56XXBTS-C	
MST56XXBTG	SOT23-5

#### Application Circuits

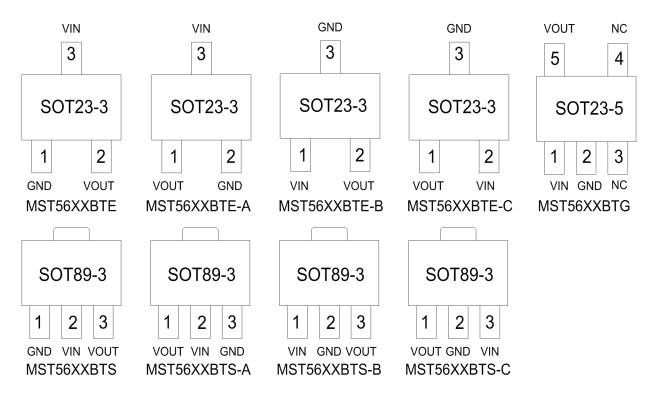


MST56XXB

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60V/150mA/2uA I<sub>Q</sub>/Fast Transient High PSRR Linear Voltage Regulator

## Packages And Pin Assignment

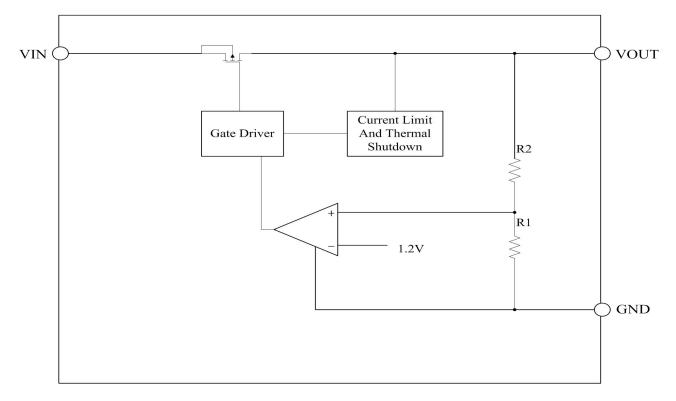


# Pin Descriptions

SOT23-3			PIN	DECOUPTION		
MST56XXBTE	MST56XXBTE-A	MST56XXBTE-B	MST56XXBTE-C	NAME	DESCRIPTION	
1	2	3	3	GND	Ground Pin	
2	1	2	1	VOUT	Output Pin	
3	3	1	2	VIN	Input Pin	
SOT89-3			PIN	DECODERION		
MST56XXBTS	MST56XXBTS-A	MST56XXBTS-B	MST56XXBTS-C	NAME	DESCRIPTION	
1	3	2	2	GND	Ground Pin	
3	1	3	1	VOUT	Output Pin	
2	2	1	3	VIN	Input Pin	
SOT23-5				PIN NAME	DESCRIPTION	
MST56XXBTG						
1				VIN	Input Pin	
2				GND	Ground Pin	
3			NC	No Connection		
4			NC	No Connection		
5			VOUT	Output Pin		



# Functional Block Diagram



#### Absolute Maximum Ratings

Item	Description	Min	Max	Unit
	VIN to GND	-0.3 80		V
Voltage	VOUT to GND	-0.3	6	V
	VOUT to VIN	-35 0.3		V
Current	Peak output current	Internally limited		
Temperature	Operating Ambient Temperature	-40	85	°C
	Storage Temperature	-40	150	°C
•	Operating virtual junction Temperature	- 150		°C
Thermal Resistance	SOT89	180		°C/W
(Junction to Ambient)	SOT23	380		°C/W
Downer Dissingtion	SOT89	600		mW
Power Dissipation	SOT23	300		mW
Electrostatic	Human Body Model (HBM)	6		kV
discharge rating	Charged Device Model (MM)	100		V

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60V/150mA/2uA I<sub>Q</sub>/Fast Transient High PSRR Linear Voltage Regulator

## Electrical Characteristics

#### $(At T_{A=}25^{\circ}C, C_{IN}=1uF, V_{IN}=V_{OUTNOM}+1.0V, C_{OUT}=10uF, unless otherwise noted)$

Symbol	Parameter	Test Conditions	MIN	ТҮР	MAX	UNIT
VIN	Input Voltage		5	—	60	V
Ignd	Quiescent Current	Vin=12V, No load		2		uA
VOUT	Output Voltage	VIN=12V, IOUT=10mA	Voutnom * <b>0.98</b>	Voutnom	V <sub>оитном</sub> * 1.02	V
Iout_max	Output Current			150		mA
Vdrop	Dropout Voltage(1)	IOUT=10mA , Vin=Voutnom-0.1V		70		mV
		IOUT=100mA , Vin=Voutnom-0.1V		700	_	mV
ΔVουτ(διουτ)	Load Regulation	VIN=12V, 1mA≤Iouт≤100mA	—	0.02		%/mA
ΔVουτ(Δνιν)	Line Regulation	IOUT=1mA, Voutnom+0.5V≤Vin≤60V	_	0.01	—	%/V
Ilimit	Current Limit			250	_	mA
Tshdn	Thermal Shutdown Temperature	Shutdown, temperature increasing		150		°C
		Reset, temperature decreasing		140		C
PSRR		Vin=12V , Iout=10mA F=1Khz,Vout=3.3V		70		dB

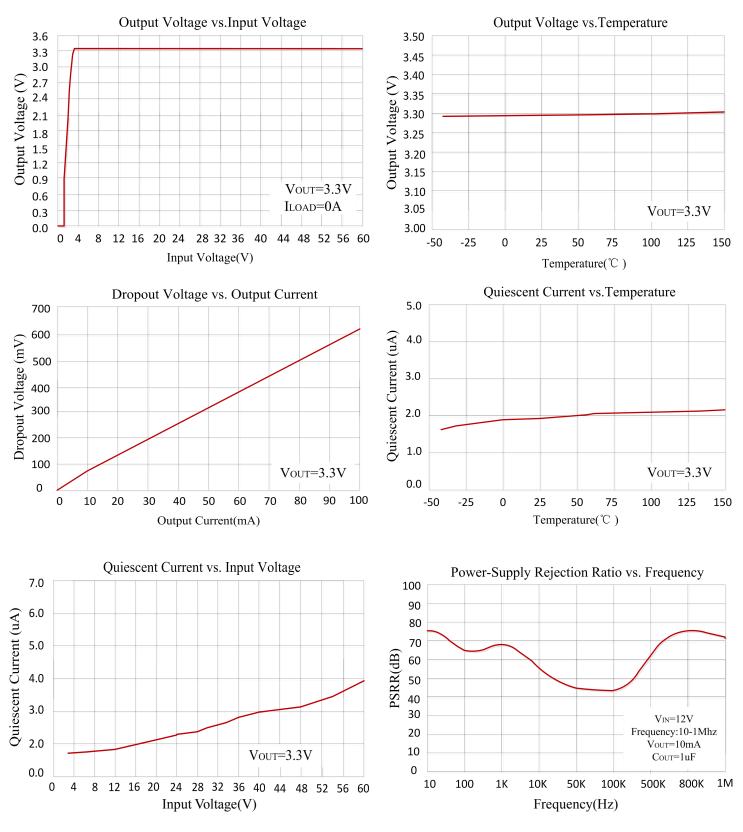
Note: (1) Dropout Voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value.

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## Typical Performance Characteristics

Test Condition: TA=25°C, Vin=12V, IOUT=1mA, COUT=10uF, unless otherwise noted

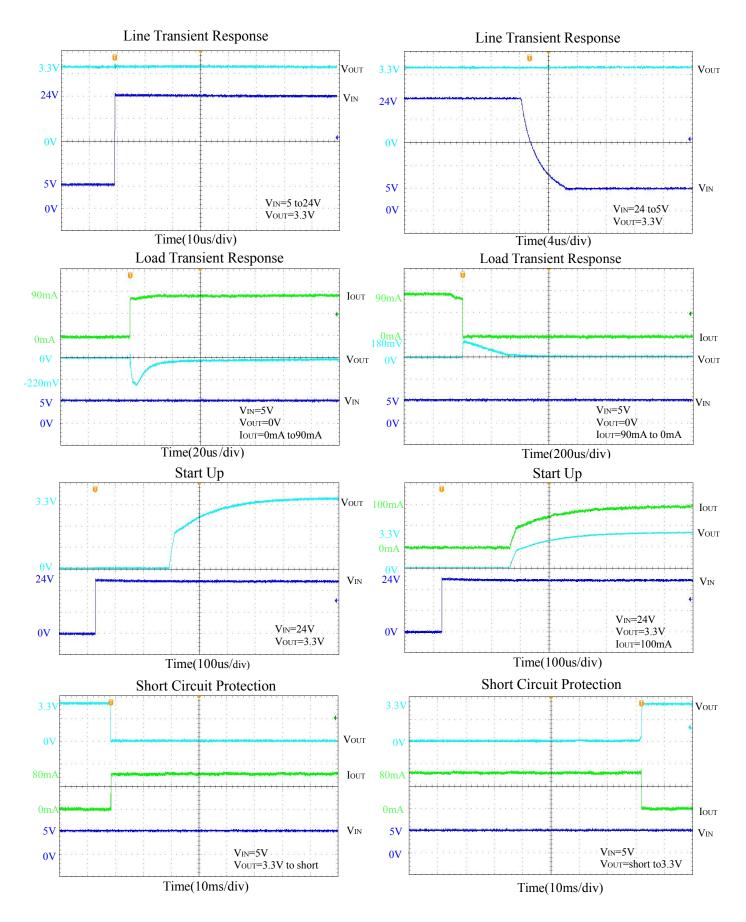


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## **MST56XXB**

# Milestone Semiconductor Inc.

60V/150mA/2uA I<sub>Q</sub>/Fast Transient High PSRR Linear Voltage Regulator



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#### Functional Description

#### **Input Capacitor**

A  $1\mu$ F ceramic capacitor is recommended to connect between VIN and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both VIN and GND.

#### **Output Capacitor**

An output capacitor is required for the stability of the LDO. The recommended minimum output capacitance is  $1\mu$ F, ceramic capacitor is recommended, and temperature characteristics are X7R or X5R. Higher capacitance values help to improve load/line transient response. The output capacitance may be increased to keep low undershoot/overshoot. Place output capacitor as close as possible to VOUT and GND pins.

#### **Current Limit and Short Circuit Protection**

When output current at VOUT pin is higher than current limit threshold or the VOUT pin is direct short to GND, the current limit protection will be triggered and clamp the output current at a pre-designed level to prevent over-current and thermal damage.

#### **Thermal Protection**

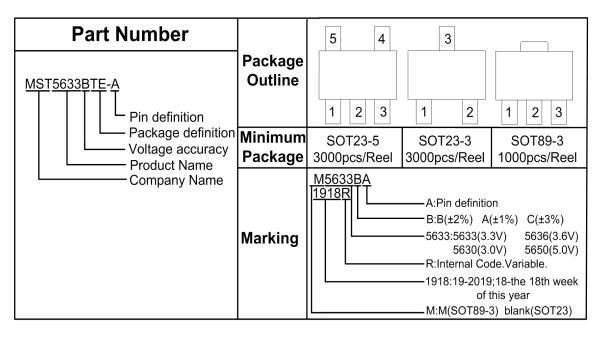
The MST56XXB has internal thermal sense and protection circuits. When excessive power dissipation happens on the device, such as short circuit at the output pin or very heavy load current with a large voltage drop across the device, the internal thermal protection circuit will be triggered, and it will shut down the power MOSFET to prevent the LDO from damage. As soon as excessive thermal condition is removed and the temperature of the device drops down, the thermal protection circuit will lease the control of the power MOSFET, and the LDO device goes to normal operation.

# MST56XXB



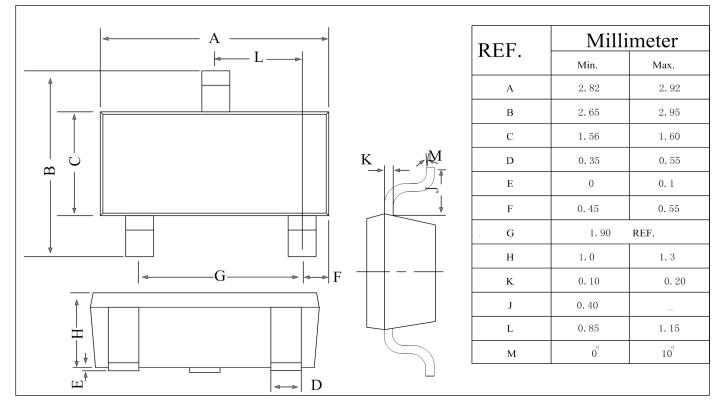
60V/150mA/2uA I<sub>Q</sub>/Fast Transient High PSRR Linear Voltage Regulator

# Ordering And Marking Information



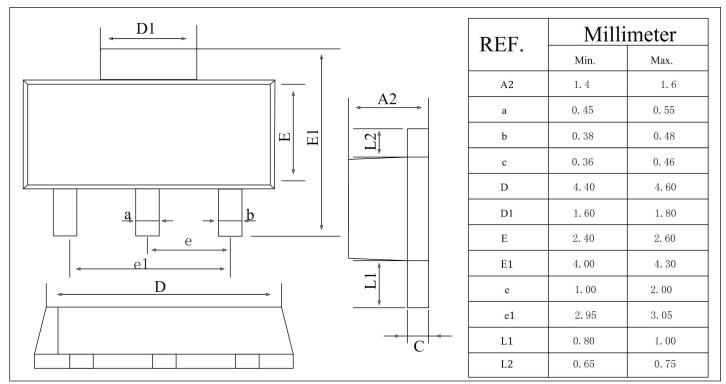
# Package Outline Dimensions

#### SOT23-3

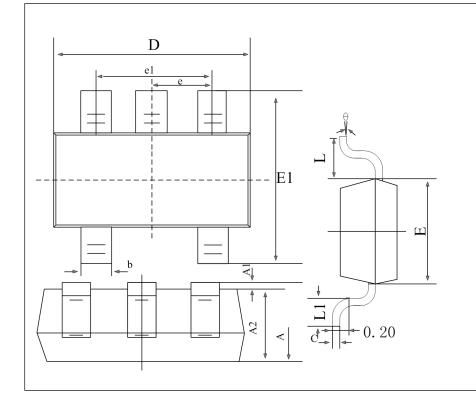




SOT89-3



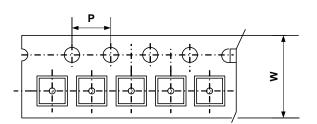
SOT23-5

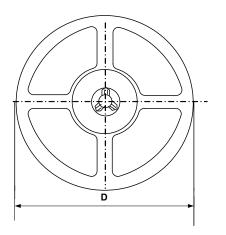


REF.	Millimeter			
<b>III</b> .	Min.	Max.		
А	1.05	1.25		
A1	0	0.1		
A2	1.05	1.15		
b	0.3	0.5		
с	0.1	0.2		
D	2.85	3.05		
Е	1.5	1.7		
E1	2.65 2.95			
e	0.95(BSC)			
e1	1.8	2.0		
L	0.3	0.6		
θ	00	8°		



# Packing Information





Туре	W(mm)	P(mm)	D(mm)	Qty (pcs)
SOT23-3	8.0±0.1 mm	4.0±0.1 mm	180±1 mm	3000pcs
SOT23-5	8.0±0.1 mm	4.0±0.1 mm	180±1 mm	3000pcs
SOT89-3	12.0±0.1 mm	4.0±0.1 mm	180±1 mm	1000pcs

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