

# Reference Specification

Type KX
Safety Standard Certified Lead Type Disc Ceramic Capacitors for General Purpose

Product specifications in this catalog are as of Mar. 2021, and are subject to change or obsolescence without notice.

Please consult the approval sheet before ordering. Please read rating and Cautions first.

# **⚠** CAUTION

#### 1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.

Voltage	DC Voltage	DC+AC Voltage AC Voltage		Pulse Voltage(2)	
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

#### 2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20 °C on the condition of atmosphere temperature 25 °C. When measuring, use a thermocouple of small thermal capacity-K of  $\phi$ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.(Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

#### 3. TEST CONDITION FOR WITHSTANDING VOLTAGE

#### (1) TEST EQUIPMENT

Test equipment for AC withstanding voltage should be used with the performance of the wave similar to 50/60 Hz sine wave.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

#### (2) VOLTAGE APPLIED METHOD

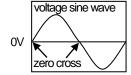
When the withstanding voltage is applied, capacitor's lead or terminal should be firmly connected to the out-put of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the \*zero cross. At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminal should be taken off the out-put of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

\*ZERO CROSS is the point where voltage sine wave pass 0V.

- See the right figure -



# 4. FAIL-SAFE

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

#### 5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

#### 6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 °C max. Soldering iron wattage: 50W max. Soldering time: 3.5s max.

#### 7. BONDING, RESIN MOLDING AND COATING

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive, molding resin or coating may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

#### 8. TREATMENT AFTER BONDING, RESIN MOLDING AND COATING

When the outer coating is hot (over 100 °C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

#### 9. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 °C and 15 to 85%.

Use capacitors within 6 months after delivered. Check the solderability after 6 months or more.

#### 10. LIMITATION OF APPLICATIONS

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. Power plant control equipment
- 5. Medical equipment
- 6. Transportation equipment (vehicles, trains, ships, etc.)
- 7. Traffic signal equipment
- 8. Disaster prevention / crime prevention equipment
- 9. Data-processing equipment exerting influence on public
- 10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

#### NOTICE

### 1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

# 2. CAPACITANCE CHANGE OF CAPACITORS

· Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

· Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit.

Please contact us if you need a detail information.

#### 3. PERFORMANCE CHECK BY EQUIPMENT

Before using a capacitor, check that there is no problem in the equipment's performance and the specifications.

Generally speaking, CLASS 2 ceramic capacitors have voltage dependence characteristics and temperature dependence characteristics in capacitance. So, the capacitance value may change depending on the operating condition in a equipment. Therefore, be sure to confirm the apparatus performance of receiving influence in a capacitance value change of a capacitor, such as leakage current and noise suppression characteristic.

Moreover, check the surge-proof ability of a capacitor in the equipment, if needed, because the surge voltage may exceed specific value by the inductance of the circuit.

# $oldsymbol{\Lambda}$ note

- 1.Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

EGD08E

#### 1. Application

This specification is applied to Safety Standard Certified Lead Type Disc Ceramic Capacitors Type KX used for General Electric equipment.

Type KX is Safety Standard Certified capacitors of Class X1,Y1.

Do not use these products in any automotive power train or safety equipment including battery chargers for electric vehicles and plug-in hybrids.

Approval standard and certified number

1,1	Standard number	*Certified number	AC Rated volt. V(r.m.s.)
UL	UL60384-14	E37921	
CSA	CSA E60384-14	1343810	
VDE	IEC60384-14, EN60384-14	40002831	
BSI	EN62368-1, IEC60384-14, EN60384-14	KM 37901	
SEMKO		1905545	X1:440
DEMKO	JE000004.44	D-07250	Y1:250
FIMKO	IEC60384-14, EN60384-14	FI 40129	11.200
NEMKO	P19223458		
ESTI		21.0060	
IMQ	EN60384-14	V4069	
CQC	GB/T6346.14	CQC04001011643	
KTC	K60384-14	HU03008-4003, HU03008-4004	

<sup>\*</sup>Above Certified number may be changed on account of the revision of standards and the renewal of certification.

#### 2. Rating

2-1. Operating temperature range

-40 ~ +125°C

2-2. Part number configuration

ex.) DE1 KX 1X 680 Product Capacitance Capacitance Temperature Type **Packing** Individual Lead code characteristic name tolerance code style code specification

Product code

DE1 denotes X1,Y1 class.

• Temperature characteristic

Code	Temperature characteristic
1X	SL

Please confirm detailed specification on [ Specification and test methods ].

• Type name

This denotes safety certified type name Type KX.

<sup>&</sup>lt;The rated voltage of this product is AC250V(r.m.s).>

#### Capacitance

The first two digits denote significant figures; the last digit denotes the multiplier of 10 in pF. ex.) In case of 680.

$$68 \times 10^0 = 68 pF$$

#### • Capacitance tolerance

Please refer to [ Part number list ].

#### • Lead code

_	oao					
	Code	Lead style				
	A*	Vertical crimp long type				
	B*	Vartical arises abort tune	Lead Length: 5mm			
	J*	Vertical crimp short type	Lead Length: 3.5mm			
	N*	Vertical crimp taping type				

<sup>\*</sup> Please refer to [ Part number list ]

• Packing style code

<u>9 - 7 </u>	
Code	Packing type
В	Bulk type
Α	Ammo pack taping type

#### • Individual specification

In case part number cannot be identified without 'individual specification', it is added at the end of part number.

Code	Specification
C05F	<ul> <li>Halogen free         (Br ≤ 900ppm, Cl ≤ 900ppm)         Br + Cl ≤ 1500ppm</li> <li>CP wire</li> </ul>

Note) Murata part numbers might be changed depending on lead code or any other changes. Therefore, please specify only the type name(KX) and capacitance of products in the parts list when it is required for applying safety standard of electric equipment.

3. Marking

<Right side> <Reverse side>

Type name : KX Rated voltage mark : **X1 440~** 

Nominal capacitance : Actual value Y1 250~ Capacitance tolerance : Code CQC Approval mark : CQC

Company name code : (Made in Tailand) KTC Approval mark :

Manufacturing year : Letter code

(The last digit of A.D. year.)

Manufacturing month : Code

Feb./Mar.  $\rightarrow$  2 Aug./Sep.  $\rightarrow$  8 Apr./May  $\rightarrow$  4 Oct./Nov.  $\rightarrow$  O Jun./Jul.  $\rightarrow$  6 Dec./Jan.  $\rightarrow$  D

UL Approval mark : 51

CSA Approval mark : 😘

VDE Approval mark :

BSI Approval mark : BSI

SEMKO Approval mark : S

DEMKO Approval mark : ①

FIMKO Approval mark : (F)

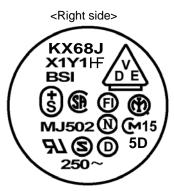
NEMKO Approval mark : (N)

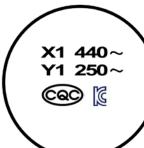
Class code : X1Y1

Halogen free mark : #F

Rated voltage mark : 250~

(Example)

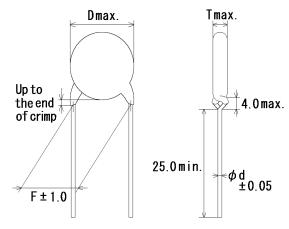




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#### 4. Part number list

·Vertical crimp long type (Lead code: A\*)

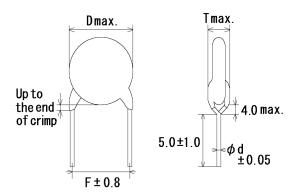


Note) The mark '\*' of lead code differ from lead spacing(F) and lead diameter(d).

Please see the following list about details.

Unit: mm Pack Dimension (mm) Cap. Lead Cap. T.C. **Customer Part Number** Murata Part Number qty. (pF) tol. code Τ F D d (pcs) SL DE11XKX100JA4BC05F 10.0 10 9.0 8.0 0.6 Α4 250  $\pm 5\%$ SL 15  $\pm 5\%$ DE11XKX150JA4BC05F 9.0 8.0 10.0 0.6 A4 250 22  $\pm 5\%$ SL DE11XKX220JA4BC05F 9.0 8.0 10.0 0.6 Α4 250 33  $\pm 5\%$ DE11XKX330JA4BC05F 250 SL 9.0 8.0 10.0 0.6 A4 47 DE11XKX470JA4BC05F 10.0 SL  $\pm 5\%$ 9.0 8.0 0.6 Α4 250 SL 68  $\pm 5\%$ DE11XKX680JA4BC05F 10.0 9.0 8.0 0.6 Α4 250

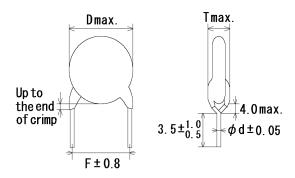
Vertical crimp short type (Lead code:B\*)



Note) The mark '\*' of lead code differ from lead spacing(F) and lead diameter(d).
Please see the following list about details.

TC	Cap.	Cap.	Customer Part Number Murata Part Number		Dir	mensi	on (m	m)	Lead	Pack
T.C.	(pF)	tol.	Customer Part Number	Murata Part Number	D	Т	F	d	code	qty. (pcs)
SL	10	±5%		DE11XKX100JB4BC05F	9.0	8.0	10.0	0.6	B4	500
SL	15	±5%		DE11XKX150JB4BC05F	9.0	8.0	10.0	0.6	B4	500
SL	22	±5%		DE11XKX220JB4BC05F	9.0	8.0	10.0	0.6	B4	500
SL	33	±5%		DE11XKX330JB4BC05F	9.0	8.0	10.0	0.6	B4	500
SL	47	±5%		DE11XKX470JB4BC05F	9.0	8.0	10.0	0.6	B4	500
SL	68	±5%		DE11XKX680JB4BC05F	9.0	8.0	10.0	0.6	В4	500

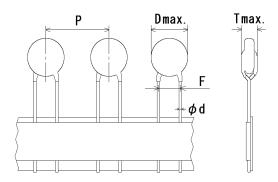
·Vertical crimp short type
(Lead code:J\*)



Note) The mark '\*' of lead code differ from lead spacing(F) and lead diameter(d).
Please see the following list about details.

									Offic.	111111
T.C.	Сар.	Сар.	Customor Part Number	r Part Number Murata Part Number -		nensi	on (mı	m)	Lead	Pack
1.0.	(pF)	tol.	Customer Fait Number			Т	F	d	code	qty. (pcs)
SL	10	±5%		DE11XKX100JJ4BC05F	9.0	8.0	10.0	0.6	J4	500
SL	15	±5%		DE11XKX150JJ4BC05F	9.0	8.0	10.0	0.6	J4	500
SL	22	±5%		DE11XKX220JJ4BC05F	9.0	8.0	10.0	0.6	J4	500
SL	33	±5%		DE11XKX330JJ4BC05F	9.0	8.0	10.0	0.6	J4	500
SL	47	±5%		DE11XKX470JJ4BC05F	9.0	8.0	10.0	0.6	J4	500
SL	68	±5%		DE11XKX680JJ4BC05F	9.0	8.0	10.0	0.6	J4	500

# ·Vartical crimp taping type (Lead code:N\*)



Note) The mark '\*' of lead code differ from lead spacing(F), lead diameter(d) and pitch of component(P). Please see the following list or taping specification about details.

										OTHE.	
T.C.	Cap.	Сар.	Customer Part Number	Murata Part Number -		Dimer	nsion	(mm	)	Lead	Pack
1.0.	(pF) tol.	Customer Fait Number	warata i art i vuilibei		Т	F	d	Р	code	qty. (pcs)	
SL	10	±5%		DE11XKX100JN4AC05F	9.0	8.0	10.0	0.6	25.4	N4	500
SL	15	±5%		DE11XKX150JN4AC05F	9.0	8.0	10.0	0.6	25.4	N4	500
SL	22	±5%		DE11XKX220JN4AC05F	9.0	8.0	10.0	0.6	25.4	N4	500
SL	33	±5%		DE11XKX330JN4AC05F	9.0	8.0	10.0	0.6	25.4	N4	500
SL	47	±5%		DE11XKX470JN4AC05F	9.0	8.0	10.0	0.6	25.4	N4	500
SL	68	±5%		DE11XKX680JN4AC05F	9.0	8.0	10.0	0.6	25.4	N4	500

5.5	pecification and	test methods		ciciciice on						
No.	pecification and Iter		Sne	cification				Test n	nethod	
1	Appearance and d		No marked defect on appearance form and dimensions.		The capacitor should be inspected by naked eyes for visible evidence of defect.					
2	Marking		Please refer to [Part number list].  To be easily legible.		Dimensions should be measured with slide calipers.  The capacitor should be inspected by naked eyes.					
3	Dielectric	Between lead	No failure.							
	strength	wires				AC4 lead	000V(r.m.s wires for 60	.)<50/60Hz ) s.	z> is applie	ed between the
	Landation Decision	Body insulation	No failure.		conn Then close the b to the abou from Then conta diam Final 60 s balls	lly, AC4000 between th	ther. bil should b I around capacitor of  nal. bitor should with metal V (r.m.s.)< e capacito	Metal / foil / which is a second be inserted balls of about 50/60Hz> r lead wire	About 3 to 6 mm balls ed into a cout 1mm is applied for s and metal	
4	Insulation Resistar	nce (I.R.)	10 000MΩ min	i.		DC50	00±50V wit voltage sho	hin 60±5 s ould be app	of chargin lied to the	
5	Capacitance		Within specifie	ed tolerance.		The		e should be	e measure	d at 20°C with
6	Q		400+20C*2min.(30pF under) 1000min. (30pF min.)			1±0.1MHz and AC5V(r.m.s.) max  The Q should be measured at 20°C with 1±0.1MHz and AC5V(r.m.s.) max				
7	Temperature chara	acteristic	+350 to -1000 ppm/°C (Temp. range : +20 to +85°C)				capacitance step speci			ld be made at
				Step Temp.(°C)	2	1 0±2	2 -25±2	3 20±2	4 85±2	5 20±2
8	Active flammability		The cheese-cl on fire.	oth should not be	3	least chee to 20 disch main	cone but mose-cloth. TI cone but mose-cloth. cone	ore than two ne capacitos. The interpolation of the capacitos of the capac	or complete or should be real between the UAc she last disconsistency of the last disconsistency of th	e subjected en successive hould be charge.  R ct ut sciloscope 9% 10kV choke kV
* <sup>2</sup> ''C''	expresses nominal	capacitance value	(pF)							

			Reference only	
No.	Item		Specification	Test method
9	Robustness of terminations	Tensile	Lead wire should not cut off. Capacitor should not be broken.	Fix the body of capacitor, a tensile weight gradually to each lead wire in the radial direction of
		Bending		capacitor up to 10N and keep it for 10±1 s.  With the termination in its normal position, the
				capacitor is held by its body in such a manner that the axis of the termination is vertical; a mass
				applying a force of 5N is then suspended from the end of the termination.
				The body of the capacitor is then inclined,
				within a period of 2 to 3 s, through an angle of approximately 90° in the vertical plane and then
				returned to its initial position over the same period of time; this operation constitutes one bend.
				One bend immediately followed by a second bend in the opposite direction.
10	Vibration	Appearance	No marked defect.	The capacitor should be firmly soldered to the
	resistance	Capacitance	Within the specified tolerance.	supporting lead wire and vibration which is 10 to
		Q	400+20C* <sup>2</sup> min.(30pF under)	55Hz in the vibration frequency range,1.5mm in total amplitude, and about 1min in the rate of
			1000min. (30pF min.)	vibration change from 10Hz to 55Hz and back to
				10Hz is applied for a total of 6 h; 2 h each in
				3 mutually perpendicular directions.
11	Solderability of lead	ls	Lead wire should be soldered	The lead wire of a capacitor should be dipped into a
			With uniformly coated on the axial direction over 3/4 of the	ethanol solution of 25wt% rosin and then into
			circumferential direction.	molten solder for 2±0.5 s. In both cases the depth of
			on summer of the different of the summer of	dipping is up to about 1.5 to 2.0mm from the root of lead wires.
				Temp. of solder:
				245±5°C Lead Free Solder (Sn-3Ag-0.5Cu) 235±5°C H63 Eutectic Solder
12	Soldering effect	Appearance	No marked defect.	Solder temperature: 350±10°C or 260±5°C
	(Non-preheat)	Capacitance change	Within ±10%	Immersion time : 3.5±0.5 s (In case of 260±5°C : 10±1 s)
		I.R.	1 000MΩ min.	The depth of immersion is up to about
		Dielectric	Per item 3	1.5 to 2.0mm from the root of lead wires.
		strength		Thermal Capacitor insulating
				1.5
				to 2.0mm
				solder
				Pre-treatment: Capacitor should be stored at 85±2°C for 1 h, then placed at
				*1room condition for 24±2 h
				before initial measurements.
				Post-treatment: Capacitor should be stored for 1 to 2 h at *1room condition.
13	Soldering effect	Appearance	No marked defect.	First the capacitor should be stored at 120+0/-5°C
	(On-preheat)	Capacitance	Within ±10%	for 60+0/-5 s. Then, as in figure, the lead wires should be
		change I.R.	1 000MΩ min.	immersed solder of 260+0/-5°C up to 1.5 to 2.0mm
		Dielectric	Per item 3	from the root of terminal for 7.5+0/-1 s.
		strength		Thermal
				insulating 1.5
				1.5 to 2.0mm
				Molten   solder
				Pre-treatment : Capacitor should be stored at
				85±2°C for 1 h, then placed at  *1room condition for 24±2 h
				before initial measurements.
				Post-treatment: Capacitor should be stored for 1 to
لــــا		<u> </u>		2 h at *1room condition.
*1 "roc	om condition" Temper	rature: 15 to 35°0	C, Relative humidity: 45 to 75%, Atm	ospheric pressure: 86 to 106kPa

\*2 "C" expresses nominal capacitance value(pF)

			Reference only					
No.	Item	1	Specification	Test method				
14	Flame test		The capacitor flame discontinue as follows.	The capacitor should be subjected to applied flame for 15 s. and then removed for 15 s until 5 cycle.				
			Cycle         Time           1 to 4         30 s max.           5         60 s max.	Capacitor Flame Gas Burner				
15	Passive flammabilit	у	The burning time should not be exceeded the time 30 s. The tissue paper should not ignite.	The capacitor under test should be held in the flame in the position which best promotes burning.  Time of exposure to flame is for 30 s.  Length of flame: 12±1mm  Gas burner: Length 35mm min. Inside Dia. 0.5±0.1mm Outside Dia. 0.9mm max.  Gas: Butane gas Purity 95% min.				
				About 8mm  Gas burner  A5°  Capacitor  Capacitor  Tissue  About 10mm thick board				
16	Humidity (Under steady	Appearance Capacitance	No marked defect. Within ±5%	Set the capacitor for 500±12 h at 40±2°C in 90 to 95% relative humidity.				
	state)	change	VVIIIIII ±3 /0	5070 Totalive Harmarty.				
	,	Q	275+5/2C*2min.(30pF under) 350min. (30pF min.)	Post-treatment: Capacitor should be stored for 1 to 2 h at *1room condition.				
		I.R.	3000MΩ min.					
		Dielectric strength	Per item 3					
17	Humidity loading	Appearance	No marked defect.	Apply the rated voltage for 500±12 h at 40±2°C in				
		Capacitance change	Within ±5%	90 to 95% relative humidity.				
		Q	275+5/2C*2min.(30pF under) 350min. (30pF min.)	Post-treatment: Capacitor should be stored for 1 to 2 h at *1room condition.				
		I.R.	3000MΩ min.					
		Dielectric strength	Per item 3					
*1 !!	and the state of the state of	45 to 050	C Bolotivo humidity: 45 to 75% Atm					

<sup>\*1 &</sup>quot;room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa \*2 "C" expresses nominal capacitance value(pF)

Appearance   No marked defect.   Impulse voltage   Each individual capacitor should be subjected to a RkV impulses for three times. Then the capacitors are applied to life test.	٧o.	Item	า	Reference on Specification	<del></del>		Test m	ethod		
Capacitance change   LR.   3000MΩ min.	18				Impuls	e voltac		ictrica		
Change   1.R.   3000MΩ min.   2.000MΩ min.   2.000MΩ min.   3000MΩ mi										
Transport   Tran				VVIIIII ±2070		8kV impulses for three times. Then the capacitors				
Dielectric strength  Per item 3  The capacitors are placed in a circulating air oven for a period of 1000 h. The air in the oven is maintained at a temperature of 125+2/-0 °C, and relative humidity of 50% max. Throughout the test, heapacitors are subjected to a AC425V(r.m.s.) < 50/60Hz - alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0:  Post-treatment: Capacitor should be stored for 1 2 h at "froor condition.  I.R. 3000MΩ min.  I.R. 3000MΩ min.  Dielectric strength  Per item 3  Per item 3  Per item 3  Per item 3  The capacitors are placed in a circulating air oven for a period of 1000 h. The apacitors are subjected to a AC425V(r.m.s.) < 50/60Hz - alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0:  Post-treatment: Capacitor should be stored for 1 2 h at "froor condition.  The capacitor should be subjected to 5 femperature (°C) = 1 min				3000MO min.					· ·	
Strength										
The capacitors are placed in a circulating air over for a period of 1000 h. The air in the oven is maintained at a temperature of 125+2/-0°C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a AC425V(r.m.s.)-50/061/2-alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1 000V(r.m.s.) for 0. Post-treatment: Capacitor should be stored for 1 2 hat "froom condition.  The capacitor should be subjected to 5 temperature voltage is increased to AC1 000V(r.m.s.) for 0. Post-treatment: Capacitor should be subjected to 5 temperature voltage. The capacitor should be subjected to 5 temperature voltage. The capacitor should be subjected to 5 temperature voltages, then consecutively to 2 immersion cycles.  The capacitors are placed in a circulating air over for 1 25+2/-0°C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a AC425V(r.m.s.)+50/061/2 alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1 000V(r.m.s.) for 0.  Post-treatment: Capacitor should be subjected to 5 temperature voltes.  The capacitors are placed in a circulating air over for 125+2/-0°C, and relative humidity of 125-80% of 125-				T of Rom o	10	00 (%)	_ F	ront time (T1)	$= 1.7 \mu$ s=1.67T	
The capacitors are placed in a circulating air oven for a period of 1 000 h. The air in the oven is maintained at a temperature of 125+27-0°C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a AC425V(r.m.s.), 550/60Hz> altermating voltage of mains frequency, except that once each hour the voltage is increased to AC1 000V(r.m.s.) for 0.º Post-treatment: Capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 1 2 h at **room condition.  The capacitor should be stored for 4 2 h at **room condition.  The capacitor should be stored at a stored of 1 h then placed at **room condition.						/ 11	_ T	ime to half-va	lue (T2) = $50 \mu$ s	
The capacitors are placed in a circulating air oven for a period of 1 000 h.  The air in the oven is maintained at a temperature of 125+2/-0°C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a AC425V(r.m.s.) <50/60/12-alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1 000V(r.m.s.) for 0.  Post-treatment: Capacitor should be stored for 1 2 h at *¹room condition.  The capacitor should be stored for 1 2 h at *¹room condition.  The capacitor should be stored for 1 2 h at *¹room condition.  The capacitor should be stored for 1 2 h at *¹room condition.  The capacitor should be stored for 1 2 h at *¹room condition.  The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  **Temperature cycle>    Step   Temperature(°C)   Time						/ / II		_		
Tracapacitors are placed in a circulating air oven for a period of 1000 h.  The air in the oven is maintained at a temperature of 125+2/-0 °C, and relative humidity of 50% max. Throughout the stitle capacitors are subjected to a AC425V(r.m.s.)<80/60Hz> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.¹  Post-treatment: Capacitor should be stored for 1 2 h at **froom condition.  Post-treatment: Capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  **Temperature and immersion cycle**    Appearance						<b>∠</b>		<del>_</del>		
The capacitors are placed in a circulating air oven for a period of 1 000 h.  The air in the oven is maintained at a temperature of 125±2/-0 °C for 1 h, then placed in a circulating air oven for a period of 1 000 h.  The air in the oven is maintained at a temperature of 125±2/-0 °C for 1 h, then placed at a circulating air oven for a period of 1 000 h.  The air in the oven is maintained at a temperature of 125±2/-0 °C for 1 h, then placed at a 'room condition.  The capacitors are placed in a circulating air oven for a period of 1 000 h.  The air in the oven is maintained at a temperature of 125±2/-0 °C for 1 h, then placed at strength of 1 25±2/-0 °C for 1 h, then placed at 'room condition.  The capacitor should be stored for 4 on an of the voltage is increased to AC1 000V(r.m.s.) for 0.  Post-treatment: Capacitor should be stored at 85±2°C for 1 h, then placed at 'room condition.								•		
Temperature and immersion cycle   Appearance   No marked defect.   Post-treatment : Capacitor should be stored for 1						L	T2			
Temperature and immersion cycle   Appearance   No marked defect.   Post-treatment : Capacitor should be stored for 1					Thorac	naoitar	oro placed	in a airaul	oting oir ovon	
The air in the oven is maintained at a temperature of 125+2/-0 °C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a ΛC425V(r.m.s.).50/60Hz- alternating voltage of mains frequency, except that once each hour the voltage is increased to ΛC1 000V(r.m.s.) for 0.1 Post-treatment: Capacitor should be stored for 1 2 h at *1 room condition.  The carporature and immersion cycle (Capacitance change)  Q 275+5/2C*2min.(30pF under) 350min. (30pF min.)  I.R. 3000MΩ min.  Dielectric strength  Per item 3  The carporative specified to 5 temperature cycles, then consecutively to 2 immersion cycles.  *Temperature cycle>  *Temperature cycle>  *Temperature cycle>  *Temperature cycle>  *Immersion cycle immersion cycles.  *Immersion cycle immersion cycles.  *Immersion cycle>  *Immer								iii a ciicui	alling all over	
of 125+2/-0 °C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a ΛC425V(r.m.s.)<0/4/1000V(r.m.s.) for 0.*  Post-treatment: Capacitor should be stored for 1 2 h at *froom condition.  Appearance No marked defect. Capacitance change Q 275+5/2C*²min.(30pF under) 350min. (30pF min.)  I.R. 3000MΩ min.  Dielectric strength  Per item 3  Per item 3  Appearance No marked defect. The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. **  **Temperature cycle>  Step Temperature(°C) Time 1 mersion cycle time: 5 cycle time: 5 cycle time: 5 cycle time: 2								tained at :	a temperature	
Throughout the test, the capacitors are subjected to a AC425V(r.m.s.)<50/60Hz> atternating voltage of mains frequency, except that once each hour the voltage is increased to AC1 000V(r.m.s.) for 0.  Post-treatment: Capacitor should be stored for 1 2 h at *1room condition.  The capacitor should be subjected to 5 temperature condition.  The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  **Temperature cycle>**    Step   Temperature (°C)   Time   Time   1										
to a AČ425V(r.m.s.)-\(50\)/60Hz> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1  Post-treatment: Capacitor should be stored for 1 2 h at *\frac{1}{1} room condition.  The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  The capacitor should be subjected to 5 temperature cycles.  Temperature cycle>  Step Temperature(°C) Time										
Step   Temperature cycles										
Post-treatment : Capacitor should be stored for 1 2 h at *'room condition.					of mair	ns frequ	ency, except	that once	e each hour	
Post-treatment : Capacitor should be stored for 1 2 h at *'room condition.					the vol	tage is	increased to	AC1000\	/(r.m.s.) for 0.1	
Temperature and immersion cycle   Appearance   Appearance   Within ±5%   Capacitance change   Within ±5%   Capacitance change   Q   275+5/2C*²min.(30pF under)   350min.   (30pF min.)       I.R.   3000MΩ min.   Per item 3       Dielectric strength   Per item 3       Step   Temperature(°C)   Time       1										
Temperature and immersion cycle   Appearance   Capacitance change   Within ±5%     Q					Post-tr	eatmen				
Capacitance change   Within ±5%   Cycles, then consecutively to 2 immersion cycles.			_				2 h at *1ro	oom cond	lition.	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	9									
Q   275+5/2C*2min.(30pF under)   350min. (30pF min.)     I.R.   3000MΩ min.     Dielectric strength   Per item 3   Per item 3     Cycle time : 5 cy   Temperature(°C)   Time   1		immersion cycle		Within ±5%	cycles,	then co	onsecutively	to 2 imme	ersion cycles.	
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$			change		.Tomo	0 * 0 tu . * 0	ovele.			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			Q		< remp				-	
Dielectric strength  Per item 3  Room temp. 3 min 3 min 4 Room temp. 3 min 4 Room temp.  Cycle time: 5 cy  Immersion cycle>  Step Temperature(°C) Time Immersion water 1 +65+5/-0 15 min Salt water 2 0±3 15 min Salt water 2 cycle time: 2 cy  Pre-treatment: Capacitor should be stored at 85±2°C for 1 h, then placed at *1 room condition for 24±2 h.  Post-treatment: Capacitor should be stored for 4 24 h at *1 room condition.						Step				
strength    3										
A   Room temp.   3 min   Cycle time : 5 cy				Per item 3			Room te	emp.		
Cycle time : 5 cy <pre></pre>			strength							
Step Temperature(°C) Time Immersion water 1 +65+5/-0 15 min Clean water 2 0±3 15 min Salt water Cycle time : 2 cy Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at *1room condition for 24±2 h. Post-treatment : Capacitor should be stored for 4 24 h at *1room condition.						4	Room te			
1 +65+5/-0 15 min Clean water 2 0±3 15 min Salt water  Cycle time : 2 cy  Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at *1 room condition for 24±2 h.  Post-treatment : Capacitor should be stored for 4 24 h at *1 room condition.								Time	Immersion	
Pre-treatment: Capacitor should be stored at 85±2°C for 1 h, then placed at *1room condition for 24±2 h.  Post-treatment: Capacitor should be stored for 4 24 h at *1room condition.					1	+1	65+5/-0	15 min	Clean	
Cycle time: 2 cy  Pre-treatment: Capacitor should be stored at 85±2°C for 1 h, then placed at *1room condition for 24±2 h.  Post-treatment: Capacitor should be stored for 4 24 h at *1room condition.		1			2		0+3	15 min	Salt	
Pre-treatment: Capacitor should be stored at 85±2°C for 1 h, then placed at *1room condition for 24±2 h.  Post-treatment: Capacitor should be stored for 4 24 h at *1room condition.							0±3	13 111111	water	
85±2°C for 1 h, then placed at *¹room condition for 24±2 h.  Post-treatment: Capacitor should be stored for 4 24 h at *¹room condition.								Cv		
*1room condition for 24±2 h.  Post-treatment: Capacitor should be stored for 4 24 h at *1room condition.								•	•	
Post-treatment : Capacitor should be stored for 4 24 h at *1 room condition.						atment		r should b	e stored at	
24 h at *1room condition.						atment	85±2°C f	r should b or 1 h, the	pe stored at en placed at	
24 h at *1room condition.						atment	85±2°C f	r should b or 1 h, the	pe stored at en placed at	
					Pre-tre		85±2°C fo *1room co	r should bor 1 h, the	pe stored at en placed at or 24±2 h.	
					Pre-tre		85±2°C for *1 room cont : Capacito	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h.	
	'roo	om condition" Tempe	erature: 15 to 35°(	C, Relative humidity: 45 to 75%, A	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h. pe stored for 4	
	'roc	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h. pe stored for 4	
,	'roo	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h. pe stored for 4	
	'roi "C'	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, <i>P</i> e(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h. pe stored for 4	
	'roi "C'	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, <i>A</i> e(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h.	
	"roo "C'	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h.	
	"roo "C'	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, <i>A</i> e(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h.	
	"ro "C'	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h.	
	"roo	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h.	
	"roi "C'	om condition" Tempe " expresses nominal	erature: 15 to 35°0 capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h.	
	"ro:	om condition" Tempe " expresses nominal	erature: 15 to 35°0 capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h.	
	"ro "C'	om condition" Tempe " expresses nominal	erature: 15 to 35°0 capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h. pe stored for 4	
	"ro	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h. pe stored for 4	
	'roc	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h. pe stored for 4	
	'roi "C'	om condition" Tempe " expresses nominal	erature: 15 to 35°( capacitance valu	C, Relative humidity: 45 to 75%, Ae(pF)	Pre-tre	eatmen	85±2°C for the state of the sta	r should bor 1 h, the condition for should b	pe stored at en placed at or 24±2 h. pe stored for 4	

# 6.Packing specification

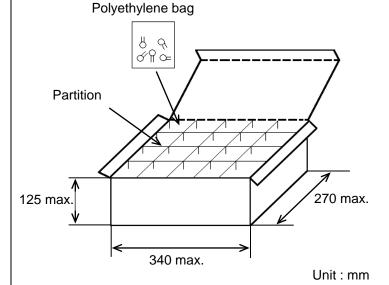
•Bulk type (Packing style code : B)

\*1 \*2
The number of packing = Packing quantity × n

The size of packing case and packing way

\*1 : Please refer to [Part number list].

\*2 : Standard n = 20 (bag)

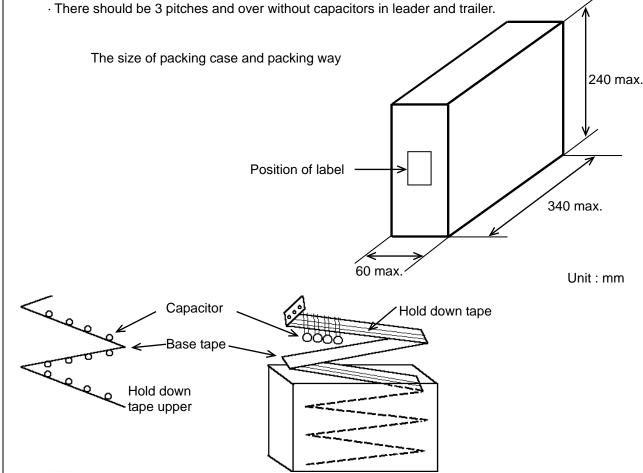


Note)

The outer package and the number of outer packing be changed by the order getting amount.

•Ammo pack taping type (Packing style code : A)

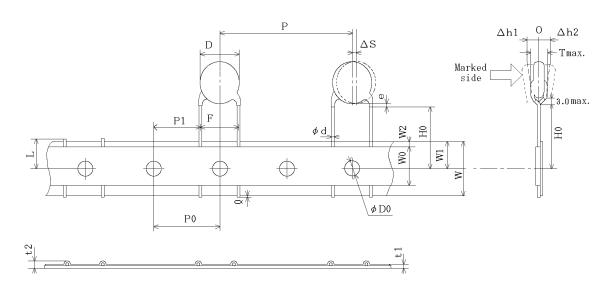
- · The tape with capacitors is packed zigzag into a case.
- $\cdot$  When body of the capacitor is piled on other body under it.



# 7. Taping specification

# 7-1. Dimension of capacitors on tape

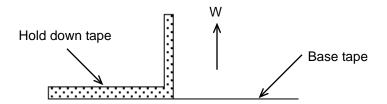
Vertical crimp taping type < Lead code : N4 >
Pitch of component 25.4mm / Lead spacing 10.0mm



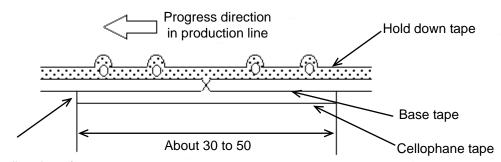
			Offit : Illill
Item	Code	Dimensions	Remarks
Pitch of component	Р	25.4±2.0	
Pitch of sprocket hole	P0	12.7±0.3	
Lead spacing	F	10.0±1.0	
Length from hole center to lead	P1	7.7±1.5	
Body diameter	D	Please refer to [ P	art number list ].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend .
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and	H0	18.0± <sub>0</sub> <sup>2.0</sup>	
bottom planes	ПО	16.0± <sub>0</sub>	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	0.0	
Deviation across tape, rear	∆h2	2.0 max.	
Portion to cut in case of defect	L	11.0± <sub>1.0</sub>	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	Up to the end of c	rimp
Body thickness	Т	Please refer to [ P	art number list ].

# 7-2. Splicing way of tape

1) Adhesive force of tape is over 3N at test condition as below.



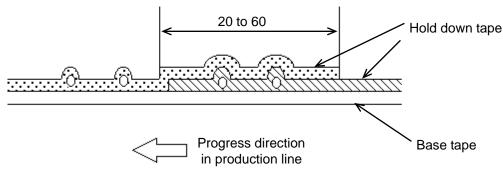
- 2) Splicing of tape
  - a) When base tape is spliced
    - •Base tape should be spliced by cellophane tape. (Total tape thickness should be less than 1.05mm.)



No lifting for the direction of progressing

Unit: mm

- b) When hold down tape is spliced
  - •Hold down tape should be spliced with overlapping. (Total tape thickness should be less than 1.05mm.)



- c) When both tape are spliced
  - •Base tape and hold down tape should be spliced with splicing tape.
- 3) Missing components
  - •There should be no consecutive missing of more than three components.
  - •The number of missing components should be not more than 0.5% of total components that should be present in a Ammo pack.

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33910103 YV101103Z060HAND5P 46KN3330JBM1K 413N32200000M 463I333000M1K 46KF2470JBN0M 46KF268000M1M

46KF310000M1M 46KI22205001M 46KI24705201K 46KI2470CK01M 46KI2470ND01K 46KI2680JH01M 46KI315000M2K

46KI315000M2M 46KI3150CKM2K 46KI3150CKM2M 46KI3150NDM2M 46KI3220CKP0M 46KI3220JLM1M 46KN3150JH01K

46KN34705001K 46KN347050N0K 46KN3470JHP0M 46KN410040H1M 46KW510050M1K 474I24700003K PHE840MD6220MD13R30

PHE840MY6470MD14R06 PHE845VD5470MR06 YV500103Z060B20X5P MKPX2R-1/400/10P27 YP102271K050B20C6P

YP102391K050BAND5P YP501101K040BAND5P YP102681K060B20C6P YP501121K040B20C6P