



Type SA Safety Standard Certified Lead Type Disc Ceramic Capacitors for General Purpose

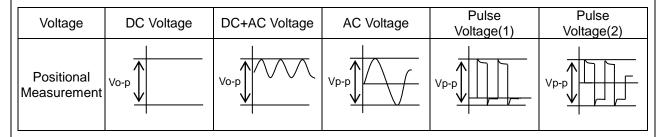
Product specifications in this catalog are as of Jun. 2019, and are subject to change or obsolescence without notice.

Please consult the approval sheet before ordering. Please read rating and Cautions first.

⚠ CAUTION

1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.



2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20 °C on the condition of atmosphere temperature 25 °C. When measuring, use a thermocouple of small thermal capacity-K of ϕ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.(Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

3. TEST CONDITION FOR WITHSTANDING VOLTAGE

(1) TEST EQUIPMENT

Test equipment for AC withstanding voltage should be used with the performance of the wave similar to 50/60 Hz sine wave.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

(2) VOLTAGE APPLIED METHOD

When the withstanding voltage is applied, capacitor's lead or terminal should be firmly connected to the out-put of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminal should be taken off the out-put of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

*ZERO CROSS is the point where voltage sine wave pass 0V.

- See the right figure -

voltage sine wave

4. FAIL-SAFE

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 °C max. Soldering iron wattage: 50W max. Soldering time: 3.5s max.

7. BONDING, RESIN MOLDING AND COATING

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive, molding resin or coating may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

8. TREATMENT AFTER BONDING, RESIN MOLDING AND COATING

When the outer coating is hot (over 100 $^{\circ}$ C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 °C and 15 to 85%.

Use capacitors within 6 months after delivered. Check the solderability after 6 months or more.

10. LIMITATION OF APPLICATIONS

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. Power plant control equipment
- 5. Medical equipment
- 6. Transportation equipment (vehicles, trains, ships, etc.)
- 7. Traffic signal equipment
- 8. Disaster prevention / crime prevention equipment
- 9. Data-processing equipment exerting influence on public
- Application of similar complexity and/or reliability requirements to the applications listed in the above.

NOTICE

1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum. Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

2. CAPACITANCE CHANGE OF CAPACITORS

· Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

· Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit.

Please contact us if you need a detail information.

3. PERFORMANCE CHECK BY EQUIPMENT

Before using a capacitor, check that there is no problem in the equipment's performance and the specifications.

Generally speaking, CLASS 2 ceramic capacitors have voltage dependence characteristics and temperature dependence characteristics in capacitance. So, the capacitance value may change depending on the operating condition in a equipment. Therefore, be sure to confirm the apparatus performance of receiving influence in a capacitance value change of a capacitor, such as leakage current and noise suppression characteristic.

Moreover, check the surge-proof ability of a capacitor in the equipment, if needed, because the surge voltage may exceed specific value by the inductance of the circuit.

⚠ NOTE

- 1.Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

EGD08E

1. Application

This specification is applied to Safety Standard Certified Lead Type Disc Ceramic Capacitors Type SA used for General Electric equipment.

Type SA is Safety Standard Certified capacitors of Class X1,Y2.

Do not use these products in any automotive power train or safety equipment including battery chargers for electric vehicles and plug-in hybrids.

Approval standard and certified number

	Standard number	*Certified number	AC Rated volt. V(r.m.s.)
UL/cUL	UL60384-14	E37921	
ENEC	ENICO204 44	40040000	X1:440
(VDE)	EN60384-14	40042990	Y2:400
CQC	IEC60384-14	CQC15001137840	

^{*}Above Certified number may be changed on account of the revision of standards and the renewal of certification.

2. Rating

2-1. Operating temperature range $-40 \sim +125$ °C

2-2. Rated Voltage X1:AC440V(r.m.s.) Y2:AC400V(r.m.s.)

DC1kV

2-3. Part number configuration

ex.) <u>DE2</u>	B3	SA	471	K	_A3_	B	Y02F
Product	Temperature	Туре	Capacitance	Capacitance	Lead	Packing	Individual
code	characteristic	name		tolerance	code	style code	specification

• Product code

DE2 denotes class X1,Y2.

•Temperature characteristic

Code	Temperature characteristic
1X	SL
B3	В
E3	Е

Please confirm detailed specification on [Specification and test methods].

• Type name

This denotes safety certified type name Type SA.

• Capacitance

The first two digits denote significant figures; the last digit denotes the multiplier of 10 in pF. ex.) In case of 471.

$$47 \times 10^1 = 470 pF$$

• Capacitance tolerance

Please refer to [Part number list].

• Lead code

Code	Lead style	
A*	Vertical crimp long type	
J*	Vertical crimp short type	
N*	Vertical crimp taping type	

^{*} Please refer to [Part number list].

• Packing style code

Code	Packing type	
В	Bulk type	
Α	Ammo pack taping type	

• Individual specification

In case part number cannot be identified without 'individual specification', it is added at the end of part number.

c cha or part hamber.				
Code	Specification			
Y02F	 Rated voltage: X1:AC440V(r.m.s.)			
1	1 1000 W1100. 7 (02000 V (1.111.0.)			

Note) Murata part numbers might be changed depending on lead code or any other changes. Therefore, please specify only the type name(SA) and capacitance of products in the parts list when it is required for applying safety standard of electric equipment.

3. Marking

Type name : SA

Nominal capacitance : Actual value(under 100pF)
3 digit system(100pF and over)

Capacitance tolerance : Code Class code and Rated voltage mark : **X1 440~**

Y2 400~

Manufacturing year : Letter code (The last digit of A.D. year.)

Manufacturing month : Code

Company name code : (Made in Thailand)

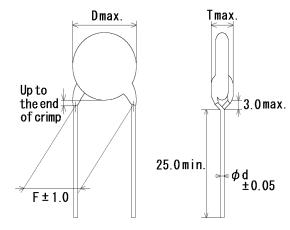
(Example)

SA 471K X1 440~ Y2 400~ 5D (M15

ETSA01B

4. Part number list

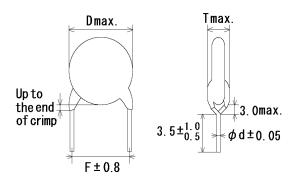
·Vertical crimp long type (Lead code:A*)



Note) The mark '*' of lead code differ from lead spacing(F) and lead diameter(d).
Please see the following list about details.

Customer Part Number Murata Part Number D T F d Customer Part Number D E21XSA100KA3BY02F 7.0 5.0 7.5 0.6	. Pack
SL 10 ±10% DE21XSA100KA3BY02F 7.0 5.0 7.5 0.6 0.5 0.	ead qty.
SL 15 ±10% DE21XSA150KA3BY02F 6.0 6.0 7.5 0.6 A SL 22 ±10% DE21XSA220KA3BY02F 6.0 5.0 7.5 0.6 A SL 33 ±10% DE21XSA330KA3BY02F 7.0 5.0 7.5 0.6 A SL 47 ±10% DE21XSA470KA3BY02F 7.0 5.0 7.5 0.6 A SL 68 ±10% DE21XSA680KA3BY02F 9.0 5.0 7.5 0.6 A B 100 ±10% DE2B3SA101KA3BY02F 6.0 5.0 7.5 0.6 A B 150 ±10% DE2B3SA151KA3BY02F 6.0 5.0 7.5 0.6 A	ode (pcs)
SL 22 ±10% DE21XSA220KA3BY02F 6.0 5.0 7.5 0.6 6 SL 33 ±10% DE21XSA330KA3BY02F 7.0 5.0 7.5 0.6 6 SL 47 ±10% DE21XSA470KA3BY02F 7.0 5.0 7.5 0.6 6 SL 68 ±10% DE21XSA680KA3BY02F 9.0 5.0 7.5 0.6 6 B 100 ±10% DE2B3SA101KA3BY02F 6.0 5.0 7.5 0.6 6 B 150 ±10% DE2B3SA151KA3BY02F 6.0 5.0 7.5 0.6 6	A3 250
SL 33 ±10% DE21XSA330KA3BY02F 7.0 5.0 7.5 0.6 A SL 47 ±10% DE21XSA470KA3BY02F 7.0 5.0 7.5 0.6 A SL 68 ±10% DE21XSA680KA3BY02F 9.0 5.0 7.5 0.6 A B 100 ±10% DE2B3SA101KA3BY02F 6.0 5.0 7.5 0.6 A B 150 ±10% DE2B3SA151KA3BY02F 6.0 5.0 7.5 0.6 A	A3 500
SL 47 ±10% DE21XSA470KA3BY02F 7.0 5.0 7.5 0.6 6 SL 68 ±10% DE21XSA680KA3BY02F 9.0 5.0 7.5 0.6 6 B 100 ±10% DE2B3SA101KA3BY02F 6.0 5.0 7.5 0.6 6 B 150 ±10% DE2B3SA151KA3BY02F 6.0 5.0 7.5 0.6 6	A3 500
SL 68 ± 10% DE21XSA680KA3BY02F 9.0 5.0 7.5 0.6 A B 100 ± 10% DE2B3SA101KA3BY02F 6.0 5.0 7.5 0.6 A B 150 ± 10% DE2B3SA151KA3BY02F 6.0 5.0 7.5 0.6 A	A3 250
B 100 ±10% DE2B3SA101KA3BY02F 6.0 5.0 7.5 0.6 A B 150 ±10% DE2B3SA151KA3BY02F 6.0 5.0 7.5 0.6 A	A3 250
B 150 ±10% DE2B3SA151KA3BY02F 6.0 5.0 7.5 0.6 A	A3 250
	A3 500
B 220 ±10% DE2B3SA221KA3BY02F 6.0 6.0 7.5 0.6	A3 500
	A3 500
B 330 ±10% DE2B3SA331KA3BY02F 6.0 5.0 7.5 0.6 A	A3 500
B 470 ±10% DE2B3SA471KA3BY02F 7.0 5.0 7.5 0.6 A	A3 250
B 680 ±10% DE2B3SA681KA3BY02F 8.0 5.0 7.5 0.6 A	A3 250
E 1000 ±20% DE2E3SA102MA3BY02F 7.0 5.0 7.5 0.6 A	A3 250
E 1500 ±20% DE2E3SA152MA3BY02F 8.0 5.0 7.5 0.6 A	A3 250
E 2200 ±20% DE2E3SA222MA3BY02F 9.0 5.0 7.5 0.6 A	A3 250
E 3300 ±20% DE2E3SA332MA3BY02F 12.0 5.0 7.5 0.6 A	A3 200
E 4700 ±20% DE2E3SA472MA3BY02F 13.0 5.0 7.5 0.6 A	A3 200
E 10000 ±20% DE2E3SA103MA3BY02F 17.0 6.0 7.5 0.6 A	A3 100

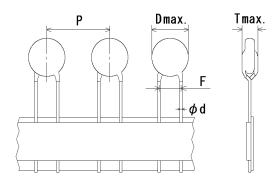
·Vertical crimp short type
(Lead code:J*)



Note) The mark '*' of lead code differ from lead spacing(F) and lead diameter(d).
Please see the following list about details.

									Unit :	mm
T.C.	Сар.	Сар.	Customer Part Number	Murata Part Number		Dimension (mm)			Lead	Pack
1.0.	(pF)	tol.	Customer Fait Number	Widiata Fait Number	D	Т	F	d	code	qty. (pcs)
SL	10	±10%		DE21XSA100KJ3BY02F	7.0	5.0	7.5	0.6	J3	500
SL	15	±10%		DE21XSA150KJ3BY02F	6.0	6.0	7.5	0.6	J3	500
SL	22	±10%		DE21XSA220KJ3BY02F	6.0	5.0	7.5	0.6	J3	500
SL	33	±10%		DE21XSA330KJ3BY02F	7.0	5.0	7.5	0.6	J3	500
SL	47	±10%		DE21XSA470KJ3BY02F	7.0	5.0	7.5	0.6	J3	500
SL	68	±10%		DE21XSA680KJ3BY02F	9.0	5.0	7.5	0.6	J3	500
В	100	±10%		DE2B3SA101KJ3BY02F	6.0	5.0	7.5	0.6	J3	500
В	150	±10%		DE2B3SA151KJ3BY02F	6.0	5.0	7.5	0.6	J3	500
В	220	±10%		DE2B3SA221KJ3BY02F	6.0	6.0	7.5	0.6	J3	500
В	330	±10%		DE2B3SA331KJ3BY02F	6.0	5.0	7.5	0.6	J3	500
В	470	±10%		DE2B3SA471KJ3BY02F	7.0	5.0	7.5	0.6	J3	500
В	680	\pm 10%		DE2B3SA681KJ3BY02F	8.0	5.0	7.5	0.6	J3	500
Е	1000	±20%		DE2E3SA102MJ3BY02F	7.0	5.0	7.5	0.6	J3	500
Е	1500	±20%		DE2E3SA152MJ3BY02F	8.0	5.0	7.5	0.6	J3	500
Е	2200	±20%		DE2E3SA222MJ3BY02F	9.0	5.0	7.5	0.6	J3	500
E	3300	±20%		DE2E3SA332MJ3BY02F	12.0	5.0	7.5	0.6	J3	250
E	4700	±20%		DE2E3SA472MJ3BY02F	13.0	5.0	7.5	0.6	J3	250
Е	10000	±20%		DE2E3SA103MJ3BY02F	17.0	6.0	7.5	0.6	J3	200

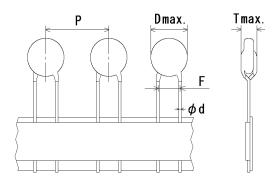
Vartical crimp taping type (Lead code:N*)



Note) The mark '*' of lead code differ from lead spacing(F), lead diameter(d) and pitch of component(P). Please see the following list or taping specification about details.

	Offit. This								111111		
T.C.	Сар.	Сар.	Customer Part Number	Murata Part Number		Dimension (mm)				Lead	Pack
1.0.	(pF)	tol.	Customer Fait Number	IVIUIAIA FAIT INUIIIDEI	D	Т	F	d	Р	code	qty. (pcs)
SL	10	±10%		DE21XSA100KN3AY02F	7.0	5.0	7.5	0.6	15.0	N3	900
SL	15	±10%		DE21XSA150KN3AY02F	6.0	6.0	7.5	0.6	15.0	N3	900
SL	22	±10%		DE21XSA220KN3AY02F	6.0	5.0	7.5	0.6	15.0	N3	900
SL	33	±10%		DE21XSA330KN3AY02F	7.0	5.0	7.5	0.6	15.0	N3	900
SL	47	±10%		DE21XSA470KN3AY02F	7.0	5.0	7.5	0.6	15.0	N3	900
SL	68	±10%		DE21XSA680KN3AY02F	9.0	5.0	7.5	0.6	15.0	N3	900
В	100	±10%		DE2B3SA101KN3AY02F	6.0	5.0	7.5	0.6	15.0	N3	900
В	150	±10%		DE2B3SA151KN3AY02F	6.0	5.0	7.5	0.6	15.0	N3	900
В	220	±10%		DE2B3SA221KN3AY02F	6.0	6.0	7.5	0.6	15.0	N3	900
В	330	±10%		DE2B3SA331KN3AY02F	6.0	5.0	7.5	0.6	15.0	N3	900
В	470	±10%		DE2B3SA471KN3AY02F	7.0	5.0	7.5	0.6	15.0	N3	900
В	680	±10%		DE2B3SA681KN3AY02F	8.0	5.0	7.5	0.6	15.0	N3	900
Е	1000	±20%		DE2E3SA102MN3AY02F	7.0	5.0	7.5	0.6	15.0	N3	900
Е	1500	±20%		DE2E3SA152MN3AY02F	8.0	5.0	7.5	0.6	15.0	N3	900
Е	2200	±20%		DE2E3SA222MN3AY02F	9.0	5.0	7.5	0.6	15.0	N3	900
Ε	3300	±20%		DE2E3SA332MN3AY02F	12.0	5.0	7.5	0.6	15.0	N3	900
Е	4700	±20%		DE2E3SA472MN3AY02F	13.0	5.0	7.5	0.6	15.0	N3	900

Vartical crimp taping type (Lead code:N*)



Note) The mark '*' of lead code differ from lead spacing(F), lead diameter(d) and pitch of component(P). Please see the following list or taping specification about details.

TC	Сар.	Сар.	Customer Part Number	Murata Part Number	Dimension (mm)					Lead	Pack
1.0.	(pF)	tol.	Customer Fait Number	IVIUIAIA FAIT INUIIIDEI	D	Т	F	d	Р	code	qty. (pcs)
Е	10000	±20%		DE2E3SA103MN7AY02F	17.0	6.0	7.5	0.6	30.0	N7	400

			1/6	eference only			
	ecification and test		_	ification	The state of the state of		
No.	Ite			ect on appearance	Test method The capacitor should be inspected by naked eyes		
ı	Appearance and dimensions		form and dime		for visible evidence of defect.		
			Please refer to [Part number list].		Dimensions should be measured with slide calipers.		
2	Marking		To be easily leg	•	The capacitor should be inspected by naked eyes.		
3	Dielectric	Between lead	No failure.		The capacitor should not be damaged when		
	strength wires				AC2600V(r.m.s.) <50/60Hz> is applied between		
		Dody	No failure.		the lead wires for 60 s. First, the terminals of the capacitor should be		
		Body insulation	No failure.		connected together.		
		inoulation.			Then, a metal foil should		
					be closely wrapped around		
					the body of the capacitor Metal About About A foil A mr		
					to the distance of		
					about 3 to 4mm		
					Then, the capacitor should be inserted into a		
					container filled with metal balls of about 1mm		
					diameter. Finally, AC2600V (r.m.s.)<50/60Hz> is		
					applied for 60 s between the capacitor lead wires and metal balls.		
4	Insulation Resista	nce (I.R.)	10000MΩ min.		The insulation resistance should be measured		
		` /			with DC500±50V within 60±5 s of charging.		
					The voltage should be applied to the capacitor		
_					through a resistor of $1M\Omega$.		
5	Capacitance		Within specifie	d tolerance.	The capacitance should be measured at 20°C with		
6	Dissipation Factor	(DE)	2.5% max.		1±0.1kHz and AC1±0.2V(r.m.s.) max The dissipation factor should be measured		
U	יייספועם ווטוז Factol	(0.1.)	2.5/0 IIIdX.		at 20°C with 1±0.1kHz and AC1±0.2V(r.m.s.) max		
7	Tomporation	o oto rioti -	01- 01- 5=	0.1- 4000 /20	, ,		
7	Temperature char	acteristic		0 to -1000 pm/°C	The capacitance measurement should be made at each step specified in Table.		
			(Temp. range : +20 to +85°C) Char. B: Within ±10 %		each step specified in Table.		
			Char. E : Within +20/-55%				
			(Temp. range :				
			, ,	,	· - - - -		
				•	1 2 3 4 5		
				Temp.(°C) 20	0±2 -25±2 20±2 85±2 20±2		
8	Active flammabilit	V	The cheese-clo	oth should not be on	The capacitors should be individually wrapped in at		
			fire.		least one but more than two complete layers of		
					cheese-cloth. The capacitor should be subjected to		
					20 discharges. The interval between successive discharges should be 5 s. The UAc should be		
					maintained for 2min after the last discharge.		
					o.		
					Tr		
					<u> </u>		
					Osciloscope		
					C1,2 : 1μF±10%, C3 : 0.033μF±5% 10kV		
					L1 to L4 : 1.5mH±20% 16A Rod core choke		
					R : $100\Omega\pm2\%$, Ct : 3μ F $\pm5\%$ 10kV UAc : UR $\pm5\%$ UR : Rated working voltage		
					Cx : Capacitor under test		
					F : Fuse, Rated 10A		
					Ut : Voltage applied to Ct		
					Ux		
					5kV 🗍		
					V V		
					time		
					unv		
			<u> </u>				

			Reference only	
No.	Item		Specification	Test method
9	Robustness of terminations	Tensile	Lead wire should not cut off. Capacitor should not be broken.	Fix the body of capacitor, apply a tensile weight gradually to each lead wire in the radial direction of
		Bending		capacitor up to 10N and keep it for 10±1 s. With the termination in its normal position, the
				capacitor is held by its body in such a manner that
				the axis of the termination is vertical; a mass applying a force of 5N is then suspended from the
				end of the termination.
				The body of the capacitor is then inclined, within a period of 2 to 3 s, through an angle of
				about 90° in the vertical plane and then
				returned to its initial position over the same period of time; this operation constitutes one bend.
				One bend immediately followed by a second bend in the apposite direction
10	Vibration	Appearance	No marked defect.	in the opposite direction. The capacitor should be firmly soldered to the
	resistance	Capacitance	Within the specified tolerance. 2.5% max.	supporting lead wire and vibration which is 10 to 55Hz in the vibration frequency range,1.5mm in
		D.F.	2.5% max.	total amplitude, and about 1min in the rate of
				vibration change from 10Hz to 55Hz and back to 10Hz is applied for a total of 6 h; 2 h each in
				3 mutually perpendicular directions.
11	Solderability of lead	ls	Lead wire should be soldered with uniformly coated on the axial	The lead wire of a capacitor should be dipped into a ethanol solution of 25wt% rosin and then into
			direction over 3/4 of the	molten solder for 2±0.5 s. In both cases the depth
			circumferential direction.	of dipping is up to about 1.5 to 2.0mm from the root of lead wires.
				Temp. of solder:
12	Soldering effect	Appearance	No marked defect.	245±5°C Lead Free Solder (Sn-3Ag-0.5Cu) Solder temperature: 350±10°C or 260±5°C
12	(Non-preheat)	Capacitance	Within ±10%	Immersion time : 3.5±0.5 s
		change I.R.	1000M Ω min.	(In case of 260±5°C: 10±1 s) The depth of immersion is up to about
		Dielectric	Per item 3	1.5 to 2.0mm from the root of lead wires.
		strength		Thermal
				insulating 1.5
				to 2.0mm
				Molten solder
				Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the
				AC2000V(r.m.s.) 60s then placed
				at *1room condition for 24±2 h before initial measurements.
				(Do not apply to Char. SL)
				Post-treatment: Capacitor should be stored for 1 to 2 h at *1 room condition.
13	Soldering effect	Appearance	No marked defect.	First the capacitor should be stored at 120+0/-5°C
	(On-preheat)	Capacitance change	Within ±10%	for 60+0/-5 s. Then, as in figure, the lead wires should be
		I.R.	1000MΩ min.	immersed solder of 260+0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.
		Dielectric strength	Per item 3	- Consider
		55.1g		Thermal insulating
				1.5 to 2.0mm
				Molten solder
				Pre-treatment : Capacitor should be stored at
				125±2°C for 1 h, and apply the
				AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h
				before initial measurements.
				(Do not apply to Char. SL) Post-treatment: Capacitor should be stored for 1 to
*1 "-~	om condition" Tomas	ratura: 15 to 25°	C, Relative humidity: 45 to 75%, Atmo	2 h at *1room condition.
10	от сонашон теттре	nature. 10 to 35°	o, neiative numiuity. 40 to 75%, Atmo	ספטוופווט טופססטוופ. טט נט וטטארמ

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			Reference only	
No.	Item	า	Specification	Test method
14	Flame test		The capacitor flame discontinue as follows. Cycle Time 1 to 4 30 s max. 5 60 s max.	The capacitor should be subjected to applied flame for 15 s. and then removed for 15 s until 5 cycle.
15	Passive flammabili	ty	The burning time should not be exceeded the time 30 s. The tissue paper should not ignite.	The capacitor under test should be held in the flame in the position which best promotes burning. Time of exposure to flame is for 30 s. Length of flame: 12±1mm Gas burner: Length 35mm min. Inside Dia. 0.5±0.1mm Outside Dia. 0.9mm max. Gas: Butane gas Purity 95% min. About 8mm Gas burner About 10mm thick board
16	Humidity (Under steady state)	Appearance Capacitance change D.F. I.R. Dielectric strength	No marked defect. Char. SL: Within $\pm 5\%$ Char. B: Within $\pm 10\%$ Char. E: Within $\pm 15\%$ Char. SL: 2.5% max. Char. B, E: 5.0% max. $3000M\Omega$ min. Per item 3	Set the capacitor for 500±12 h at 40±2°C in 90 to 95% relative humidity. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for 1 to 2 h at *1 room condition.
17	Humidity loading	Appearance Capacitance change D.F. I.R. Dielectric strength	No marked defect. Char. SL: Within ±5% Char. B: Within ±10% Char. E: Within ±15% Char. SL: 2.5% max. Char. B, E: 5.0% max. 3000MΩ min. Per item 3	Apply AC440V(r.m.s.) for 500±12 h at 40±2°C in 90 to 95% relative humidity. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for 1 to 2 h at *1room condition.

^{*1 &}quot;room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa

Capacitance change I.R. 3000MΩ min. Dielectric strength Per item 3	Appearance No marked defect. Impulse voltage Each individual capacitor should be subjected a 8kV impulses for three times. Then the capare are applied to life test. 100	Appearance No marked defect. Impulse voltage Each individual capacitor should be subjected to a 8kV impulses for three times. Then the capacitors are applied to life test.	Life Appearance No marked defect. Impulse voltage Each individual capacitor should be subjected to a Rehample R. 3000ΜΩ min.	Appearance Capacitance change I.R. Dielectric strength Appearance Capacitance change D.F. I.R. Dielectric	Parance No marked defect. Within ±20% 3000MΩ min. Per item 3	Impulse voltage Each individual capacitor should be subjected to a 8kV impulses for three times. Then the capacitor are applied to life test. 100 (%) 90 1 1.7 \(\mu \) = 1.67T 1 1 1 1 1 1 1 1 1			
Capacitance change I.R. 3000MΩ min. Dielectric strength Per item 3	Capacitance change I.R. 3000MΩ min.	Capacitance change T.R. 3000MΩ min.	Sepacitance change I.R. 3000MΩ min. Dielectric strength Per item 3	Capacitance change I.R. Dielectric strength Appearance Capacitance change D.F. I.R. Dielectric	arance No marked defect. The strict of the	Each individual capacitor should be subjected to a 8kV impulses for three times. Then the capacitor are applied to life test. 100 (%) 100 (%			
Change I.R. 3000MΩ min.	Change I.R. 3000MΩ min.	change I.R. Jicelectric strength Per item 3 The capacitors are placed in a circulating air oven for a period of 1000 h. The air in the oven is maintained at a temperature of 125+2/-0 °C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a AC680V(r.m.s.) < 50/60Hz- alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *¹room condition for 24±2 h before initial measurements. (Do not apply to Char. St.) Post-treatment: Capacitor should be stored for 24±2 h at *¹room condition for 24±2 h before initial measurements. (Do not apply to Char. St.) Post-treatment : Capacitor should be stored for 24±2 h at *¹room condition for 24±2 h before initial measurements. (Do not apply to Char. St.) Post-treatment : Capacitor should be stored for 24±2 h at *¹room condition. The capacitor should be stored for 24±2 h at *¹room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. Char. B. Within ±20% Char. B. Within ±20% Char. B. Within ±20% Char. B. Within ±20% D.F. Char. St. Vithin ±20% Char. B. Within ±	Section Per item 3 Per item 4 Per item 5 Per item 6 Per item 7 Per item 6 Per item 6 Per item 7 Per item 7 Per item 7 Per item 7 Per item 8 Per item 8 Per item 8 Per item 9 Per item 1 Per item 9 Per item 1 Per item 2 Per item 1 Per item 2 Per item 1 Per item 2 Per item 2 Per item 2 Per item 3 Per item 4 Per i	e and Appearance Capacitance change D.F. I.R. Dielectric strength	3000MΩ min. Per item 3 Per item 4 P	a 8kV impulses for three times. Then the capacitor are applied to life test. 100 (%) Front time (T1) = 1.7 \(\mu \) s=1.67T Time to half-value (T2) = 50 \(\mu \) s Time to half-value (T2) = 50 \(\mu \) s Time to half-value (T2) = 50 \(\mu \) s Time to half-value (T2) = 50 \(\mu \) s Time to half-value (T2) = 50 \(\mu \) s Time to half-value (T2) = 50 \(\mu \) s Time to half-value (T2) = 50 \(\mu \) s Time to half-value (T2) = 50 \(\mu \) s Time to half-value (T2) = 50 \(\mu \) s The air in the oven is maintained at a temperature of 125+2/-0 °C, and relative humidity of 50% max Throughout the test, the capacitors are subjected to a AC680V(r.m.s.) c50/60Hz> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment : Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) c60s then place at *1 room condition for 24±2 h before initial measurements. (Do not apply to Char. SL.) Post-treatment : Capacitor should be stored for 24±2 h at *1 room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. ±10%			
I.R.	I.R. 3000MΩ min. Dielectric strength Per item 3 Front time (T1) = 1.7 μ = 1. The capacitors are placed in a circulating air for a period of 1000 h. The air in the oven is maintained at a tempera of 125±2/-0 for 1 h, and apply to a AC880V(r.m.s.) < 5006Hzs alternating vo of mains frequency, except that once each hot voltage is increased to AC1000V(r.m.s.) for 0 Pre-treatment : Capacitors should be stored a 125±2/-0 for 1 h, and apply to Ac2000V(r.m.s.) for 0 Pre-treatment : Capacitor should be stored a 125±2/-0 for 1 h, and apply to Char. St.) Post-treatment : Capacitor should be stored a 125±2/-0 for initial measurements (Do not apply to Char. St.) Post-treatment : Capacitor should be stored for 24±2 h at "froom condition.	I.R. 3000MΩ min. Dielectric strength Per item 3 The capacitors are placed in a circulating air oven for a period of 1000 h. The air in the oven is maintained at a temperature of 125±20 °C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a AC860V(r.m.s.) 60% then placed of a AC860V(r.m.s.) 60% then placed at *1°come condition for 24±2 h at *1°come condition for 24±2 h at *1°come condition. Persture and ersion cycle Capacitance Change Char. St.: Within ±10% Char. E.: Within ±10% Char. E.: Within ±10% Char. B.: Within ±10	I.R.	e and Appearance change D.F. I.R. Dielectric strength	3000MΩ min. Per item 3 Per item 4 P	are applied to life test. 100			
Dielectric strength Per item 3 Per item 3 The capacitors are placed in a circulatifor a period of 1000 h. The air in the oven is maintained at at of 125+22-0 °C, and relative humidity of Throughout the test, the capacitors are to a AC680V(r.m.s.)-60/60Hz> alterna of mains frequency, expet that once evoltage is increased to AC1000V(r.m.s.) 60s at "froom condition for before initial measure (Do not apply to Char Ac2000V(r.m.s.) 60s at "loom condition for before initial measure (Do not apply to Char B. Within ±10% Char. B. Within ±20% Char. B. Within ±20% Char. B. E. 5.0% max. Char. B, E. 5.0% max. I.R. 3000MΩ min. Dielectric strength Per item 3 Prost-treatment: Capacitor should be subjected to 5 temperature cycles, then consecutively immersion cycles. Capacitance Char. B. E. 5.0% max. Char. B, E. 5.0% max. Char. B. E. Within ±20% Char. E. Step Temperature cycles. Capacitance Char. S. E. Char. S. E. Within ±6% change Char. E. Within ±6% Char. B. Within ±10% Char. E. Within ±20% Char. E	Dielectric strength Per item 3 Front time (T1) = 1.7 μ s=1.	Dielectric strength Per item 3 Dielectric strength Per item 3 Per item 3 The capacitors are placed in a circulating air oven for a period of 1000 h. The air in the oven is maintained at a temperature of 125+2/-0 °C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a AC680V(r.m.s.) colored-red red red red red red red red red red	Delectric strength Per item 3 Delectric strength Per item 3 Delectric strength The capacitors are placed in a circulating air over for a period of 1000 h. The air in the oven is maintained at a temperatur of 125±27-0°C, and relative humidity of 50% max Throughout the test, the act 1000 V(r.m.s.) to 50% the place at "1000 mains frequency, except that once each hour the voltage is increased to AC680V(r.m.s.) 60% then place at "1000 m condition for 2±2.2 h before initial measurements. (Do not apply to Char. St. 12 5% max. Char. B. : Within ±20% Char. B. : Within ±20% Char. B. : Within ±20% Char. St. 12 5% max. Char. B. E. : 5.0% max. 1.R. 3000MΩ min. Dielectric strength Dielectric strengt	Dielectric strength Appearance Capacitance change D.F. I.R. Dielectric	Per item 3	The capacitors are placed in a circulating air over for a period of 1000 h. The air in the oven is maintained at a temperatur of 125+2/-0 °C, and relative humidity of 50% may Throughout the test, the capacitors are subjected to a AC680V(r.m.s.)<50/60Hz> alternating voltage of mains frequency, except that once each hour troltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1 room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for 24±2 h at *1 room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. **Temperature cycles** Temperature cyc			
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Dielectric	earance No marked defect. Incitance ge Char. SL: Within ±5% Char. B: Within ±10% Char. E: Within ±20% Char. SL: 2.5% max. Char. B, E: 5.0% max. 3000MΩ min. Per item 3	Time to half-value (T2) = 50 µs Time to half-value (T2) = 50 µs The capacitors are placed in a circulating air over for a period of 1000 h. The air in the oven is maintained at a temperatur of 125+2/-0 °C, and relative humidity of 50% may Throughout the test, the capacitors are subjected to a AC680V(r.m.s.)<50/60Hz> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1 room condition for 24±2 h before initial measurements. (Do not apply to Char. SL.) Post-treatment: Capacitor should be stored for 24±2 h at *1 room condition. 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E: Within ±20% Char. E: Within ±20% Char. E: Within ±20% Char. B, E: 5.0% max. I.R. Dielectric strength Tor a period of 1000 h. The air in the oven is maintained at a temperof 125+2/-0 °C, and relative humidity of 50% Throughout the test, the capacitors are subject to a AC680V(r.m.s.) <50/60Hz> alternating vo of mains frequency, except that once each hot voltage is increased to AC1000V(r.m.s.) for 0. Pre-treatment: Capacitor should be stored a 125±2°C for 1 h, and apply t AC2000V(r.m.s.) 60s then pl at *1*room condition for 24±2 before initial measurements (Do not apply to Char. SL). Post-treatment: Capacitor should be stored for 24±2 hat *1*room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. Char. B: Within ±20% Char. E: Within ±20% D.F. Char. SL: 2.5% max. Char. B, E: 5.0% max. 1.R. 3000MΩ min. Dielectric strength The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. 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Post-treatment: Capacitor should be stored for 24±2 h at *1room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. Char. B: Within ±10% char. SL: 2.5% max. Char. B, E: 5.0% max. I.R. 3000MΩ min. I.R. 3000MΩ min. Dielectric strength Throughout the test, the capacitors are table to AC1000V(r.m.s.) for 0. Throughout the test, the capacitor should be stored at 1 -40+0/-3 30 min 3 +125+3/-0 30 min 4 Room temp. 3 min Cycle time:500 cycle time:	The air in the oven is maintained at a temperature of 125+2/0 °C, and relative humidity of 50% max. Throughout the test, the capacitors are subjected to a AC680V(r.m.s.)<50/60Hz> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at "froom condition for 24±2 h before initial measurements. (Do not apply to Char. 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Throughout the test, the capacitor should be stored for 25±2°C for 1 h, and apply the AC2000V(r.m.s.) for 0.1 s. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. Throughout the test, the capacitor should be stored for 25±2°C for 1 h, and apply the AC2000V(r.m.s.) for 15 min and 15 min	The air in the oven is maintained at a temperature of 125+2/-0°C, and relative humidity of 50% max Throughout the test, the capacitors are subjected to a AC680V(r.m.s.)<0000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at "iroom condition for 24±2 h before initial measurements. (Do not apply to Char. St.) Post-treatment: Capacitor should be stored for 24±2 h at **Iroom condition. Appearance No marked defect. (Danct apply to Char. St.) Post-treatment: Capacitor should be stored for 24±2 h at **Iroom condition. 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SL) Post-treatment: Capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. Throughout for 1 hand apply to Char.	Throughout the test, the capacitors are subjected to a AC680V(r.m.s.) <50/60Hz> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for 24±2 h at *1room condition for 24±2 h at *1room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. Char. B. : Within ±5% change Char. B. : Within ±5% change Char. B. : 5.0% max. Throughout the text, that voltage is increased to AC1000V(r.m.s.) 60s then placed at *10cm char. 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Dielectric strength Per item 3 Throughout the test, the capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. CFENGERAL CAPACITOR (**C) Time 1	Capacitance change D.F. I.R. Dielectric	Char. SL : Within ±5%	Throughout the test, the capacitors are subjected to a AC680V(r.m.s.)<50/60Hz> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1 room condition for 24±2 h at *1 room condition for 24±2 h at *1 room condition. The capacitor should be stored for 24±2 h at *1 room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. ±5% temperature cycles, then consecutively to 2 immersion cycles. **Temperature cycles** **Temperature cycles**			
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Dielectric strength Per item 3 Cycle time **Immrersion cycle>** Step Temperature(°C) Time 1	to a AČ680V(r.m.s.) <a "1room="" ("c)="" (do="" +125+3="" +65+5="" -0="" -3="" 0±3="" 1="" 125±2°c="" 15="" 2="" 2.5%="" 24±2="" 3="" 30="" 4-04-0="" 5.5%="" 60s="" ac2000v(r.m.s.)="" and="" apply="" at="" at<="" b.:="" be="" before="" capacitance="" capacitor="" change="" char.="" chimersion="" condition="" cycle="" cycles="" cycles.="" d.f.="" deritem="" dielectric="" e.:="" for="" h="" h,="" href="https://docs.org/linear.com/linear.co</td><td>to a AČ680V(r.m.s.)<50/60H2> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1 froom condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for 24±2 h at *1 froom condition. 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The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. **20%** **max.** **max.** **max.** **max.** **ax.** **1</td>	Capacitance change D.F. I.R. Dielectric	Char. SL : Within ±5%	to a AC680V(r.m.s.)<50/60Hz> alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1 room condition for 24±2 h at *1 room condition for 24±2 h at *1 room condition. Post-treatment: Capacitor should be stored for 24±2 h at *1 room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. **20%** **max.** **max.** **max.** **max.** **ax.** **1					
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Appearance No marked defect.	Capacitance change D.F. I.R. Dielectric	Char. SL : Within ±5%	AC2000V(r.m.s.) 60s then place at *1 room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for 24±2 h at *1 room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. ±20% max. max. Temperature cycle> Temperature cycle> 1			
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before initial measure (Do not apply to Cha Post-treatment : Capacitor should be stighted to Stemperature and Mersion cycle Appearance No marked defect. Capacitance Char. SL: Within ±5% Char. B: Within ±10% Char. E: Within ±20% D.F. Char. SL: 2.5% max. Char. B, E: 5.0% max. I.R. 3000MΩ min. Dielectric strength Defore initial measure (Do not apply to Cha Post-treatment : Capacitor should be subjected to 5 temperature cycles, then consecutively immersion cycles. **Temperature cycles** **Temperature	Defore initial measurements (Do not apply to Char. SL.) Post-treatment : Capacitor should be stored for 24±2 h at *¹ room condition.	before initial measurements. (Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2 h at *¹room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. D.F. Char. SL : Within ±20% D.F. Char. SL : 2.5% max. Char. B, E : 5.0% max. I.R. 3000MΩ min. Dielectric strength Dielectric	Defore initial measurements. ((Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2 h at *¹room condition.	Capacitance change D.F. I.R. Dielectric	Char. SL : Within ±5%	before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for 24±2 h at *¹room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. ±20% max. max. Temperature cycle Temperature(°C) Time 1 -40+0/-3 30 min 2 Room temp. 3 min 3 +125+3/-0 30 min 4 Room temp. 3 min Cycle time:500 cycle			
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Appearance No marked defect. The capacitor should be subjected to 5 temperature cycles, then consecutively immersion cycles	Temperature and immersion cycle Appearance No marked defect. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles.	Appearance Appearance Char. SL : Within ±5% Char. B : Within ±20%	Temperature and immersion cycle Appearance Capacitance change Char. St. : Within ±5% Char. E : Within ±20% Char. E : Within ±20% Char. B, E : 5.0% max. 1.R. 3000MΩ min. 2 Room temp. 3 min. 3 min. 2 Room temp. 3 min. 3 min. 3 min. 3 min. 4 Room temp. 3 min. 3 min	Capacitance change D.F. I.R. Dielectric	Char. SL : Within ±5%	24±2 h at *1 room condition. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. ±20% max. max. 1			
Appearance No marked defect. Capacitance change Char. SL : Within ±5% Char. B : Within ±20% D.F. Char. SL : 2.5% max. Char. B, E : 5.0% max. I.R. 3000MΩ min. Dielectric strength Per item 3 Step Temperature (°C) Time Tempe	Appearance No marked defect. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles.	Appearance No marked defect. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles.	Temperature and immersion cycle Appearance Capacitance change Char. SL : Within ±5% Char. B : Within ±20% Char. B : Within ±20% Char. B : S.0% max. Char. B, E : 5.0% ma	Capacitance change D.F. I.R. Dielectric	Char. SL : Within ±5%	t. The capacitor should be subjected to 500 temperature cycles, then consecutively to 2 immersion cycles. ±20% max.			
Capacitance change Char. SL : Within ±5% Char. B : Within ±10% Char. E : Within ±20% D.F. Char. SL : 2.5% max. Char. B, E : 5.0% max. I.R. 3000MΩ min. Dielectric strength Per item 3 Cycle time Cycle time Cycle time Temperature cycles, then consecutively immersion cycles.	Immersion cycle Capacitance change Char. SL : Within ±5% Char. B : Within ±10% Char. E : Within ±20% temperature cycles, then consecutively to 2 immersion cycles. D.F. Char. SL : 2.5% max. Char. B, E : 5.0% max. 1 -40+0/-3 30 min. I.R. 3000MΩ min. 2 Room temp. 3 min. Dielectric strength Per item 3 4 Room temp. 3 min. Cycle time:500 cycles Step Temperature (°C) Time Immers wate 1 +65+5/-0 15 min Clear 1 +65+5/-0 15 min Clear 2 Room temp. 3 min Cycle time:500 cycles 3 +125+3/-0 30 min Cycle time:500 cycles 4 Room temp. 3 min Cycle time:500 cycles 2 Room temp. 3 min Cycle time:500 cycles 3 +125+3/-0 30 min Cycle time:500 cycles 4 Room temp. 3 min Cycle time:500 cycles 2 Room temp. 3 min Cycle time:500 cycles 3 Room temp. 3 min Cycle time:500 cycles 4 Room temp. 3 min Cycle time:500 cycles 5 Room temp. 3 min Cycle time:500 cycles 6 Room temp. 3 min Cycle time:500 cycles 7 Room temp. 3 min Cycle time:500 cycles 8 Room temp. 3 min Cycle time:500 cycles 1 Room temp. 3 min Cycle time:500 cycles 2 Room temp. 3 min Cy	Capacitance change Char. SL : Within ±5% Char. B : Within ±10% Char. E : Within ±20% D.F. Char. SL : 2.5% max. Char. B, E : 5.0% max. I.R. 3000MΩ min. Dielectric strength Per item 3 Cycle time:500 cycles	Immersion cycle Capacitance change Char. SL : Within ±5% Char. B : Within ±20%	Capacitance change D.F. I.R. Dielectric	Char. SL : Within ±5%	#5% #10% #20% max. max. max. 1			
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Char. B, E : 5.0% max. 1	Char. B, E : 5.0% max. 1.R. 3000MΩ min. 2 Room temp. 3 min 2. Room temp. 3 min 3 min 3. +125+3/-0 3 min 4. Room temp. 3 min Cycle time:500 cycle> Temperature(°C) Time Immers wate 1. +65+5/-0 15 min Clear 1. +65+5/-0 15 min Clear 2. Room temp. 3 min 3. +125+3/-0 30 min 4. Room temp. 3 min 5. Temperature(°C) Time Immers wate Salt	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Char. B, E: 5.0% max. I.R. 3000MΩ min. Dielectric strength Per item 3 Cycle time:500 cycle Immersion cycle> Step Temperature(°C) Time Immersion water 1 +65+5/-0 15 min Clean water 2 0±3 15 min Salt water Cycle time:2 cycles Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for	Dielectric	Char. B, E : 5.0% max. 3000ΜΩ min. cetric Per item 3	max. 1 -40+0/-3 30 min 2 Room temp. 3 min 3 +125+3/-0 30 min 4 Room temp. 3 min Cycle time:500 cycle			
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Dielectric strength	Dielectric strength	Dielectric strength	Dielectric strength Per item 3 3	Dielectric	ectric Per item 3	3 +125+3/-0 30 min 4 Room temp. 3 min Cycle time:500 cycle			
Strength Cycle time Step Temperature(°C) Time Temperature(°C)	Strength Cycle time:500 cycles Temperature(°C) Time Temperature(°C) Time Cycle 1	Strength 4 Room temp. 3 min Cycle time:500 cycles Immersion cycle> Temperature(°C) Time water Immersion water 1 +65+5/-0 15 min water Clean water 2 0±3 15 min water Salt water	Strength Cycle time:500 cycle Step Temperature(°C) Time Immersion water	strength	gth	4 Room temp. 3 min Cycle time:500 cycle			
Cycle time Step Temperature(°C) Time Time	Cycle time:500 c Commercial Cycle time:500 c Commercial Cycle	Cycle time:500 cycles Step Temperature(°C) Time Immersion water	Cycle time:500 cycle Step Temperature(°C) Time Immersion water			Cycle time:500 cycle			
Step Temperature(°C) Time I 1 +65+5/-0 15 min 2 0±3 15 min Cycle time Pre-treatment : Capacitor should be si 125±2°C for 1 h, and AC2000V(r.m.s.) 60s	Step Temperature(°C) Time Wate 1 +65+5/-0 15 min Clear wate	Step Temperature(°C) Time Immersion water 1	Attention cycle> Step Temperature(°C) Time Immersion water 1 +65+5/-0 15 min Clean water 2 0±3 15 min Salt water Cycle time:2 cycles Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for						
Step Temperature(°C) Time I 1 +65+5/-0 15 min 2 0±3 15 min Cycle time Pre-treatment : Capacitor should be si 125±2°C for 1 h, and AC2000V(r.m.s.) 60s	Step Temperature(°C) Time Wate 1 +65+5/-0 15 min Clear wate	Step Temperature(°C) Time Immersion water 1	Attention cycle> Step Temperature(°C) Time Immersion water 1 +65+5/-0 15 min Clean water 2 0±3 15 min Salt water Cycle time:2 cycles Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for						
Step Temperature(°C) Time 1 +65+5/-0 15 min 2 0±3 15 min Cycle time Pre-treatment : Capacitor should be single to the single	Step Temperature(°C) Time wate 1 +65+5/-0 15 min Clear wate	Step Temperature(°C) Time water	Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			< Immersion cycle > Immersion			
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2 0±3 15 min Cycle time Pre-treatment : Capacitor should be st 125±2°C for 1 h, and AC2000V(r.m.s.) 60s	1 +65+5/-0 15 min wate	2 0±3 15 min water Salt water	Cycle time: 2 cycles Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			Clean			
Cycle time Pre-treatment : Capacitor should be si 125±2°C for 1 h, and AC2000V(r.m.s.) 60s	Salt	2 0±3 15 min Salt water	2 0±3 15 min Salt water Cycle time:2 cycles Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			1 1 +65+5/-0 15 min			
Cycle time Pre-treatment : Capacitor should be si 125±2°C for 1 h, and AC2000V(r.m.s.) 60s	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	L J water	Cycle time:2 cycles Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for	•		Salt			
Pre-treatment: Capacitor should be single 125±2°C for 1 h, and AC2000V(r.m.s.) 60s		Cycle time:2 cycles	Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			11 2 1 0+3 1 15 min 1			
Pre-treatment: Capacitor should be single 125±2°C for 1 h, and AC2000V(r.m.s.) 60s		Cycle time:2 cycles	Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for						
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125±2°C for 1 h, and AC2000V(r.m.s.) 60s			125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			Cycle time:2 cycles			
AC2000V(r.m.s.) 60s		Dra trantment : Conneiter chould be stored at	AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			, , ,			
			at *¹room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			Pre-treatment : Capacitor should be stored at			
at *¹room condition to		125±2°C for 1 h, and apply the	before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for	1		Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the			
		125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed	(Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place			
		125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h	Post-treatment : Capacitor should be stored for			Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1 room condition for 24±2 h			
	I DO NOL ADDIV TO CHALL SEL	125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements.				Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements.			
		125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL)	24±2 II at 100III condition.			Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL)			
74171181 111111113111	Post-treatment : Capacitor should be stored	125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for	"room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa			Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			
	125±2°C for 1 h, and apply t	Dra trootmant : Canacitar should be stored at	at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			Cycle time:2 cycles			
		Fie-treatment . Capacitor should be stored at	at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			, , ,			
at *!room condition to		125±2°C for 1 h, and apply the	before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for	i		Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the			
		125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed	(Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place			
		125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h	Post-treatment : Capacitor should be stored for			Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1 room condition for 24±2 h			
	(Do not apply to Char. SL)	125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1 room condition for 24±2 h before initial measurements.				Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements.			
		125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL)	24±2 h at *1room condition.			Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL)			
24+2 h at *1 room cond	Post-treatment : Capacitor should be stored	125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for				Pre-treatment: Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then place at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment: Capacitor should be stored for			
			llun aun ann alitinull Te						

6.Packing specification

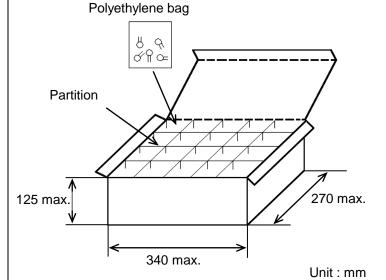
•Bulk type (Packing style code : B)

*1 *2 The number of packing = Packing quantity \times n

The size of packing case and packing way

*1 : Please refer to [Part number list].

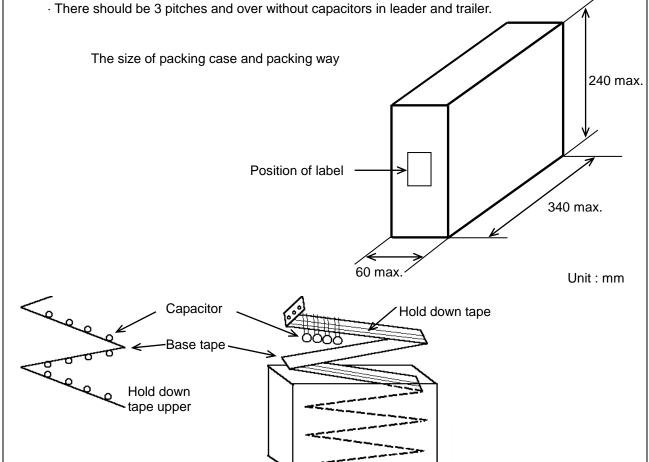
*2 : Standard n = 20 (bag)



Note)

The outer package and the number of outer packing be changed by the order getting amount.

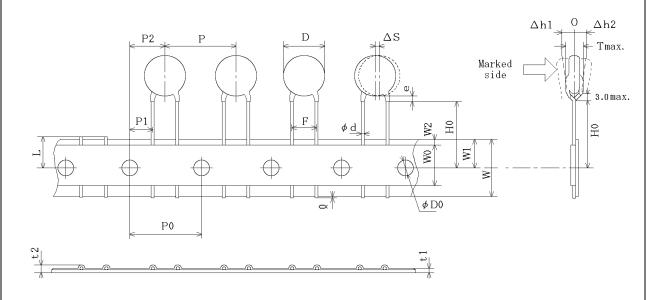
- •Ammo pack taping type (Packing style code : A)
 - · The tape with capacitors is packed zigzag into a case.
 - · When body of the capacitor is piled on other body under it.



7. Taping specification

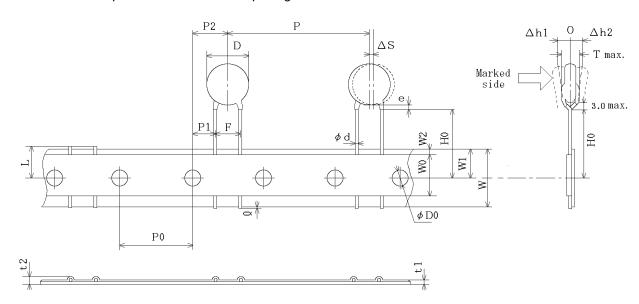
7-1. Dimension of capacitors on tape

Vertical crimp taping type < Lead code : N3 > Pitch of component 15.0mm / Lead spacing 7.5mm



		Unit . r			
Item		Dimensions	Remarks		
Pitch of component	Р	15.0±2.0			
Pitch of sprocket hole	P0	15.0±0.3			
Lead spacing	F	7.5±1.0			
Length from hole center to component center	P2	7.5±1.5	B i ii af na ana a dina at a		
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction		
Body diameter	D	Please refer to [Part number list].		
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend.		
Carrier tape width	W	18.0±0.5			
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction		
Lead distance between reference and bottom	II0	40.0+2.0			
planes	H0	$18.0\pm_0^{2.0}$			
Protrusion length	Q	+0.5~-1.0			
Diameter of sprocket hole	φD0	4.0±0.1			
Lead diameter	φd	0.60±0.05			
Total tape thickness	t1	0.6±0.3			
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.		
Deviation across tape, front	∆h1				
Deviation across tape, rear	∆h2	2.0 max.			
Portion to cut in case of defect	L	11.0± _{1.0}			
Hold down tape width	Wo	11.5 min.			
Hold down tape position	W2	1.5±1.5			
Coating extension on lead	е	Up to the end of crimp			
Body thickness	Т	Please refer to [Part number list].			
Hold down tape position Coating extension on lead	W2 e	1.5±1.5 Up to the end of	•		

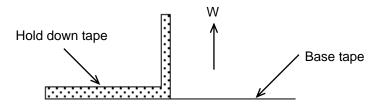
Vertical crimp taping type < Lead code : N7 > Pitch of component 30.0mm /Lead spacing 7.5mm



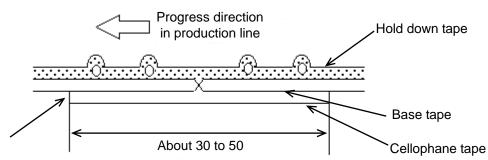
Item		Dimensions	Remarks
Pitch of component		30.0±2.0	
Pitch of sprocket hole	P0	15.0±0.3	
Lead spacing	F	7.5±1.0	
Length from hole center to component center	P2	7.5±1.5	
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction
Body diameter	D	Please refer to [Part number list].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend.
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	H0	18.0± ^{2.0}	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	2.0	
Deviation across tape, rear	∆h2	2.0 max.	
Portion to cut in case of defect	L	11.0± ⁰ _{1.0}	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	Up to the end of crimp	
Body thickness	Т	Please refer to [Part number list].	

7-2. Splicing way of tape

1) Adhesive force of tape is over 3N at test condition as below.



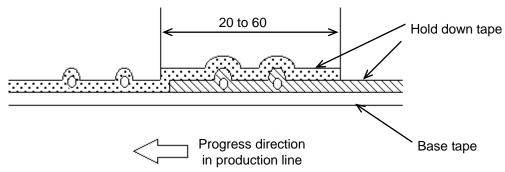
- 2) Splicing of tape
 - a) When base tape is spliced
 - •Base tape should be spliced by cellophane tape. (Total tape thickness should be less than 1.05mm.)



No lifting for the direction of progressing

Unit: mm

- b) When hold down tape is spliced
 - •Hold down tape should be spliced with overlapping. (Total tape thickness should be less than 1.05mm.)



- c) When both tape are spliced
 - •Base tape and hold down tape should be spliced with splicing tape.
- 3) Missing components
 - •There should be no consecutive missing of more than three components.
 - •The number of missing components should be not more than 0.5% of total components that should be present in a Ammo pack.

EU RoHS and Halogen Free

This products of the following crresponds to EU RoHS and Halogen Free

(1) RoHS

EU RoHs 2011/65/EC compliance

maximum concentration values tolerated by weight in homogeneous materials

- •1000 ppm maximum Lead
- •1000 ppm maximum Mercury
- •100 ppm maximum Cadmium
- •1000 ppm maximum Hexavalent chromium
- •1000 ppm maximum Polybrominated biphenyls (PBB)
- •1000 ppm maximum Polybrominated diphenyl ethers (PBDE)

(2) Halogen-Free

The International Electrochemical Commission's (IEC) Definition of Halogen-Free (IEC 61249-2-21) compliance

- •900 ppm maximum chlorine
- •900 ppm maximum bromine
- •1500 ppm maximum total chlorine and bromine

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46KI2680JH01M 46KI315000M2K 46KI3150CKM2K 46KI3150CKM2M 46KI3150NDM2M 46KI3220JLM1M 46KN3150JH01K

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YV500103Z060B20X5P MKPX2R-1/400/10P27 YU0AH222M090DAMD0B LS1808N102K302NX080TM R463F210000N0K

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