# muRata

**Reference Specification** 

DEB Series Lead Type Disc Ceramic Capacitors of Class 2 for General Purpose

Product specifications in this catalog are as of Dec. 2017, and are subject to change or obsolescence without notice.

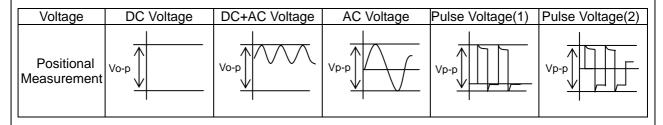
Please consult the approval sheet before ordering. Please read rating and Cautions first.

# 

# 1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.

When DC-rated capacitors are to be used in input circuits from commercial power source (AC filter), be sure to use Safety Recognized Capacitors because various regulations on withstand voltage or impulse withstand established for each equipment should be taken into considerations.



# 2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the selfgenerated heat due to dielectric-loss. The allowable frequency should be in less than 300kHz in sine wave. Applied voltage should be the load such as self-generated heat is within 20 °C <u>on the condition of</u> <u>atmosphere temperature 25 °C.</u> When measuring, use a thermocouple of small thermal capacity-K of  $\phi$ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. While, in case of non-sine wave which include a harmonic frequency, please contact our sales representatives or product engineers.

## 3. FAIL-SAFE

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

## 4. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

## 5. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron tip : 400 °C max.

Soldering iron wattage : 50W max.

Soldering time : 3.5 s max.

## 6. BONDING, RESIN MOLDING AND COATING

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive, molding resin or coating may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

#### 7. TREATMENT AFTER BONDING, RESIN MOLDING AND COATING

When the outer coating is hot (over 100  $^{\circ}$ C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

## 8. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors

in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 °C and 15 to 85%. Use capacitors within 6 months after delivered. Check the solderability after 6 months or more.

# 9. LIMITATION OF APPLICATIONS

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. Power plant control equipment
- 5. Medical equipment
- 6. Transportation equipment (vehicles, trains, ships, etc.)
- 7. Traffic signal equipment
- 8. Disaster prevention / crime prevention equipment
- 9. Data-processing equipment exerting influence on public
- 10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

# NOTICE

# 1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity : Output of 20 watts per liter or less.

Rinsing time : 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

# 2. CAPACITANCE CHANGE OF CAPACITORS

- Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

- Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit. Please contact us if you need a detail information.

# 

- 1.Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

Γ

Class 2 of DE Do not use the for electric veh	B series used f ese products in	or Genera any autor	al Electric econotive powe				ratings and battery chargers
2. Rating 2-1. Operating -25							
2-2. Part numb	er configuratio	n					
ex.) <u>DEB</u> Series	<u>B3</u> Temperature characteristic	<u>3D</u> Rated voltage	<u>332</u> Capacitan	ce Capacitance tolerance	<u>A3</u> Lead code	B Packing style code	Individual specification
•Tempe	rature characte	eristic					
		ode	Te	mperature chara	cteristic		
		33		<u> </u>			
		<u>-3</u>		<u> </u>			
		-	specification	on [ Specificatio	n and te		1
			specification			st methous	].
Rated	voltage						
• Nated		ode		Rated voltage	1e		
		BD		DC2kV	,0		
		3F		DC3.15kV			
ex.	e first two digits ) In case of 33 33	2. ×10 <sup>2</sup> = 33		ures ; the last dio	git denot	es the multip	blier of 10 in pF.
-	itance toleranc ase refer to [ F		er list ].				
• Lead o							
		ode	1/0-1	Lead style	<b></b>		
		<b>∖</b> ∗ C∗		cal crimp long ty	pe		
		<u>_*</u> 3*		ght long type cal crimp short ty	/De		
		<u>⊃*</u> )∗		ght short type	, PC		
		V*		cal crimp taping	type		
		• ⊃∗		ght taping type	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
	* Please refer	to [Part					
Sol	der coated cop	oper wire i	s applied fo	r termination.			

Packing style code

Code	Packing type
В	Bulk type
A	Ammo pack taping type

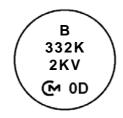
• Individual specification

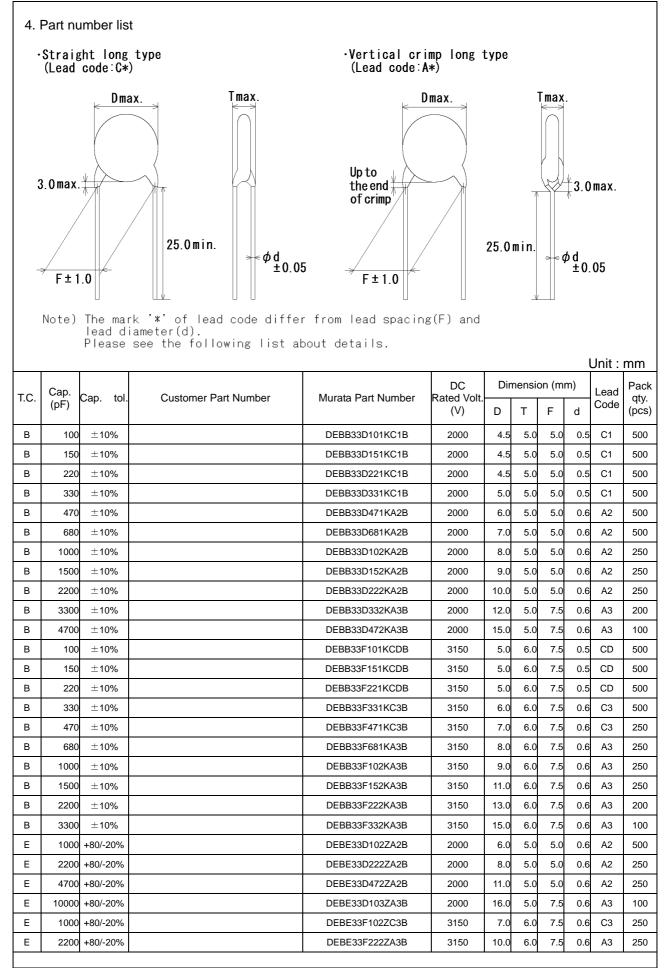
In case part number cannot be identified without 'individual specification', it is added at the end of part number.

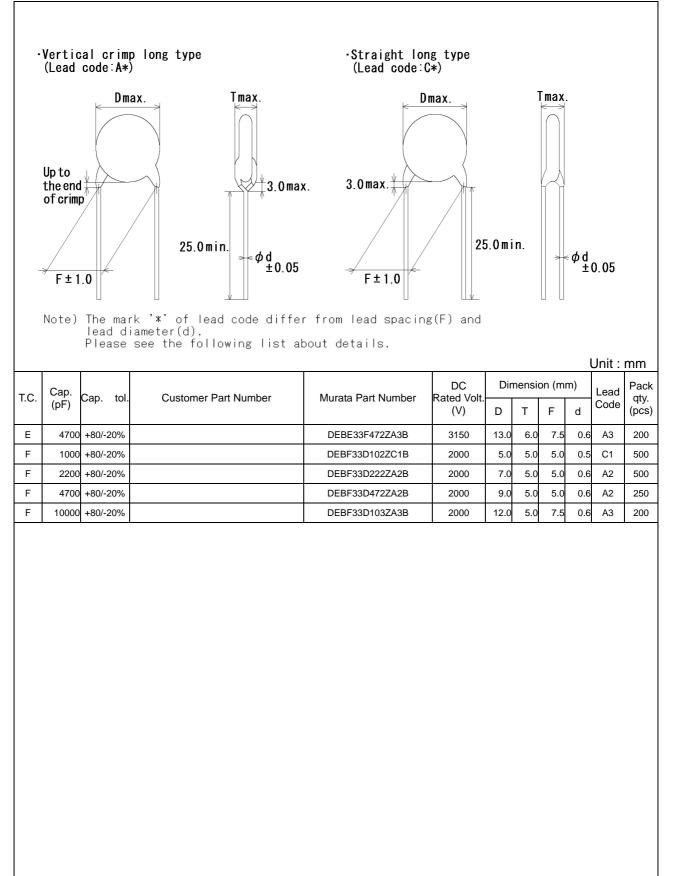
#### 3. Marking

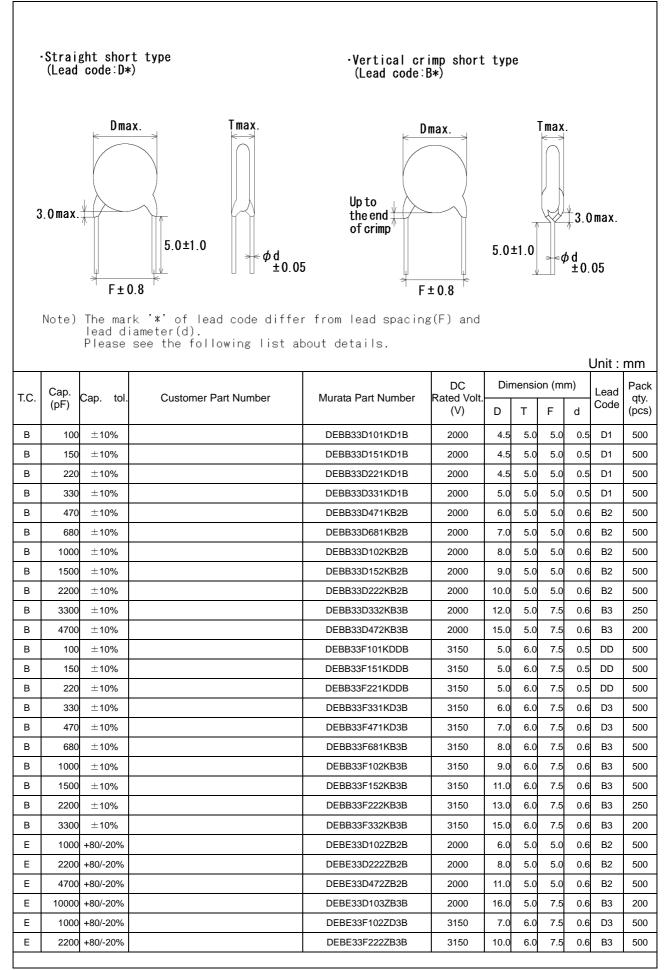
Temperature characteristic	Letter code Identified by code for char. B or char. E.
	(Omitted for maximum body diameter $\phi$ 9mm and under )
Nominal capacitance	: 3 digit system
Capacitance tolerance	: Code(Omitted for maximum body diameter $\phi$ 6mm and under)
Rated voltage	: Letter code(In case of DC3.15kV, marked with 3KV)
Company name code	: Abbreviation 🕞
	(Omitted for maximum body diameter $\phi$ 9mm and under)
Manufacturing year	: Letter code(The last digit of A.D. year.)
	(Omitted for maximum body diameter $\phi$ 5mm and under)
Manufacturing month	: Code(Omitted for maximum body diameter $\phi$ 5mm and under) (Feb./Mar. $\rightarrow 2$ Aug./Sep. $\rightarrow 8$ Apr./May $\rightarrow 4$ Oct./Nov. $\rightarrow 0$ Jun./Jul. $\rightarrow 6$ Dec./Jan. $\rightarrow D$

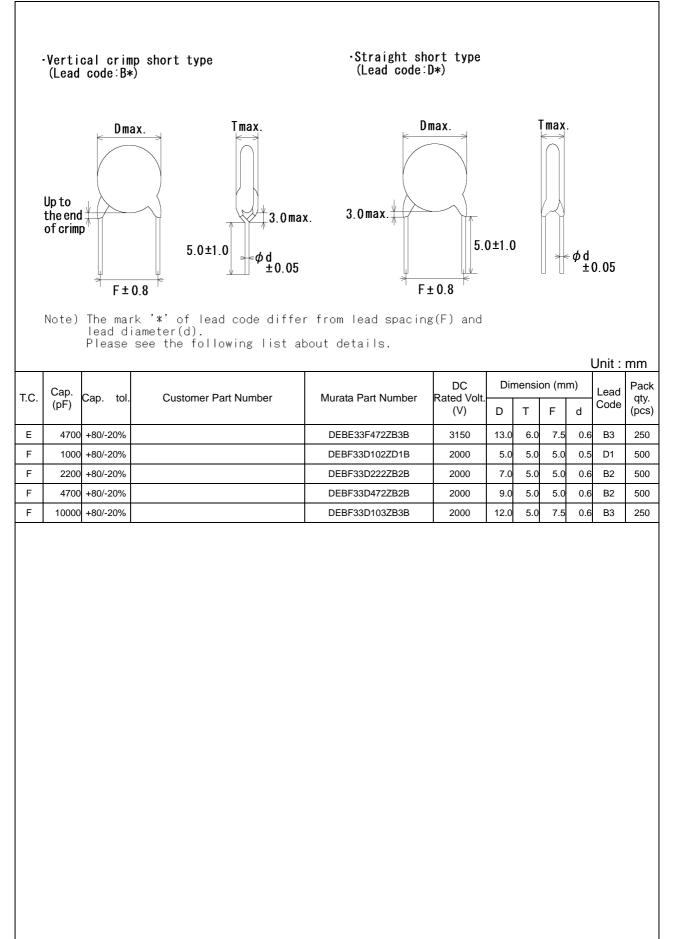
(Example)

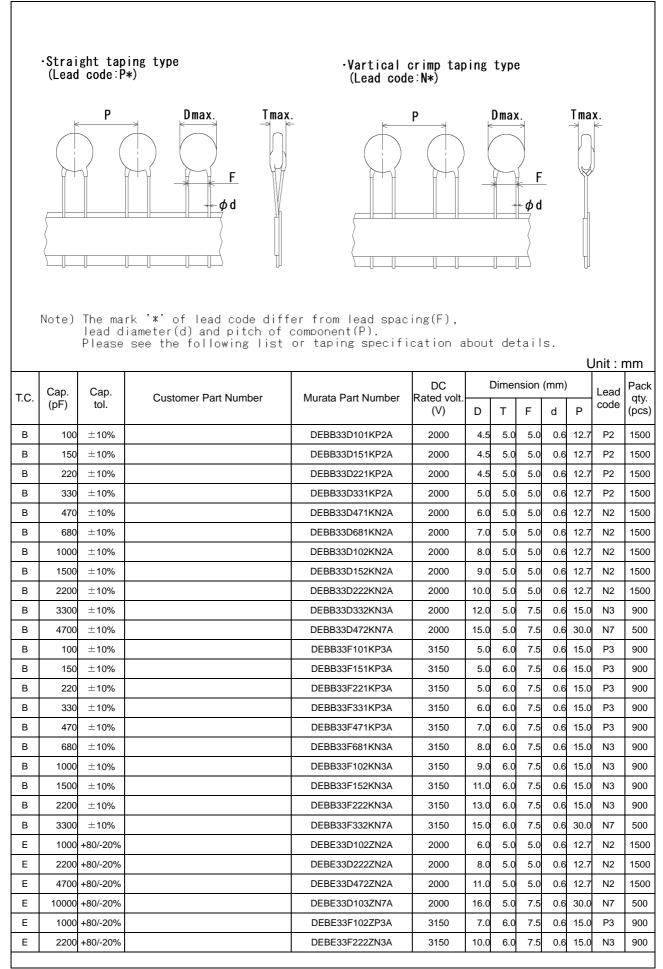


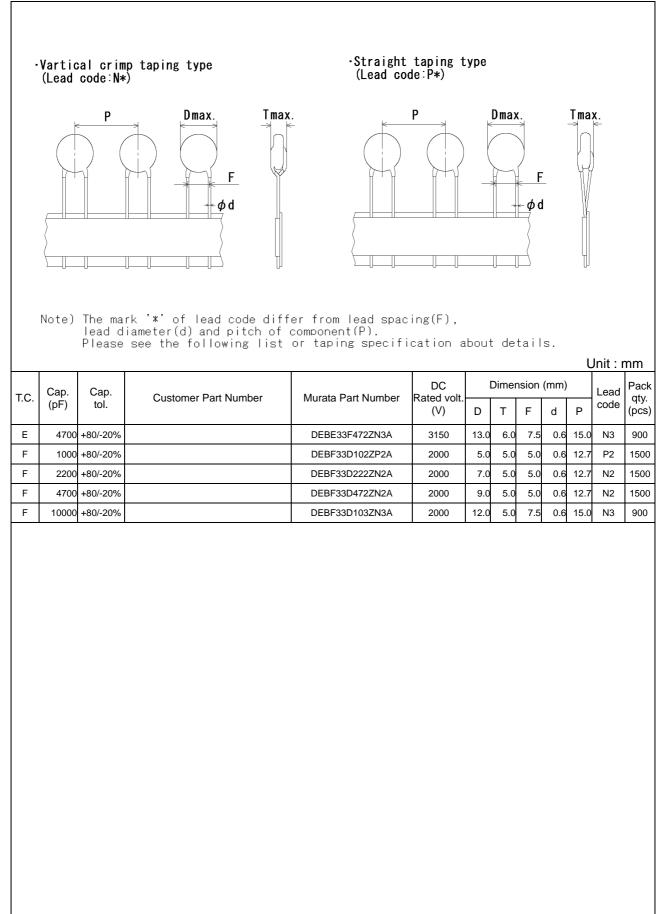












# **Reference only**

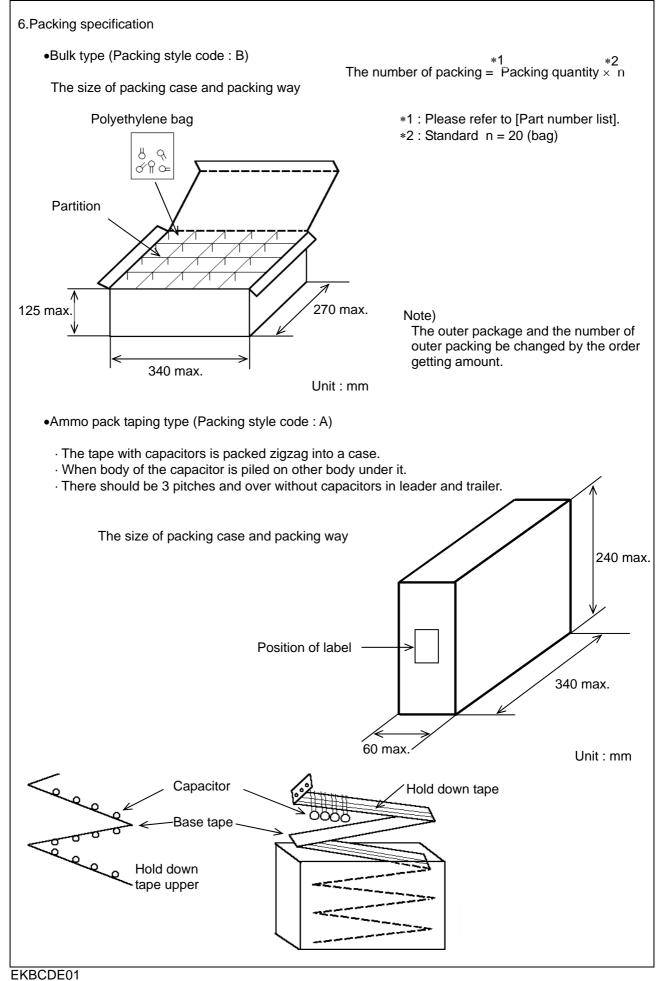
				lefence only						
	ecification and test			· · ·	1		<b>-</b>			
No.	lte		Spec	Test method The capacitor should be inspected by naked eyes						
1	Appearance and o	dimensions		ect on appearance					naked eyes	
			form and dimen				ce of defea			
				[Part number list].					n slide caliper	
2	Marking		To be easily leg	ible.	The capacitor should be inspected by naked eyes.					
3	Dielectric	Between lead	No failure.		The capacitor should not be damaged when DC voltage of 200% of the rated voltage are applied					
	strength	wires							are applied	
							wires for			
		Body				(Charge	/Discharg	ge current⊴	≤50mA.)	
		Body	No failure.						er with metal	
		insulation					1mm so th		ead wire,	
					shortcirc	cuited, is	kept about	2mm		
					off the b	alls as sh	Iown		M	
						gure, and			¥.	
						of 1.3kV i			$\Lambda$ .	
						for 1 to 5		000	About	
						n capacito		- Contraction of the contraction	00000000000000000000000000000000000000	
						nd small n		26	Metal bal	
					(Charge	/Discharg	ge current⊴	≤50mA.)		
4	Insulation	Between lead	10000MΩ min.		The insu	ulation res	sistance sł	nould be n	neasured with	
	Resistance (I.R.)	wires			DC500+	50V with	in 60±5 s d	of charging	g.	
5	Capacitance		Within specified	l tolerance.	The cap	acitance	should be	measured	d at 20°C with	
					1±0.2kH	Iz and AC	5V(r.m.s.)	max		
6	Dissipation Factor	r (D.F.)	Char. B.E : 2.5% max.						sured at 20°C	
		. ,	Char. F : 5.0% max.		with 1±0	).2kHz an	d AC5V(r.	m.s.) max		
7	Temperature char	acteristic	Char. B : Within ±10%		The cap	acitance	measurem	nent shoul	d be made at	
	•		Char. E : Within $\pm 20/-55\%$				ed in Table			
			Char. F : Within							
			Pre-treatment : Capacitor should b			+ 05-200	for 1 h th	on placed	at *room	
					h before initial measurements.					
					n pelore in	iiliai mea	surements	•	· · · · · · · · ·	
					1	2	3	4	5	
					20±2	-25±3	20±2	85±2	20±2	
		1								
8	Strength of lead	Pull	Lead wire shoul				igure at rig			
			Capacitor shoul	d not be broken.			citor and a			
							to each lea		the	
							the capac			
							diameter	0.5mm ),	••+	
			_			p it for 10				
		Bending							o 5N ( 2.5N fo	
									bent 90° at th	
									returned to it	
							and bent 9			
							te of one l			
9	Vibration	Appearance	No marked defe				ould be firr			
	resistance	Capacitance	Within specified						a frequency	
		D.F.		Char. B,E : 2.5% max.					mplitude, with	
			Char. F : 5.0%	6 max.	about a 1min rate of vibration change from 10Hz					
					to 55Hz and back to 10Hz. Apply for a total of 6 h; 2 h each in 3 mutually perpendicular directions.					
		<u> </u>			-					
10	Solderability of lea	ads	Lead wire should be soldered		The lead	d wire of a	a capacito	r should b	e dipped into	
				oated on the axial	ethanol	solution of	of 25wt% r	osin and t	hen into	
			direction over 3		molten s	solder for	2±0.5 s. lr	n both cas	es the depth	
			circumferential	direction.	molten solder for $2\pm0.5$ s. In both cases the depth dipping is up to about 1.5 to 2mm from the root of					
				lead wires.						
						Temp. of solder :				
				245±5°C Lead Free Solder (Sn-3Ag-0.5Cu)						
							Eutectic So		- ,	
*	"room condition" Te	mperature: 15 to 1	35°C. Relative hum	nidity: 45 to 75% A	tmospheri	c pressu	e: 86 to 10	)6kPa		
					uncopriori	o procoui	0.00101			

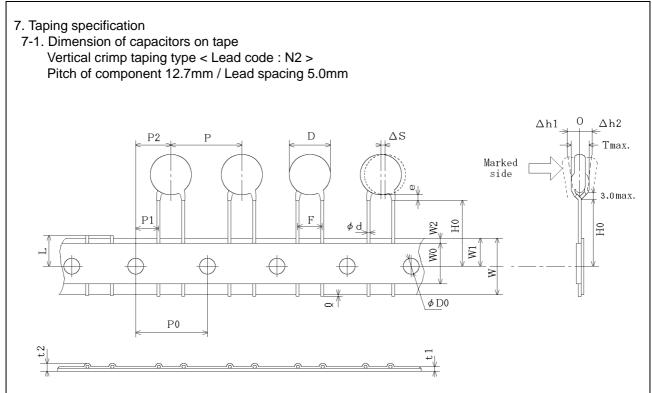
#### **Reference only**

11       Soldering effect (Non-preheat)       Appearance       No marked defect.       The lead wire should be immersed into the melted solder of 350±10°C (Body of \$\$ and under:         Capacitance change       Char. B : Within ± 5% Char. E : Within ± 15%       Solder of 350±10°C (Body of \$\$ and under:         Dielectric strength (Between lead wires)       Per item 3.       Per item 3.         Pre-treatment :       Capacitor should be stored at * room condition for 24±2 h before initial measurements.         Post-treatment :       Capacitor should be stored for 4 to 24 h at * room condition.					
(Non-preheat)         Capacitance chane         Char. E: Within ± 5% Char. F: Within ± 20%         Solder of 350-10°C (Body of \$6 and under: 500 and 500-10°C (Body of \$6 and under: 500 and 500 an	No.				
change         Char. E: Within ± 15%         270:55°C) up to about 15 (5 0.20m from the main body for 36:05 s. (5 0.5 s.)           Delectric         Per item 3.         270:55°C) up to about 15 (5 0.20m from the stored at transmitter to the stored of the stored of the stored at transmitter to the store of	11				The lead wire should be immersed into the melted
Image: Char, F: Within ± 20%         main body for 3.5:0.5 s. (Body of 45 and under: 5:0.5 s.)           Pertreatment: Capacitor should be stored at 8:5:2°C for 1 h, then placed at 'room condition for 24:2. h           Post-treatment: Capacitor should be stored for 24:2°C hore and 100 stored for 24:2°C hore and 100 stored for 24:2°C hore and 100 stored for 10:24:2°C hore and 100 stored for 10:24:2°C hore and 100 stored for 24:2°C hore and 100 stored for 10:24:2°C hore and 100 stored for 10:24:2°C hore and 10:24:2°C hore and 10:25°C hore hore and		(Non-preheat)			
Dielectric strength (Between lead wires)         Per item 3.         5:0.5.5.         5:0.5.5.           Pre-treatment : Capacitor should be stored at store initial measurements. Post-treatment : Capacitor should be stored at the capacitor should be stored at tabulaction			change		
strength (Between lead wires)         Strength (Between lead wires)         Pre-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at 7000 condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at 120-0-5°C for 60-0-5 s. Then, as in figure, the lead wires should be char, F : Within ± 5%, Char, F : Within ± 20%           12         Soldering effect charge         Appearance charge         No marked defect.         First the capacitor should be stored at 120-0-5°C for 60-0-5 s. Then, as in figure, the lead wires should be stored at strength (Between lead wires)           13         Humidity         Appearance. Capacitance charge         No marked defect.         First the capacitor should be stored at 85:2°C for 1 h, then placed at 85:2°C for 1 h, then placed at 'room condition for 24:2 h at 'room condition for 24:2 h before initial measurements.           13         Humidity (Under steady state)         Appearance. Char, F : Within ± 10% charge         No marked defect. Char, F : 7.5% max.         Set the capacitor should be stored at 85:2°C for 1 h, then placed at 'room condition for 24:2 h before initial measurements.           14         Humidity loading Char, F : 7.5% max.         No marked defect. Char, F : 7.5% max.         Set the capacitor should be stored at 'soon condition for 24:2 h before initial measurements.           16         Life         Appearance Char, F : 7.5% max. I.R.         No marked defect. Char, F : 7.5% max.         Set the capacitor should be stored at 'soon condition for 24:2 h before initial measurements.           16         Life         Appea				Char. F: Within ± 20%	main body for 3.5 $\pm$ 0.5 s. (Body of $\phi$ 5 and under:
Between lead wires)         No marked defect.         Bistic control of 242 hours of the before initial measurements. Post-treatment. Capacitor should be stored at 120-0-5°C for 60-0-5°S.           2         Soldering effect (On-preheat)         Appearance lead wires)         No marked defect.         First the capacitor should be stored at 120-0-5°C for 60-0-5°S.           2         Soldering effect (On-preheat)         Appearance lead wires)         No marked defect.         First the capacitor should be stored at 120-0-5°C for 1h. then placed at 120-0-5°C for 50-0-5°S.           3         Humidity (Under steady state)         Appearance Char. B: Within ±10% (Char. E: Within ±10% Char. F: 7.5% max.         Set the capacitor should be stored at 85:2°C for 1 h, then placed at "room condition for 242:2 h before initial measurements.           4         Humidity loading Char. E: Within ±10% Char. F: 7.5% max.         Apply a DC volage of 1500 + 24/-0 h at 85:2°C for 1 h, then placed at "room condition for 242:2 h. before initial measurements.           5         Life         Appearance Char. F:				Per item 3.	
8         Between lead wires)         85:22 Cor 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment: Capacitor should be stored at Capacitance Char. B: Within ± 5% Char. F: Within ± 20%           2         Soldering effect (On-preheat)         Appearance Char. B: Within ± 15% Char. F: Within ± 20%         First the capacitor should be stored at 10:0-0-5°C for 60:0-0-5°C. Dielectric Strength (Between lead wires)         Per item 3.           3         Humidity (Under steady state)         Appearance Capacitance Char. B: Within ± 10% Char. F: Within ± 10% Char. F: Within ± 10% Char. B: Within ± 10% Char. F: T. 5% max.         Pre-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be stored at 85:22°C for 1 h, then placed at *room condition for 24:2 h before initial measurements. Post-treatment : Capacitor should be					Pre-treatment : Capacitor should be stored at
2       Soldering effect (On-preheat)       Appearance       No marked defect.       First the capacitor should be stored of 4 to 24 h at " noom condition.         2       Soldering effect (On-preheat)       Appearance       No marked defect.       First the capacitor should be stored at 12040-75 C for 6040-5 s.         2       Char, F: Within ± 5% Char, F: Within ± 20% Char, F: Within ± 20%       First the capacitor should be stored at 12040-75 C for 6040-5 s.         2       Dielectric strength (Between lead wires)       Per item 3.         3       Humidity (Under steady state)       Appearance       No marked defect.         3       Humidity (Lange Char, F: Within ± 20% Char, F: Within ± 20% Char, F: Within ± 20%       Set the capacitor should be stored at 85:2°C for 1 h, then placed at "room condition for 24:2 h before initial measurements.         3       Humidity (Lange Char, F: Within ± 20% Char, F: Within ± 20% Char, F: Within ± 20% Char, F: 7.5% max.       Set the capacitor should be stored at 85:2°C for 1 h, then placed at "room condition.         4       Humidity loading Capacitance Char, B: Within ± 20% Char, F: 7.5% max.       Set the capacitor should be stored at "room condition.         5       Life       Appearance Capacitance Char, B: Within ± 20% Char, F: 7.5% max.       Post-treatment: Capacitor should be stored at "room condition.         4       Humidity loading Capacitance Char, B: Within ± 20% Char, F: 7.5% max.       Apply the rated values for 500 +24/0 h at "room condition for 24:2 h thore humidi					
2         Soldering effect (On-preheat)         Appearance Capacitance Char, E : Within ± 6% Char, E : Within ± 6% Char, E : Within ± 6% Char, F : Within ± 6% Char, F : Within ± 20% Char, F : Within ± 20%         First the capacitor should be stored at 1204-0*20 for 60-40-5.           Dielectric strength (Between lead wires)         Dielectric strength         Per item 3.         Then, as in figure the lead wires should be immerse closed of 260-0-57 Cup to 1.5 to 2.0mm from the root of terminal for 7.5+0-1 s.           3         Humidity (Under steady state)         Appearance         No marked defect.         Set the capacitor should be stored at 85:2:2 C for 1 h, then placed at * room condition for 242 2 h before initial measurements.           3         Humidity (Under steady state)         Appearance Char, F : Within ± 10% Char, F : 7.5% max.         Set the capacitor should be stored at 85:2:2 C for 1 h, then placed at * room condition for 242 2 h the capacitor for 500 + 24/-0 h at 40:2:2 C in 9 to 95% relative humidity.           4         Humidity loading Char, F : Within ± 10% Char, F : 7.5% max.         Set the capacitor should be stored at * room condition.           5         Life         Appearance Char, B : Within ± 10% Char, F : 7.5% max.         Apply the rate voltage for 500 + 24/-0 h at 40:2:2 C in to 2 h at * room condition.           5         Life         Appearance Char, F : Within ± 10% Char, F : 7.5% max.         Apply the rate voltage for 500 + 24/-0 h at * room condition for 242 2 h before initial measurements. Char, F : Within ± 10% Char, F : 7.5% max.           5         Life			lead wires)		
Appearance         No marked defect.           (On-preheat)         Appearance         No marked defect.           (On-preheat)         Capacitance change         Char. F: Within ± 15% Char. F: Within ± 20%         First the capacitor should be stored at 120+0/-5°C for 60+0/-5 s.           Dielectric         Per item 3.         Then, as in figure, the lead wires should be immersed solder of 280+0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.           Between lead wires)         Per item 3.         Pre-treatment:         Capacitance change           3         Humidity (Under steady state)         Appearance         No marked defect.         Pre-treatment:           0.F.         Char. B: Within ±10% Char. E: Within ±20% Char. F: Xithin ±30% D.F.         Set the capacitor should be stored at *room condition for 24±2 h before initial measurements.           0.F.         Char. B: E: 5.0% max. Char. F: 7.5% max.         Post-treatment: Capacitor should be stored at *room condition for 24±2 h before initial measurements.           0.F.         Char. B: E: 5.0% max. Char. F: Xithin ±10% Char. F: Xithin ±20% Char. F: Within ±10% Char. F: Xithin ±20% Char. F: Within ±10% Char. F: Xithin ±10% Char. F: Xithin ±10% Char. F: Xithin ±10% Char. F: Xithin ±20% Char. F: Xithin ±10% Char. F: Xithin ±10% Char. F: Xithin ±20% Char. F: Within ±10% Char. F: Xithin ±20% Char. F: Within ±10% Char. F: Xithin ±20% Char. F: Xithin ±20% Char. F: Within ±20% Char. F: Within ±10% Char. F: Xithin ±20% Char. F: Xithin ±20% Char. F: Xith					before initial measurements.
12       Soldering effect (On-preheat)       Appearance Char, B: Within ± 5%, Char, F: Within ± 20%       First the capacitor should be stored at 120-0/5°C for 60+0/5°S.         Dielectric strength (Between lead wires)       Per item 3.       Then, as in figure, the lead wires should be immersed solidor of 280+0/5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.         13       Humidity (Under steady state)       Appearance char, F: Within ± 10% (Char, F: Within ± 10% Char, F: Within ± 20%       Pre-treatment : Capacitor should be stored at solar vocation for 240+0 h at 40±2°C in 4 to 24 h at 'room condition.         14       Humidity loading       Appearance change char, F: Within ± 10% Char, F: : Within ± 10% Char, F: Within ± 20% Char, F: Within ± 20%       Set the capacitor for 500 + 24/-0 h at 40±2°C in 4 to 24 h at 'room condition.         14       Humidity loading       Appearance change char, F: : Within ± 10% Char, F: : Within ± 20% Char, F: : Within ± 20% Char, F: : Within ± 20% Char, F: : 7.5% max.       Apply the rated voltage for 500 + 24/-0 h at 85±2°C (or 1 h, then placed at 'room condition for 24±2 h before initial measurements.         14       Humidity loading       Appearance Char, B: : 7.5% max.       Apply the rated voltage for 500 + 24/-0 h at 85±2°C (or 1 h, then placed at 'room condition for 24±2 h before initial measurements.         15       Life       Appearance Char, B: Within ± 10% Char, F: : Within ± 20% Char, F: : Within ± 20					Post-treatment : Capacitor should be stored for
(On-preheat)       Capacitance change char, B: Within ± 15% Char, F: Within ± 20% Char, F: Within ± 20%       Then, as in figure, the lead wires should be immersed solder of 260-0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.         Dielectric       Per item 3.       Then, as in figure, the lead wires should be immersed solder of 260-0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.         Immersed solder of 260-0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.       Then, as in figure, the lead wires should be immersed solder of 260-0/-5°C.         Immersed solder of 260-0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.       Then, as in figure, the lead wires should be immersed solder of 260-0/-5°C.         Immersed solder of 260-0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.       Then, as in figure, the lead wires should be immersed solder of 260-0/-5°C.         Immersed solder of 260-0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s.       Then, as in figure, the lead wires should be immersed solder of 260-0/-5°C.         Immersed solder of 260-0/-5°C up to 150-0/-5°C of 1 to 1 to 21.2 h       Pre-treatment : Capacitor should be stored at before initial measurements.         Immersed solder of 260-0/-5°C up to 150-0/-5°C of 1 h, then placed at 'room condition.       Pre-treatment : Capacitor should be stored at before initial measurements.         IA       Humidity loading       Appearance Char, F : Within ±10% Char, F : Within ±20% Char, F : Within ±30% D.F.       Char, B : 4.0% max, Char, F : Within ±30% Char, F : Within ±30% Char, F : Within					4 to 24 h at * room condition.
change     change     Char. F: Within ± 15% Char. F: Within ± 20%     Then, as in figure, the lead wires should be intermed solder of 280-04-5% Cup to 15 to 2.0mm from the root of terminal for 7.5+0/-1 s.       Dielectric strength (Between lead wires)     Per item 3.     Then, as in figure, the lead wires should be intermed solder of 280-04-5% Cup to 15 to 2.0mm from the root of terminal for 7.5+0/-1 s.       13     Humidity (Under steady state)     Appearance     No marked defect.     Pre-treatment : Capacitor should be stored at 85:2°C for 1 h, then placed at " room condition for 242 h before initial measurements.       13     Humidity (Under steady state)     Appearance change     No marked defect.     Set the capacitor for 500 +24/-0 h at 40:2°C in to 0 95% relative humidity.       14     Humidity loading     Appearance change     No marked defect.     Apply the rated voltage for 500 +24/-0 h at to 2 h at ' room condition.       14     Humidity loading     Appearance hange     No marked defect.     Apply the rated voltage for 500 +24/-0 h at 40:2°C in 9 to 95% relative humidity.       15     Life     Appearance change     No marked defect.     Apply the rated voltage of 150% of the rated voltage change       15     Life     Appearance change     No marked defect.     Apply a DV close at at ' room condition for 242 h at ' com condition for 242 h at ' room condition for 242 h.       15     Life     Appearance change     No marked defect.     Apply a DV close at at 's 00° moralitial measurements.       16 </td <td>12</td> <td>Soldering effect</td> <td>Appearance</td> <td>No marked defect.</td> <td>First the capacitor should be stored at</td>	12	Soldering effect	Appearance	No marked defect.	First the capacitor should be stored at
Char. F: Within ± 20%     immersed solder of 260+0-5°C up to 1.5 to       Dielectric trength (Between lead wires)     Per item 3.       13     Humidity (Under steady state)     Appearance Char. F: Within ±10% Char. F: Within ±10% Char. F: X0% max.       13     Humidity (Under steady state)     Appearance Char. F: Within ±10% Char. F: X0% max.       14     Humidity loading     Appearance Capacitance Char. F: Within ±10% Char. F: X0% max.       15     Life       16     Appearance Char. F: X0% max.       17.     Char. F: X0% max.       18.     No marked defect.       19.     Operative Humidity.       19.     Appearance Char. F: X0% max.       19.     D.F.       19.     Appearance Char. F: X0% max.       19.     Appearance Char. F: X0% max.       19.     Appearance Char. F: X0% max.       19.     D.F.       19.     Momarked defect.       19.     Appearance Char. F: X0% max.       19.     D.F.       19.     Southin ±10% Char. F: X0% max.       19.     D.F.       19.     Southin ±20% Char. F: X0% max.       19.     D.F.       19.     Appearance Char. B: X0% max.       19.     Appearance Char. F: X0% max.       19.     D.F.       19.     Char. F: 7.5% max. <t< td=""><td></td><td>(On-preheat)</td><td>Capacitance</td><td>Char. B: Within ± 5%</td><td></td></t<>		(On-preheat)	Capacitance	Char. B: Within ± 5%	
Dielectric strength lead wires)         Per item 3.         2.0mm from the root of terminal for 7.5+0/-1 s.           2.0mm from the root of terminal for 7.5+0/-1 s.         2.0mm from the root of terminal for 7.5+0/-1 s.           13         Humidity (Inder steady state)         Appearance D.F.         No marked defect.           13         F.         Char. B : Within ±10% Char. E : Within ±20% Char. F : 7.5% max.         Set the capacitor should be stored at 85±2°C for 1 h, then placed at *room condition for 24±2 h before initial measurements.           14         Humidity loading         Appearance Char. B : Within ±10% Char. E : Within ±20% Char. F : 7.5% max.         Set the capacitor should be stored at *room condition for 24±2 h before initial measurements.           14         Humidity loading         Appearance Char. F : 7.5% max.         No marked defect.           17.         Char. B E : 5.0% max.         Post-treatment : Capacitor should be stored at *room condition for 24±2 h before initial measurements.           18.4         Humidity loading         Appearance Char. F : Within ±10% Char. F : 7.5% max.         Apply the rated voltage for 500 +24/-0 h at 85±2°C for 1 h, then placed at *room condition for 24±2 h before initial measurements.           18.5         Life         Appearance Char. F : 7.5% max.         Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C for 1 h, then placed at *room condition for 24±2 h before initial measurements.           15         Life         Appearance Char. F : 7.5%			change	Char. E: Within ± 15%	
Image: Strength (Between lead wires)         Per item 3.         2.0mm from the root of terminal for 7.5+0/-1 s.           Image: Strength (Between lead wires)         Pre-item 3.         2.0mm from the root of terminal for 7.5+0/-1 s.           Image: Strength (Between lead wires)         Pre-item 3.         Image: Strength (Between lead wires)           Image: Strength (Between lead wires)         Appearance         No marked defect.           Image: State)         Appearance Char. B: Within ±10% (Inder steady state)         State: Strength (State)           IR.         1000MQ min.         Post-treatment: Capacitor for 500 +24/-0 h at 40:2°C in 90 to 95% relative humidity.           IR.         1000MQ min.         Post-treatment: Capacitor should be stored at 85:2°C for 1 h, then placed at *room condition.           IA         Humidity loading         Appearance Char. B: Within ±10% (Char. F: 7.5% max.           I.R.         1000MQ min.         Post-treatment: Capacitor should be stored at 85:2°C for 1 h, then placed at *room condition.           I.R.         500MQ min.         Post-treatment: Capacitor should be stored at *room condition.           I.R.         Soum arked defect.         Apply the rated voltage for 1000 +48/-0 h at 85:2°C for 1 h, then placed at *room condition for 24:2 h. before initial measurements.           I.R.         SouMQ min.         Post-treatment: Capacitor should be stored at 85:2°C for 1 h, then placed at *room condition for 24:2 h. before initial measurements. </td <td></td> <td></td> <td></td> <td>Char. F: Within ± 20%</td> <td></td>				Char. F: Within ± 20%	
Image: Between lead wires)         (Between lead wires)         Case of the stand st			Dielectric		2.0mm from the root of terminal for 7.5+0/-1 s.
Image: Between lead wires)         (Between lead wires)         Constant of the stand state of the state			strength		
Image: Image					
Image: state         Appearance         No marked defect.         Pre-treatment :         Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           13         Humidity (Under steady state)         Appearance         No marked defect.         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           13         Humidity (Under steady state)         Char. B : Within ±10% Char. E : Within ±20% Char. F : 7.5% max.         Set the capacitor should be stored at * room condition.           14         Humidity loading Appearance         No marked defect.         Appearance         No marked defect.           14         Humidity loading Char. B : Within ±10% Char. B : Within ±20% Char. Char. B : D : T. 5% max.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           15         Life         Appearance No marked defect.         Apply a DC voltage of 1500 v 14 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           15         Life         Appearance Char. B : Within ±10% Char. F : 7.5% max.         Apply a DC voltage of 1500 v 14 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           16         Life         Appearance Char			lead wires)		
13       Humidity (Under steady state)       Appearance       No marked defect.       Pre-treatment : Capacitor should be stored at *room condition for 24±2 h before initial measurements.         13       Humidity (Under steady state)       Appearance       No marked defect.       Set the capacitor for 500 + 24/-0 h at 40±2°C in 4 to 24 h at * room condition.         13       Humidity (Under steady state)       Appearance       No marked defect.       Set the capacitor for 500 + 24/-0 h at 40±2°C in 4 to 24 h at * room condition.         14       Humidity loading       Appearance       No marked defect.       Applearance         14       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 + 24/-0 h at 85±2°C for 1 h, then placed at * room condition.         14       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 + 24/-0 h at 40±2°C in 90 to 95% relative humidity.         15       Life       Appearance       No marked defect.       Apply a DC voltage of 150% of the rated voltage for 1 0, 0 + 48/-0 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h.         15       Life       Appearance Char, F : Within ±00% Char, F : Within ±10% Char, F : Within ±30%       Apply a DC voltage of 150% of the rated voltage for 1 0, 0 + 48/-0 h at 85±2°C, or 1 h, then placed at * room condition for 24±2 h.         15       Life       Appearance Char, F : Within ±20% Char, F : Within ±30% <t< td=""><td></td><td></td><td>, í</td><td></td><td>□ □ □ □ □ □ □ □ ↓ to 2.0mm</td></t<>			, í		□ □ □ □ □ □ □ □ ↓ to 2.0mm
3         Humidity         Appearance         No marked defect.         Pre-treatment : Capacitor should be stored at 85:2°C for 1 h, then placed at * room condition for 24:2 h.           3         Humidity (Under steady state)         Appearance         No marked defect.         Set the capacitor of 500 + 24/-0 h at 40±2°C in 9 to 95% relative humidity.           3         Humidity (Under steady state)         Char. B : Within ±20% Char. E : Within ±20% Char. F : 7.5% max.         Set the capacitor should be stored at 85:2°C for 1 h, then placed at * room condition for 24:2 h.           4         Humidity loading         Appearance         No marked defect.         Applearance           4         Humidity loading         Appearance         No marked defect.         Applearance           5         Life         Appearance         Char. F : 7.5% max.         Post-treatment : Capacitor should be stored at 85:2°C for 1 h, then placed at * room condition.           5         Life         Appearance         No marked defect.         Apply the rated voltage for 500 + 24/-0 h at 85:2°C for 1 h, then placed at * room condition for 24:2 h.           5         Life         Appearance         No marked defect.         Apply the rated voltage for 500 + 24/-0 h at 85:2°C for 1 h, then placed at * room condition for 24:2 h.           5         Life         Appearance         No marked defect.         Apply the rated voltage of 150% of the rated voltage for 100 voltage of 150% of the rated					
3         Humidity (Under steady state)         Appearance         No marked defect.         Set the capacitor for 500 +24/-0 h at 40±2°C in 4 to 24 h at * room condition.           3         Humidity (Under steady state)         Appearance         No marked defect.         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           5         Life         Appearance         No marked defect.         Set the capacitor should be stored at * room condition for 24±2 h before initial measurements.           6         D.F.         Char. B : Within ±10% Char. E : Within ±30%         Pre-treatment : Capacitor should be stored at * room condition for 24±2 h before initial measurements.           4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at to 2 h at * room condition.           7         Char. B : Within ±10% Char. F : Within ±30%         Char. F : Within ±0% Char. F : Within ±30%         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           5         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 100 to 48/+0 h at 85±2°C or 1 h, then placed at * room condition for 24±2 h before ini					solder
3         Humidity (Under steady state)         Appearance         No marked defect.         Set the capacitor for 500 +24/-0 h at 40±2°C in 4 to 24 h at * room condition.           3         Humidity (Under steady state)         Appearance         No marked defect.         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           5         Life         Appearance         No marked defect.         Set the capacitor should be stored at * room condition for 24±2 h before initial measurements.           6         D.F.         Char. B : Within ±10% Char. E : Within ±30%         Pre-treatment : Capacitor should be stored at * room condition for 24±2 h before initial measurements.           4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at to 2 h at * room condition.           7         Char. B : Within ±10% Char. F : Within ±30%         Char. F : Within ±0% Char. F : Within ±30%         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           5         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 100 to 48/+0 h at 85±2°C or 1 h, then placed at * room condition for 24±2 h before ini					Pre-treatment : Canacitor should be stored at
4         Humidity (Under steady state)         Appearance D.F.         No marked defect.         Set the capacitor for 500 +24/-0 h at 40±2°C in 4 to 24 h at * room condition for 24±2 h before initial measurements.           4         Humidity loading         Appearance Char. F : Within ±10% Char. F : 7.5% max.         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           14         Humidity loading         Appearance Char. F : 7.5% max.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           14         Humidity loading         Appearance Char. F : Within ±10% Char. F : 7.5% max.         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           15         Life         Appearance Capacitance change         No marked defect. Char. B : Within ±10% Char. F : 7.5% max.         Apply a DC voltage of 150% of the rated voltage to 2 h at * room condition for 24±2 h before initial measurements.           15         Life         Appearance Capacitance change         No marked defect. Char. B : Within ±10% Char. F : 7.5% max.         Apply a DC voltage of 150% of the rated voltage for 100 +48/-0 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           15         Life         Appearance Char. F : 7.5% max.         Apply a DC voltage of 150% of the rated voltage for 100 +48/-0 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h bumidity of 50% max           16.         Char. F : 7					
Image: basic state         Appearance         No marked defect.         Defore initial measurements.           13         Humidity (Under steady state)         Appearance         No marked defect.         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           13         D.F.         Char. B : Within ±10% Char. F : Within ±30%         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           14         Humidity loading         Appearance         No marked defect.         Post-treatment : Capacitor should be stored for 1 to 2 h at * room condition.           14         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 85±2°C for 1 h, then placed at * room condition.           14         Humidity loading         Appearance         Char. B : Within ±10% Char. F : Within ±30%         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           15         Life         Appearance Capacitance change         Char. B : Within ±10% Char. F : Within ±30%         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           15         Life         Appearance Capacitance change         No marked defect. Char. B : 40,0% max. Char. F : Within ±30%         Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C for 1 h, then placed at * room condition for					
Image: state         Appearance (Under steady state)         Appearance (Char. B : Within ±10% Char. E : Within ±20% Char. F : Within ±30%         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           14         Humidity loading         Appearance         No marked defect.         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           14         Humidity loading         Appearance         No marked defect.         Appearance         No marked defect.           14         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           14         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           15         Life         Appearance         No marked defect.         Apply the rated voltage for 10 to 24±2 h before initial measurements.           15         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 1000 +24±2 h before initial measurements.           15         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 1000 +24±2 h before initial measurements.           15         Life         Appearance         No marked defect.         Char. F : Within ±10% Char. E, E : 40% max.					
Image: Notation in the steady state)         Appearance Char. B : Within ±10% Char. F : Within ±20% Char. F : Within ±30%         Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           13         Humidity (Under steady state)         D.F.         Char. B : Within ±10% Char. F : 7.5% max.         90 to 95% relative humidity.           1.R.         1000MΩ min.         Char. F : 7.5% max.         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           1.4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           1.4         Humidity loading         Appearance         Char. B : Within ±10% Char. F : Within ±20% Char. F : Within ±20% Char. F : Within ±20% Char. F : Within ±30%         Pre-treatment : Capacitor should be stored at 40±2°C in 90 to 95% relative humidity.           1.4         Humidity loading         Appearance         Char. B : Within ±10% Char. F : Within ±30%         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h           1.5         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 100 + 24±2 h.           1.5         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 1000 + 24±2 h.           1.5 <td< td=""><td></td><td></td><td></td><td></td><td></td></td<>					
13       Humidity (Under steady state)       Appearance       No marked defect.       Set the capacitor for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.         14       Humidity loading       D.F.       Char. F : Within ±10% Char. F : Within ±30%       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         14       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 +24/-0 h at 85±2°C for 1 h, then placed at * room condition.         14       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 +24/-0 h at 85±2°C for 1 h, then placed at * room condition.         14       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.         14       Humidity loading       D.F.       Char. B : Within ±10% Char. F : Within ±20%       Apply the rated voltage for 500 +24/-0 h at 40±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         15       Life       Appearance       No marked defect.       Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         15       Life       Appearance Char. B : Within ±10% Char. F : Within ±20% Char. F : Within ±20% Char. F : Within ±20% Char. F : 7.5% max.       Apply a DC voltage of 150% of					
(Under steady state)         Capacitance change         Char. B : Within ±10% Char. E : Within ±20% Char. F : Within ±30%         90 to 95% relative humidity. Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           4         Humidity loading         Appearance Char. B : Within ±10% Char. F : Within ±20% Char. F : Within ±30%         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           4         Humidity loading         Appearance Capacitance Char. B : Within ±10% Char. F : Within ±30%         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           5         Life         Appearance No marked defect.         Appearance Char. F : Within ±20% Char. F : Within ±30%         Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max Char. F : Within ±30%           5         Life         Appearance Capacitance change         No marked defect. Capacitance change         Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max Char. F : Within ±30%         Pre-treatment : Capacitor should be stored at * room condition for 24±2 h before initial measurements.           5         Life         Appearance Appearance         No marked defect. Char. B : Within ±10% Char. F : Within ±30%         Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C for 1 h, then placed at * room co	3	Humidity	Appearance	No marked defect	
state)       change       Char. E : Within ±20% Char. F : 7.5% max.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         4       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 +24/-0 h at 0 + 20* (Char. F : 7.5% max.         4       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 +24/-0 h at 0 + 20* (Char. F : Within ±10% Char. F : Within ±30%       Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.         5       Life       Appearance       No marked defect.       Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.         5       Life       Appearance       No marked defect.       Apply the rated voltage for 100 to 95% relative humidity.         5       Life       Appearance       No marked defect.       Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C, and relative humidity of 50% max.         5       Life       Appearance       No marked defect.       Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C, and relative humidity of 50% max.         6       Char. B.E : 4.0% max.       Char. B.E : 4.0% max.       Char. G.E : Within ±30%         0.F.       Char. B.E : 4.0% max.       Char. B.E : 4.0% max.       Char et al. * room condition for 24±2 h before initial measurements	-				
4       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 + 24/-0 h at top condition of 24±2 h before initial measurements.         4       Humidity loading       Appearance       No marked defect.       Apply the rated voltage for 500 + 24/-0 h at top condition of 24±2 h before initial measurements.         4       Humidity loading       Appearance       Char. B : Within ±10% Char. F : Within ±20% Char. F : Within ±20%       Apply the rated voltage for 500 + 24/-0 h at 40±2°C in 90 to 95% relative humidity.         5       Life       Appearance       No marked defect.       Apply and the stored at top top top top top top top top top to					
D.F.         Char. B,E : 5.0% max. Char. F : 7.5% max.         * room condition for 24±2 h before initial measurements.           4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at to 2 h at * room condition.           4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at to 2 h st * room condition.           4         Humidity loading         Appearance         Char. B : Within ±10% Char. F : Within ±20% Char. F : 7.5% max.         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity. (Charge/Discharge currents/S0MA.)           D.F.         Char. B, E : 5.0% max. Char. F : 7.5% max.         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.           5         Life         Appearance         No marked defect. Char. B : Within ±10% Char. F : Within ±20% Char. F : Within ±30%         Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C, and relative humidity of 50% max (Charge/Discharge current≤50mA.)           D.F.         Char. B, E : 4.0% max. Char. F : 7.5% max.         Char. B, E : 4.0% max. Char. F : 7.5% max.           D.F.         Char. B, E : 4.0% max. Char. F : 7.5% max.         Chare B, E : 4.0% max. Char. F : 7.5% max.           I.R.         2000MΩ min.         Pre-treatment : Capacitor should be stored at * r		·····,			
I.R.         Char. F         : 7.5% max.         before initial measurements.           4         Humidity loading         Appearance         No marked defect.         Post-treatment : Capacitor should be stored for 1 to 2 h at * room condition.           4         Humidity loading         Appearance         Char. B : Within ±10% Char. E : Within ±20% Char. F : Within ±30%         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity.           D.F.         Char. B, E : 5.0% max.         Char. F : Within ±30%         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.           1.R.         500MΩ min.         SooMΩ min.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.           5         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 100 +48/-0 h at 85±2°C, and relative humidity of 50% max           5         Life         Appearance         Char. B : Within ±10% Char. F : Within ±30%         Char. F : 0% max.           D.F.         Char. B, E : 4.0% max.         Char. F : 7.5% max.         Char. F : 7.5% max.           1.R.         2000MΩ min.         Post-treatment : Capacitor should be stored at * room condition for 24±2 h.           before initial measurements.         Post-treatment : Capacitor should be stored at * room condition for 24±2 h.			DE		
I.R.         1 000MΩ min.         Post-treatment : Capacitor should be stored for 1 to 2 h at * room condition.           4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity. (Charge/Discharge currents50mA.)           4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity. (Charge/Discharge currents50mA.)           5         D.F.         Char. B. E : 5.0% max.         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           5         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C, and relative humidity of 50% max           5         Life         Appearance Char. B. E : Within ±10% Char. E : Within ±20% Char. E : Within ±20% Char. E : Within ±30%         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.           5         Life         Appearance         Char. B : Within ±10% Char. E : Within ±30%         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.           6         D.F.         Char. B : Within ±10% Char. F : 7.5% max.         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.           1			U.I.		
Image: Construct of the second se			IR		
I4         Humidity loading         Appearance         No marked defect.         Apply the rated voltage for 500 +24/-0 h at 40±2°C in 90 to 95% relative humidity. (Charge/Discharge current≤50mA.)           No.rec         Char. B : Within ±20% Char. E : Within ±30%         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.           I.R.         500MΩ min.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.           I5         Life         Appearance         No marked defect.         Apply a DC voltage of 150% of the rated voltage for 500 +24/-0 h at 85±2°C for 1 h, then placed at * room condition for 24±2 h.           I.R.         D.F.         Char. B : Vithin ±10% Char. B : Within ±10% Char. B : Within ±10% Char. E : Within ±20% Char. F : 7.5% max.         Apply a DC voltage of 150% of the rated voltage for 100 +48/-0 h at 85±2°C, and relative humidity of 50% max           I.R.         D.F.         Char. B, E : 4.0% max. Char. F : 7.5% max.         Char. F : 7.5% max.           I.R.         2000MΩ min.         Pre-treatment : Capacitor should be stored at * room condition for 24±2 h before initial measurements.           Post-treatment : Capacitor should be stored at * room condition for 24±2 h before initial measurements.         Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.			1.1		
Capacitance change       Char. B : Within ±10% Char. F : Within ±20% Char. F : Within ±30%       40±2°C in 90 to 95% relative humidity. (Charge/Discharge current≤50mA.)         D.F.       Char. B,E : 5.0% max. Char. F : 7.5% max.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         I.R.       500MΩ min.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.         15       Life       Appearance Capacitance change       No marked defect. Char. B : Within ±10% Char. F : Within ±20% Char. F : Within ±20%       Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C, and relative humidity of 50% max.         D.F.       Char. B : Within ±10% Char. F : 7.5% max.       Char. B : Within ±20% Char. F : 7.5% max.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.         I.R.       2000MΩ min.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         I.R.       2000MΩ min.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.	14	Humidity loading	Appearance	No marked defect.	
change       Char. E : Within ±20% Char. F : Within ±30%       (Charge/Discharge current≤50mA.)         D.F.       Char. B,E : 5.0% max. Char. F : 7.5% max.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         I.R.       500MΩ min.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h         I.F.       Appearance       No marked defect.         Capacitance change       Char. B : Within ±10% Char. E : Within ±20% Char. F : Within ±30%       Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max         D.F.       Char. B,E : 4.0% max. Char. F : 7.5% max.       (Charge/Discharge current≤50mA.)         D.F.       Char. B,E : 4.0% max. Char. F : 7.5% max.       (Charge/Discharge current≤50mA.)         I.R.       2000MΩ min.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h					
Char. F : Within ±30%Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.I.R.500MΩ min.Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.I5LifeAppearanceNo marked defect. Capacitance char. F : Within ±10% Char. F : Within ±20% Char. F : Within ±30%Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max (Charge/Discharge current≤50mA.)D.F.Char. B, E : 4.0% max. Char. F : 7.5% max.Pre-treatment : Capacitor should be stored at * room condition for 24±2 h.I.R.2000MΩ min.Pre-treatment : Capacitor should be stored at * room condition for 24±2 h.Post-treatment : Capacitor should be stored at * room condition for 24±2 h.Pre-treatment : Capacitor should be stored at * room condition for 24±2 h.Post-treatment : Capacitor should be stored at * room condition for 24±2 h.Pre-treatment : Capacitor should be stored at * room condition for 24±2 h.					
D.F.Char. B,E : 5.0% max. Char. F : 7.5% max.85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.I.R.500MΩ min.Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.I5LifeAppearanceNo marked defect. Capacitance changeApply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max. (Char. F : Within ±30%)D.F.Char. B : Within ±10% Char. F : Within ±30%(Charge/Discharge current≤50mA.) Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.I.R.2000MΩ min.Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.					
I.R.       S00MΩ min.       * room condition for 24±2 h before initial measurements.         5       Life       Appearance       No marked defect.       Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max         5       Life       Appearance char. F: Within ±10% Char. F: Within ±20% Char. F: Within ±30%       Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max         D.F.       Char. B,E: 4.0% max. Char. F: 7.5% max.       Char. F: 7.5% max.         I.R.       2000MΩ min.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.         Post-treatment :       Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.			DE		
I.R.       500MΩ min.       before initial measurements.         5       Life       Appearance       No marked defect.         5       Life       Appearance       Char. B : Within ±10%         Capacitance       Char. B : Within ±20%       for 1 000 +48/-0 h at 85±2°C, and relative         Char. F : Within ±20%       Char. F : Within ±30%       (Char. F : Within ±30%)         D.F.       Char. B,E : 4.0% max.       (Char. F : 7.5% max.)         I.R.       2 000MΩ min.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h         before initial measurements.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h			U.I.	,	
5       Life       Appearance       No marked defect.       Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max         5       Life       Appearance       Char. B : Within ±10% Char. E : Within ±20% Char. E : Within ±20% Char. F : Within ±30%       Apply a DC voltage of 150% of the rated voltage for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max         D.F.       Char. B,E : 4.0% max. Char. F : 7.5% max.       Char. F : 7.5% max.         I.R.       2 000MΩ min.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.			IP		
Image: state stat			1.17.		
5       Life       Appearance       No marked defect.       Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C, and relative humidity of 50% max         5       Life       Capacitance change       Char. B : Within ±10% Char. E : Within ±20% Char. E : Within ±30%       Char. F : Within ±30%       Char.ge/Discharge current≤50mA.)         D.F.       Char. B,E : 4.0% max.       Char. F : 7.5% max.       Char. F : 7.5% max.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         I.R.       2 000MΩ min.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.					•
5       Life       Appearance       No marked defect.       Apply a DC voltage of 150% of the rated voltage for 1000 +48/-0 h at 85±2°C, and relative humidity of 50% max         5       Char. B : Within ±10% Char. E : Within ±20% Char. E : Within ±30%       for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max         0       D.F.       Char. B,E : 4.0% max.       (Char.ge/Discharge current≤50mA.)         1.R.       2 000MΩ min.       Pre-treatment : Capacitor should be stored at * room condition for 24±2 h before initial measurements.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.					· · ·
Capacitance changeChar. B : Within ±10% Char. E : Within ±20% Char. F : Within ±30%for 1 000 +48/-0 h at 85±2°C, and relative humidity of 50% max (Charge/Discharge current≤50mA.)D.F.Char. B,E : 4.0% max. Char. F : 7.5% max.(Charge/Discharge current≤50mA.) Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.I.R.2 000MΩ min.Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.	5	Life	Appearance	No marked defect.	
change       Char. E : Within ±20% Char. F : Within ±30%       humidity of 50% max (Charge/Discharge current≤50mA.)         D.F.       Char. B,E : 4.0% max. Char. F : 7.5% max.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         I.R.       2 000MΩ min.       Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h	-				
Char. F : Within ±30%       (Charge/Discharge current≤50mA.)         D.F.       Char. B,E : 4.0% max.         Char. F : 7.5% max.       Pre-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         I.R.       2 000MΩ min.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.					
D.F.       Char. B,E : 4.0% max. Char. F       Pre-treatment :       Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h before initial measurements.         I.R.       2000MΩ min.       Post-treatment :       Capacitor should be stored at * room condition for 24±2 h before initial measurements.         Post-treatment :       Capacitor should be stored at * room condition for 24±2 h.					
Char. F     : 7.5% max.     85±2°C for 1 h, then placed at       I.R.     2000MΩ min.     * room condition for 24±2 h before initial measurements.       Post-treatment :     Capacitor should be stored at 85±2°C for 1 h, then placed at       * room condition for 24±2 h     * room condition for 24±2 h			DF		
I.R.       2 000MΩ min.       * room condition for 24±2 h before initial measurements.         Post-treatment :       Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.			0.1.		
before initial measurements. Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.			IP		
Post-treatment : Capacitor should be stored at 85±2°C for 1 h, then placed at * room condition for 24±2 h.			1.17.	2 0001VI22 11111).	
85±2°C for 1 h, then placed at * room condition for 24±2 h.					
* room condition for 24±2 h.					
Toom condition Tremperature. 13 to 33 C, Relative numbers, 43 to 75%, Atmospheric pressure, 60 to TookPa	* "	room condition" Tom	l Anatura: 15 to 2	1 5°C Relative humidity: 15 to 75% Atm	
		room condition remp	berature: 15 to 3	5 C, Relative numidity: 45 to 75%, Atm	usphenc pressure: oo to TU6KPa

# **Reference only**

No. 16	ltem		Creation				
			Specification		Test m	ethod	
1	Temperature and	Appearance	No marked defect.	The ca	pacitor should be s	subjected to	0
	Immersion cycle	Capacitance	Char. B : Within ±10%		erature cycles, the	n consecut	tively to
		change	Char. E : Within ±20%	2 imme	ersion cycles.		
			Char. F : Within ±30%		erature cycle>		
		D.F.	Char. B,E : 4.0% max.	Step			
			Char. F : 7.5% max.	1	-25±3	30 m	
		I.R.	2000MΩ min.	2	Room Temp.	3 mi	
		Dielectric	Per item 3.	3	+85±3	30 m	
		strength		4	Room Temp.		
		(Between				Cycle	e time : 5 cycle
		lead wires)		<imme< td=""><td>rsion cycle&gt;</td><td></td><td></td></imme<>	rsion cycle>		
				Cton		Time	Immersion
				Step	Temperature(°C)	Time	water
				1	+65+5/-0	15 min	Clean water
				2	0±3	15 min	Salt water
			5°C, Relative humidity: 45 to 75%, At	Post-tr	* room o before eatment : Capacite 4 to 24 l	or should b for 1 h, the condition for initial mea or should b h at * room	en placed at or 24±2 h asurements.

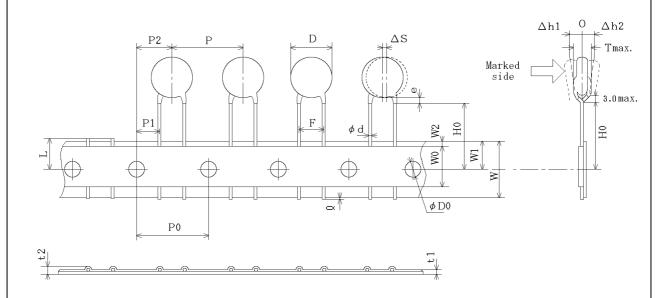




Unit : mm

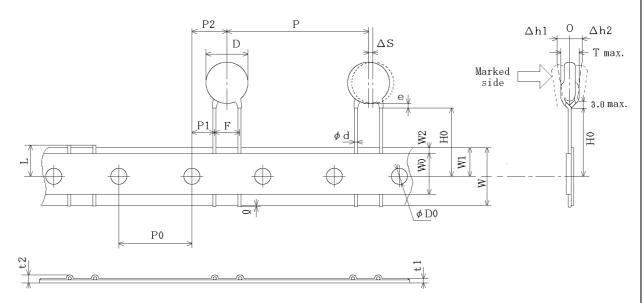
Item	Code	Dimensions	Remarks
Pitch of component	Р	12.7±1.0	
Pitch of sprocket hole	P0	12.7±0.3	
Lead spacing	F	$5.0\pm^{0.8}_{0.2}$	
Length from hole center to component center	P2	6.35±1.3	
Length from hole center to lead	P1	3.85±0.7	Deviation of progress direction
Body diameter	D	Please refer to [P	art number list ].
Deviation along tape, left or right	ΔS	0±1.0	They include deviation by lead bend .
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	HO	$18.0\pm^{2.0}_{0}$	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	1.0 may	
Deviation across tape, rear	∆h2	1.0 max.	
Portion to cut in case of defect	L	<b>11.0</b> ± <sup>0</sup> <sub>1.0</sub>	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	Up to the end of a	rimp
Body thickness	Т	Please refer to [P	art number list ].

Vertical crimp taping type < Lead code : N3 > Pitch of component 15.0mm / Lead spacing 7.5mm



	·		Unit : mm
Item	Code	Dimensions	Remarks
Pitch of component	Р	15.0±2.0	
Pitch of sprocket hole	P0	15.0±0.3	
Lead spacing	F	7.5±1.0	
Length from hole center to component center	P2	7.5±1.5	
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction
Body diameter	D	Please refer to [	Part number list ].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend .
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	HO	$18.0\pm_{0}^{2.0}$	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1		
Deviation across tape, rear	∆h2	2.0 max.	
Portion to cut in case of defect	L	<b>11.0</b> ± <sup>0</sup> <sub>1.0</sub>	
Hold down tape width	WO	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	Up to the end of	crimp
Body thickness	Т	Please refer to [	Part number list ].

Vertical crimp taping type < Lead code : N7 > Pitch of component 30.0mm /Lead spacing 7.5mm



Unit : mm

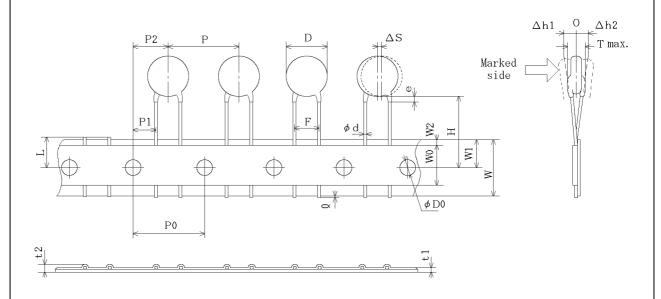
Item	Code	Dimensions	Remarks
Pitch of component	Р	30.0±2.0	
Pitch of sprocket hole	P0	15.0±0.3	
Lead spacing	F	7.5±1.0	
Length from hole center to component center	P2	7.5±1.5	
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction
Body diameter	D	Please refer to [	Part number list ].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend.
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	HO	$18.0\pm^{2.0}_{0}$	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	0.0	
Deviation across tape, rear	∆h2	2.0 max.	
Portion to cut in case of defect	L	<b>11.0</b> ± <sup>0</sup> <sub>1.0</sub>	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	Up to the end of	crimp
Body thickness	Т	Please refer to [	Part number list ].

Straight taping type < Lead code: P2 > Pitch of component 12.7mm / Lead spacing 5.0mm 0  $\Delta h2$  $\Delta h1$ D  $\Delta S$ Р2 Р \_T max.  $\rightarrow$ Marked side Ρ1 F ø d W2 Ξ W0 ΓW  $\oplus$  $\oplus$  $\oplus$ ¢ D0  $\sim$ Р0 t 2 ŝ \_\_\_\_ \_\_\_\_\_\_ \_\_\_\_\_ <u></u> -@

Unit : mm

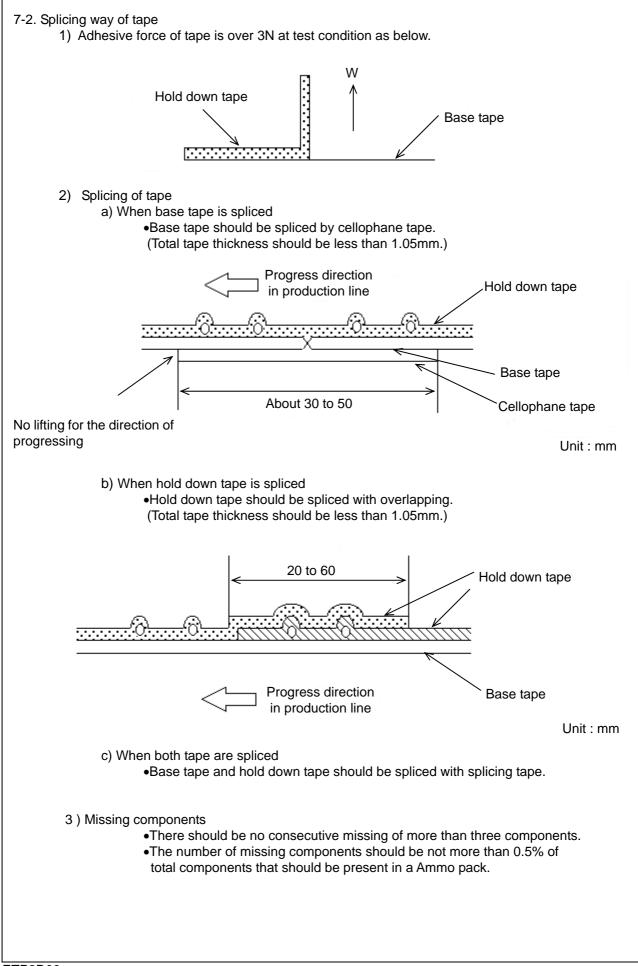
	-		Unit : mm
Item	Code	Dimensions	Remarks
Pitch of component	Р	12.7±1.0	
Pitch of sprocket hole	P0	12.7±0.3	
Lead spacing	F	$5.0\pm^{0.8}_{0.2}$	
Length from hole center to component center	P2	6.35±1.3	Deviation of more standing time of an
Length from hole center to lead	P1	3.85±0.7	Deviation of progress direction
Body diameter	D	Please refer to [ F	art number list ].
Deviation along tape, left or right	ΔS	0±1.0	They include deviation by lead bend .
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom	н	20.0± <sup>1.5</sup>	
planes		20.0±1.0	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	1.0 max.	
Deviation across tape, rear	∆h2		
Portion to cut in case of defect	L	<b>11.0</b> ± <sup>0</sup> <sub>1.0</sub>	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	3.0 max.	
Body thickness	Т	Please refer to [ F	Part number list ].
	•		-

Straight taping type < Lead code : P3 > Pitch of component 15.0mm / Lead spacing 7.5mm



Unit : mm

			Unit . Init
ltem	Code	Dimensions	Remarks
Pitch of component	Р	15.0±2.0	
Pitch of sprocket hole	P0	15.0±0.3	
Lead spacing	F	7.5±1.0	
Length from hole center to component center	P2	7.5±1.5	
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction
Body diameter	D	Please refer to [	Part number list ].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend .
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	н	$20.0\pm^{1.5}_{1.0}$	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	0.0	
Deviation across tape, rear	∆h2	2.0 max.	
Portion to cut in case of defect	L	<b>11.0</b> ± <sup>0</sup> <sub>1.0</sub>	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	3.0 max.	
Body thickness	Т	Please refer to [	Part number list ].



#### EU RoHS

This products of the following crresponds to EU RoHS.

## RoHS

maximum concentration values tolerated by weight in homogeneous materials

- •1000 ppm maximum Lead
- •1000 ppm maximum Mercury
- •100 ppm maximum Cadmium
- •1000 ppm maximum Hexavalent chromium
- •1000 ppm maximum Polybrominated biphenyls (PBB)
- •1000 ppm maximum Polybrominated diphenyl ethers (PBDE)

# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Ceramic Disc Capacitors category:

Click to view products by Murata manufacturer:

Other Similar products are found below :

5AU100JCECA 5AU220JCGCA 5AU560JCJCA DEF2CLH020CA3B NCD103M500Z5UF DEF2CLH030CJ3B 101GHR102K NCD101K1KVY5FF NCD103Z50Z5VTRF NCD220K1KVSLF F471K39S3NR63K7R DEF2CLH040CN3A DEF2CLH080DA3B 564R3DF0T22 C1210N561J102T CD70ZU2GA102MYAKA 8903D0 90410-10 0838-040-X7R0-220K SL102101J060BAND5P JN102MQ35FAAAAKPLP 0841-040-X5U0-103M ZU501103M090B20C6P SL102181J070HAND5P SL102151J070HAND5P ZU501102M050B20C6P SL500180J040B20C2P ZU102103M100B20C0P F121K25S3NN63J5R F121K25S3NP63K7R F121K25S3NR63K7R F122K47S3NP63K7R F151K29S3NR63K7R F222K47S3NN63J7R F681K43S3NR63K7R HVCC103Y6P152MEAX F681K29S3NN63J5R S103Z43Y5VN6TJ5R TCC0805X7R472K501FT C947U392MZVDBA7317 CCK-22N CCK-2P2 CCK-4P7 RDE5C1H102J0ZAH03P CCK-470P 564R30GAD10KA 25YD22-R DHS4E4G141MCXB DEJF3E2472ZB3B DEA1X3F390JC3B