

Reference Specification

DHR Series
High Voltage Lead Type Disc Ceramic Capacitors for General Purpose

Product specifications in this catalog are as of Feb. 2018, and are subject to change or obsolescence without notice.

Please consult the approval sheet before ordering. Please read rating and Cautions first.

⚠ CAUTION

1.OPERATING VOLTAGE

When DC- rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.

Voltage	DG Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage(1)	Pulse Voltage(2)
Positional Measurement	Vo-p	Vo-p	Vp-p V	Vp-p	Vp-p

2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 10 °C on the condition of atmosphere temperature 25 °C. When measuring, use a thermocouple of small thermal capacity-K of ϕ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings.

Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.(Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

3. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. The capacitor is designed to be used in the insulating media, such as epoxy resin, silicone oil, etc.. There must be 3mm or more insulating media for each direction of the capacitor. In case of cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 °C and 15 to 85 %. Use capacitors within 6 months after delivered..

4. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

5. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 °C max. Soldering iron wattage: 50W max. Soldering time: 3.5s max.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

6. Limitation of Applications

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1) Aircraft equipment
- 2) Aerospace equipment
- 3) Undersea equipment

4) Power plant control equipment

- 5) Medical equipment
- 6) Transportation equipment(vehicles, trains, ships, etc.)
- 7) Traffic signal equipment
- 8) Disaster prevention / crime prevention equipment
- 9) Data-processing equipment
- 10) Application of similar complexity and/or reliability requirements to the applications listed in the above.

NOTICE

Cleaning

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

Capacitance change of capacitor

Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit. Please contact us if you need a detail information.

⚠ NOTE

- 1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

EGDR02A - 2 -

1. Application

This specification is applied to High Voltage Lead Type Disc Ceramic Capacitors DHR series used for General Electric equipment.

Do not use these products in any automotive power train or safety equipment including battery chargers for electric vehicles and plug-in hybrids.

2. Rating

2.1 Operating temperature

-25°C to +100°C

2.2 Part number configuration

<u>DHR</u>	<u>4E</u>	<u>4A</u>	<u>102</u>	<u>K</u>	<u>2B</u>	<u>B</u>
Series	Temperature	Rated	Capacitance	Capacitance	Lead	Packing
	characteristic	voltage		tolerance	code	style code

• Temperature characteristic

Code	Temperature	characteristic
4E	Z	ZM

Please confirm detailed specification on [5. Specification and test methods].

Rated voltage

a	
Code	Rated voltage
4A	DC10kV
4B	DC12kV
4C	DC15kV

Capacitance

The first two digits denote significant figures; the last digit denotes the multiplier of 10 in pF. ex.) In case of 102.

$$10 \times 10^2 = 1000 pF$$

• Capacitance tolerance

Please refer to [4. Part number list].

• Lead code

Code	Lead style
2B	Straight long
2F	Straight long

Please refer to [4. Part number list].

Solder coated copper wire is applied for termination.

Packing code

Code	Packing type
В	Bulk type

3. Marking

①Temperature Characteristic: Omitted for nominal body dia. ø8max.

Marked with •(dot): Nominal body dia. ø9 to10.

Marked with ZM: Nominal body dia. ø11 to 14.

Marked with Z : Nominal body dia. ø15min.

②Capacitance: marked with 3 figures. ③Cap. tolerance: marked with code.

(Omitted for nominal body dia. ø8max.)

⑤Manufacturer's identification : Abbreviation ←

(Omitted for nominal body dia. ø14max.)

© Manufactuered Date: marked with code.

(Omitted for nominal body dia. ø14max.)

ex. 6 12 0 1) The last number of A.D. year

1) 2) 3) 2) 2 numbers of the month January→01, ..., December→12

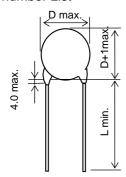
(Example)

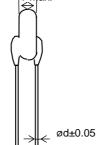
102K Z **€** 10K

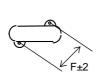
6120

3) Fix No.

4.Part number List







Unit:mm

Temp.	Cap.	Cap. tol.	Customer part number	omer part number Murata Part number		Dimensions (mm)			(mm)	Lead		Pack
Char.	(pF)	(%)	ouotomer part number	Marata Fart Hamber	volt. (kV)	D	Т	F	L	d	code	qty.
ZM	100	±10		DHR4E4A101K2BB	10	8.0	7.3	9.5	35.0	0.65	2B	200
ZM	150	±10		DHR4E4A151K2BB	10	8.0	7.0	9.5	35.0	0.65	2B	200
ZM	220	±10		DHR4E4A221K2BB	10	9.0	7.0	9.5	35.0	0.65	2B	100
ZM	330	±10		DHR4E4A331K2BB	10	10.0	7.0	9.5	35.0	0.65	2B	100
ZM	470	±10		DHR4E4A471K2BB	10	12.0	7.0	9.5	35.0	0.65	2B	100
ZM	680	±10		DHR4E4A681K2BB	10	13.0	7.0	9.5	35.0	0.65	2B	50
ZM	1000	±10		DHR4E4A102K2BB	10	15.0	7.0	9.5	35.0	0.65	2B	50
ZM	100	±10		DHR4E4B101K2BB	12	8.0	7.3	9.5	35.0	0.65	2B	200
ZM	150	±10		DHR4E4B151K2BB	12	9.0	7.3	9.5	35.0	0.65	2B	100
ZM	220	±10		DHR4E4B221K2BB	12	9.0	7.3	9.5	35.0	0.65	2B	100
ZM	330	±10		DHR4E4B331K2BB	12	11.0	7.3	9.5	35.0	0.65	2B	100
ZM	470	±10		DHR4E4B471K2BB	12	12.0	7.3	9.5	35.0	0.65	2B	100
ZM	680	±10		DHR4E4B681K2BB	12	14.0	7.3	9.5	35.0	0.65	2B	50
ZM	1000	±10		DHR4E4B102K2BB	12	16.0	7.3	9.5	35.0	0.65	2B	50
ZM	100	±10		DHR4E4C101K2BB	15	8.0	8.2	9.5	35.0	0.65	2B	200
ZM	150	±10		DHR4E4C151K2BB	15	9.0	8.2	9.5	35.0	0.65	2B	100
ZM	220	±10		DHR4E4C221K2BB	15	10.0	8.2	9.5	35.0	0.65	2B	100
ZM	330	±10		DHR4E4C331K2BB	15	12.0	8.2	9.5	35.0	0.65	2B	100
ZM	470	±10		DHR4E4C471K2BB	15	13.0	8.2	9.5	35.0	0.65	2B	50
ZM	680	±10		DHR4E4C681K2BB	15	15.0	8.2	9.5	35.0	0.65	2B	50
ZM	1000	±10		DHR4E4C102K2FB	15	18.0	8.2	12.7	35.0	0.8	2F	50

EHBDHR04 - 4 -

5. Specification and test methods

No.	1.7		0- '(' '	T (A)			
4	Item Appearance dimensions		Specification	Test method			
1	Appearance dimensions		No marked defect on appearance form and dimensions.	The capacitor should be inspected by naked eyes for visible evidence of defect.			
			Please refer to [Part number list].	Dimensions should be measured with slide			
			i lease relecto [Fart Humber IISt].	calipers.			
2	Marking		To be easily legible.	The capacitor should be inspected by naked			
	Warking		TO be easily legible.	eyes.			
3	Dielectric	Between	No failure	The capacitors shall not be damage when			
	Strength	Lead wires		DC voltage of 150% of the rated voltage are			
	· - J-··			applied between the lead wires for 60 s in			
				insulate liquid or gas.			
				(Charge/discharge current: 50mA max.)			
		Body insulation		The capacitors is placed in the container			
				with metal balls of diameter 1mm so that			
				each lead wires, Short-circuited, is kept			
				approximately 2mm off the balls as shown			
				in the figure, and DC voltage of 3kV is			
				applied for 10 s between capacitor lead wires and small metals.			
				(Charge/discharge current: 50mA max.)			
4	Insulation	Between	10,000MΩ min.	The insulation resistance shall be measured			
	Resistance	Lead wires	10,00010122111111.	with DC 1kV within 60±5 s of charging.			
	(I.R.)						
5	Capacitance		Within the specified tolerance.	The capacitance shall be measured at 20°C			
	•			with 1±0.2kHz and AC5V(r.m.s.) max			
6	6 Dissipation Factor		1.0% max.	Same condition as capacitance.			
	(D.F.)			Same condition de capacitance.			
7	Temperature Chara	acteristic	-4700±1000ppm/°C	The capacitance measurement shall be made at			
	·			each step specified in table.			
				Capacitance change from the value of step 3			
1							
				shall not exceed the limit specified.			
			,	shall not exceed the limit specified.			
				shall not exceed the limit specified. Step 1 2 3 4 5			
	Charge	Appearance	No marked defect.	shall not exceed the limit specified. Step femp 1 2 3 4 5 ZM - - 20±2°C 85±2°C 20±2°C			
	Charge Discharge			shall not exceed the limit specified. Step 1 2 3 4 5			
		Capacitance	No marked defect. Within ±10%	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the			
	Discharge			shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle.			
	Discharge	Capacitance Change	Within ±10%	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage			
	Discharge	Capacitance Change D.F.	Within ±10% 1.5% max.	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles			
	Discharge	Capacitance Change D.F.	Within $\pm 10\%$ 1.5% max. 5,000M Ω min.	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition.			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength (Between	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. E:Direct-current			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Step 2 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Cycle 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored Figure 20±2°C 85±2°C 20±2°C Cycle 20±2°C 20±2°C C			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength (Between	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength (Between	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. ■ 6 s ■ E:Direct-current Voltage source Co:Suppled energy for Cx. X			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength (Between	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Significantly a condition of the process of the proce			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength (Between	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Significant condition of the process o			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength (Between	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Significantly a condition of the product of the prod			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength (Between	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Significant condition of the process o			
	Discharge Test	Capacitance Change D.F. I.R. Dielectric Strength (Between	Within $\pm 10\%$ 1.5% max. 5,000M Ω min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. E:Direct-current Voltage source Co:Suppled energy for Cx. Coz + 10Cx Cx Specimen R1:Circuit protective resistor (300kΩ) R2:Current limiting Resistor (E/10Ω)			
	Discharge	Capacitance Change D.F. I.R. Dielectric Strength (Between Lead wires)	Within ±10% 1.5% max. 5,000MΩ min. No failure	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Significantly a condition of the product of the prod			
	Discharge Test	Capacitance Change D.F. I.R. Dielectric Strength (Between Lead wires)	Within ±10% 1.5% max. 5,000MΩ min. No failure Lead wire shall not cut off.	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Significantly a product of the produc			
	Discharge Test	Capacitance Change D.F. I.R. Dielectric Strength (Between Lead wires)	Within ±10% 1.5% max. 5,000MΩ min. No failure Lead wire shall not cut off.	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20±2°C 85±2°C 20±2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. E:Direct-current Voltage source Co:Suppled energy for Cx. Coy ± 10 Cx Cx: Specimen R1:Circuit protective resistor (300kΩ) R2:Current limiting Resistor (E/10Ω) Fix the body of capacitor, apply a tensile weight gradually to each lead wire in the			
	Discharge Test	Capacitance Change D.F. I.R. Dielectric Strength (Between Lead wires)	Within ±10% 1.5% max. 5,000MΩ min. No failure Lead wire shall not cut off.	shall not exceed the limit specified. Step 1 2 3 4 5 ZM 20 \pm 2°C 85 \pm 2°C 20 \pm 2°C Charge discharge test shall be measured in the following test circuit and cycle. Applied voltage: rated voltage Cycle numbers: 20,000 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition. Final Capacitor shall be stored Final Capacitor shall be			
	Discharge Test	Capacitance Change D.F. I.R. Dielectric Strength (Between Lead wires)	Within ±10% 1.5% max. 5,000MΩ min. No failure Lead wire shall not cut off.	shall not exceed the limit specified. Step			
	Discharge Test	Capacitance Change D.F. I.R. Dielectric Strength (Between Lead wires)	Within ±10% 1.5% max. 5,000MΩ min. No failure Lead wire shall not cut off.	shall not exceed the limit specified. Step 1			
	Discharge Test	Capacitance Change D.F. I.R. Dielectric Strength (Between Lead wires)	Within ±10% 1.5% max. 5,000MΩ min. No failure Lead wire shall not cut off.	shall not exceed the limit specified. Step 1			
	Discharge Test	Capacitance Change D.F. I.R. Dielectric Strength (Between Lead wires)	Within ±10% 1.5% max. 5,000MΩ min. No failure Lead wire shall not cut off.	shall not exceed the limit specified. Step 1			

No.	Item		Specification	Test method
10			Lead wire shall be soldered with uniformly coated on the axial direction over 3/4 of the circumferential direction.	The lead wire shall be dipped into a 25% ethanol solution of rosin and then into molten solder of 235±5°C for 2±0.5 s. In both cases the depth of dipping is up to about 1.5 to 2.0mm from the root of lead wires. Temp. of solder: Lead Free Solder (Sn-3Ag-0.5Cu) 245±5°C H63 Eutectic Solder 235±5°C
11	Soldering Effect (Non-preheat)	Appearance Capacitance Change Dielectric Strength (Between lead wires)	No marked defect. Within±10% No failure	The lead wires shall be immersed into the melted solder of 350±10°C up to about 1.5 to 2.0mm from the main body for 3.5±0.5 s. Post-treatment: Capacitor shall be stored for 24±2 h at room condition.
12	Soldering effect (On-preheat)	Appearance Capacitance Change Dielectric Strength (Between lead wires)	No marked defect. Within±10% No failure	First the capacitor should be stored at 120+0/-5°C for 60+0/-5 s. Then, as in figure, the lead wires should be immersed solder of 260+0/-5°C up to 1.5 to 2.0mm from the root of terminal for 7.5+0/-1 s. Thermal insulating Capacitor to 2.0mm Post-treatment: Capacitor should be stored for 24±2 h at room condition.
13	Humidity (Under steady state)	Appearance Capacitance Change D.F. I.R. Dielectric Strength (Between lead wires)	No marked defect. Within±10% 1.5% max. 5000MΩ min. No failure	Set the capacitor for 240±8 h at 40±2°C in 90 to 95% humidity. Post-treatment: Capacitor shall be stored for 1 to 2 h at room condition. (Charge/discharge current: 50mA max.)
14	Life (High temperature load)	Appearance Capacitance	No marked defect. Within±10% 1.5% max. 5000MΩ min. No failure	Apply a DC voltage of 125% of the rated voltage for 1000+48/-0 h in silicon oil at 100±2°C. Post-treatment: Capacitor shall be stored for 24±2 h at room condition. (Charge/discharge current:50mA max.)
15	Temperature Cycling	Appearance Capacitance Change D.F. I.R. Dielectric Strength (Between lead wires)	No marked defect. Within±10% 1.5% max. 5000MΩ min. No failure	Step Temperature(°C) Time 1 -30 30 min 2 +100 30 min Temperature cycling shall be measured in the following test. Cycle numbers: 5 cycles Post-treatment: Capacitor shall be stored for 4 h at room condition.

Note) Tests for Dielectric strength(Between lead wires) ,Charge/Discharge test, Humidity , Life and Temperature Cycling shall be performed with specimens having molded resin (MR1023C:made by Murata) extending over 3mm on all the surface.

Room condition

Temperature:15~35°C Relative Humidity:45~75% Atmospheric pressure:86~106kPa

6. Packing Specification

Packaging Styles: Bulk type

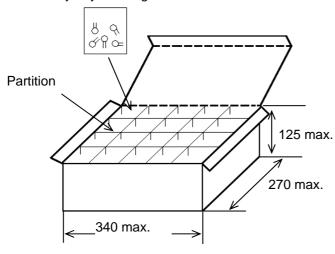
(Packing style code : B)

*2

The number of packing = Packing quantity \times n

The size of packing case and packing way

Polyethylene bag



*1 : Please refer to [Part number list].

*2 : Standard n = 20 (bag)

Note)

The outer package and the number of outer packing be changed by the order getting amount.

Unit: mm

EKBLDHR01 -7-

EU RoHS RoHS指令への対応

This products of the following crresponds to EU RoHS 当製品は以下の欧州RoHSに対応しています。

(1) RoHS

EU RoHs 2011/65/EC compliance 2011/65/EC(改正RoHS指令)に対応

maximum concentration values tolerated by weight in homogeneous materials

- •1000 ppm maximum Lead
- •1000 ppm maximum Mercury
- •100 ppm maximum Cadmium
- •1000 ppm maximum Hexavalent chromium
- •1000 ppm maximum Polybrominated biphenyls (PBB)
- •1000 ppm maximum Polybrominated diphenyl ethers (PBDE)

鉛:1000ppm以下 水銀:1000ppm以下 カドミウム:100ppm以下 六価クロム:1000ppm以下

ポリ臭化ビフェニル(PBB): 1000ppm以下

ポリ臭化ジフェニルエーテル(PBDE): 1000ppm以下

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Ceramic Disc Capacitors category:

Click to view products by Murata manufacturer:

Other Similar products are found below:

5AU100JCECA 5AU220JCGCA 5AU560JCJCA DEF2CLH020CA3B NCD103M500Z5UF DEF2CLH030CJ3B 101GHR102K
NCD101K1KVY5FF NCD103Z50Z5VTRF NCD220K1KVSLF F471K39S3NR63K7R DEF2CLH040CN3A DEF2CLH080DA3B
564R3DF0T22 C1210N561J102T CD70ZU2GA102MYAKA 8903D0 90410-10 0838-040-X7R0-220K SL102101J060BAND5P
JN102MQ35FAAAAKPLP 0841-040-X5U0-103M ZU501103M090B20C6P SL102181J070HAND5P SL102151J070HAND5P
ZU501102M050B20C6P SL500180J040B20C2P ZU102103M100B20C0P F121K25S3NN63J5R F121K25S3NP63K7R
F121K25S3NR63K7R F122K47S3NP63K7R F151K29S3NR63K7R F222K47S3NN63J7R F681K43S3NR63K7R HVCC103Y6P152MEAX
F681K29S3NN63J5R S103Z43Y5VN6TJ5R TCC0805X7R472K501FT C947U392MZVDBA7317 CCK-22N CCK-2P2 CCK-4P7
RDE5C1H102J0ZAH03P CCK-470P 564R30GAD10KA 25YD22-R DHS4E4G141MCXB DEJF3E2472ZB3B DEA1X3F390JC3B