

Isolated, 12 Vout, 30A, Half-Brick DC/DC Converters



FEATURES

- 12Vout @ 30A (360W)
- Industry Standard "Half Brick" package
- High Efficiency: up to 93%
- Outstanding thermal performance
- Optional Baseplate for conduction cooled applications
- No output reverse conduction
- Input to Output Isolation, 2250Vdc (Basic)
- Input under-voltage lockout
- On/Off Control (Positive or Negative Logic)
- Output over-voltage protection
- Thermal shutdown
- Output short circuit protection (hiccup) technique)

PRODUCT OVERVIEW

For applications requiring improved electrical and thermal performance, consider Murata's new HPH series "Half Brick" DC/DC power converters. These compact modules measure 2.4" X 2.3" X 0.4" (61 X 58 X 10.2mm) and offer the industry-standard Half Brick footprint.

The module will provide a 12Vdc output at 30Amps and accept a wide range input voltage of 36-75Vdc. The HPH topology offers high efficiency up to 93%, tight line and load regulation, low ripple/ noise, and a fast dynamic load response. A singleboard, highly optimized thermal design contributes to the superior thermal performance.

These DC/DC's provide output trim, sense pins, and primary side on/off control. Standard features also include input under-voltage shutdown, output over-voltage protection, output short-circuit/current limiting protection, and thermal shutdown.

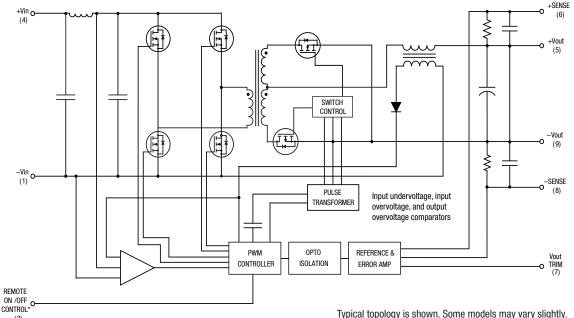








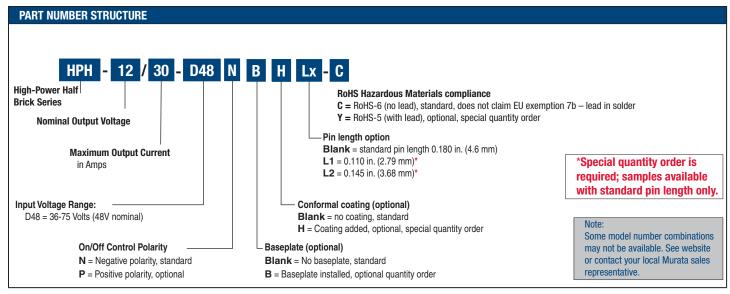
Figure 1. Simplified Schematic

* Can be ordered with positive (standard) or negative (optional) polarity.

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PERFORMANCE SPECIFICATIONS SUMMARY AND ORDERING GUIDE															
	Output					Input				Efficiency					
	Vout	Іоит	Power	R/N (m\	/ pk-pk)	Regulatio	n (Max.)	VIN Nom.	Range	lin, no	lın, full	lin, full		Package (Case/	
Root Model ①	(Volts)	(Amps, Max.) ②	(Watts)	Тур.	Max.	Line	Load	(Volts)	(Volts)				Тур.	٠,	out)
HPH-12/30-D48	12	30	360	100	200	±0.05%	±0.1%	48	36-75	150	8.1	92%	93%	C61	P17

- ① Please refer to the full model number structure for additional ordering part numbers and options.
- ② Please refer to maximum input/output voltage graph.
- 3 All specifications are at nominal line voltage and full load, +25°C. unless otherwise noted. See detailed specifications.
- 4 Full power continuous output requires baseplate installation. Please refer to the derating curves.



Note: Because of the high currents, wire the appropriate input, output and common pins in parallel. Be sure to use adequate PC board etch. If not sufficient, install additional discrete wiring.



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FUNCTIONAL SPECIFICATIONS

Page	ABSOLUTE MAXIMUM RATINGS	Conditions ①	Minimum	Typical/Nominal	Maximum	Units
Ambient temperature range				Typical/Nollillal		
Injust Voltage, Translent	mput voltage, continuous		•			
Topic design, framework Topic on Finace, carpation Topic on Finace,					03	Vuc
	Input Voltage, Transient		0		100	Vdc
Power an or off, referred to Vin 0	Isolation Voltage				2250	Vdc
Output Deworr	Input Reverse Polarity	None, install external fuse		None		Vdc
Current Curr	On/Off Remote Control	Power on or off, referred to -Vin	0		50	Vdc
Substitution Subs	Output Power		0		450	W
Storage Pemperature Range	Output Current	7	0		30	Α
Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions may adversely affect long-term reliability. Proper operation under conditions may adversely affect long-term reliability. Proper operation under conditions between the long time of the property property and the long time of the long time. The long time of the long time	Storage Temperature Range	·	-55		125	°C
Islaed in the Performance/Functional Specifications Table is not implied nor recommended.				ng-term reliability. Proper on		
Montain March Ma			iay aavoroory arroot ior	ig term remability. I reper op	oration andor containion	outer than those
Operating voltage range 2	·	no lable to flet implied flet recommended.				
Turn Orf. Start-up threshold Rising input voltage 33 34 35 37 37 37 37 37 37 37			36	18	75	Vdc
None Falling input voltage 31 32 33 Voltage		Picing input voltage				
None						
Recommended External Fuse Fast blow Pi Pi Pi Pi Pi Pi Pi P			J1		- 33	_
Internal Filter Type		· · · · · · · · · · · · · · · · · · ·				
		i dot DIOW				
Full Load Conditions Vin = mominal 8.06 8.23 A Low Line Vin = minimum 10.75 10.98 A A Low Line Vin = minimum 10.75 10.98 A A A Simulation Shart Circuit Short Circuit					<u> </u>	
Low Line		Vin – nominal		8.06	R 23	Δ
Invision Transient						_
Dutyput in Short Circuit		viii — i/iiiiiiidiii			10.00	
No Load					100	_
Standby Mode (Off, UV, OT)		lout = minimum_unit-0N				
Reflected (back) ripple current (a) Measured at input with specified filter Selected (back) ripple current (b) Vin=48V, full load 92 93 93 % %		lout = milimum, umt=or				
Selection Vin=48V, full load 92 93 93 %		Measured at input with specified filter				
Fiftienery Win-49X, full load 92 93 93 % %		wicasured at hipat with specified filter		00	100	IIIA, IIIVIO
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Solation Voltage: no baseplate Input to output, continuous Z250 Vdc	Linciency					
Input to output, continuous 2250	Isolation	VIII—30V, Iuli Ioau	JL	30		/0
Input to dusput, continuous TBD Vdc		Input to output, continuous	2250			Vdc
Input to Baseplate, continuous 1500	isolation voltage. No basepiate					
Substitute Startup Time Power On to Vout regulated 10-90% (50% resistive load) Startup Time Remote On to 10% to 10% vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Soft-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/µSec Dynamic Load Peak Deviation Dynamic Load Peak Dynamic Load Peak Deviation Dynamic Load Peak Deviation Dynami	Isolation Voltage: with hasenlate					Vuo
Insulation Safety Rating	looladon voltage. With baseplate					
Solation Resistance 100	Insulation Safety Rating	Output to Bussipiate, continuous	1000	hasic		
Solation Capacitance	· · ·					Mohm
Safety (Designed to meet the following requirements) UL-60950-1, CSA-C22.2 No.60950-1, and Edition Yes Hours x 10 ⁶						
Per MIL-HDBK-217F Far	•	III -60950-1 CSA-C22 2 No 60950-1		,		pi pi
Calculated MTBF Ground benign, Tambient=+30°C Per Telcordia SR332, issue 1 class 3, ground fixed, Tambient=+40°C 1.4 Hours x 10°	, , ,	IEC/EN60950-1, 2nd Edition		Yes		
Calculated MTBF				TBD		Hours x 10 ⁶
1.4	Calculated MTBF					
DYNAMIC CHARACTERISTICS				1.4		Hours x 10 ⁶
Fixed Switching Frequency 350 400 450 KHz Startup Time Power On to Vout regulated 10-90% (50% resistive load) 20 mS Startup Time Remote ON to 10% Vout (50% resistive load) 20 mS Dynamic Load Response 50-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/μSec 200 400 μSec Dynamic Load Peak Deviation same as above ±400 mV FEATURES and OPTIONS Remote On/Off Control ® "N" suffix: Negative Logic, ON state ON = Pin grounded or external voltage 0 0.8 V Negative Logic, OFF state OFF = Pin open or external voltage 3.5 13.5 V Control Current ON = Pin open or external voltage 3.5 13.5 V Positive Logic, ON state ON = Pin open or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current Open collector/drain 1 2 mA	DVNAMIC CHARACTERISTICS	iixeu, iaiiibleiil=+40 C				
Power On to Vout regulated 10-90% (50% resistive load) 20 mS			350	400	450	V □-
Startup Time (50% resistive load) 20 mS		Power On to Vout regulated 10, 000/	300	400	400	ΚΠΖ
Startup Time Remote ON to 10% Vout (50% resistive load) 20 mS Dynamic Load Response 50-75-50% load step, settling time to within ±1% of Vout di/dt = 1 A/μSec 200 400 μSec Dynamic Load Peak Deviation same as above ±400 mV FEATURES and OPTIONS Remote On/Off Control ⊕ "N" suffix: Negative Logic, ON state ON = Pin grounded or external voltage 0 0.8 V Negative Logic, OFF state OFF = Pin open or external voltage 3.5 13.5 V Control Current open collector/drain 1 2 mA "P" suffix: Positive Logic, ON state ON = Pin open or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current open collector/drain 1 2 mA	Startup Time				20	mS
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FEATURES and OPTIONS Remote On/Off Control ⊕ "N" suffix: Negative Logic, ON state OFF = Pin open or external voltage	•	±1% of Vout di/dt = 1 A/μSec			400	· ·
Remote On/Off Control ⊕ "N" suffix: Negative Logic, ON state OFF = Pin open or external voltage OFF = Pin open or external voltage OFF = Vo		same as above		±400		mV
"N" suffix: Negative Logic, ON state ON = Pin grounded or external voltage 0 0.8 V Negative Logic, OFF state OFF = Pin open or external voltage 3.5 13.5 V Control Current open collector/drain 1 2 mA "P" suffix: Positive Logic, ON state ON = Pin open or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current open collector/drain 1 2 mA						
Negative Logic, ON state ON = Pin grounded or external voltage 0 0.8 V Negative Logic, OFF state OFF = Pin open or external voltage 3.5 13.5 V Control Current open collector/drain 1 2 mA "P" suffix: Positive Logic, ON state ON = Pin open or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current open collector/drain 1 2 mA						
Negative Logic, OFF state OFF = Pin open or external voltage 3.5 13.5 V Control Current open collector/drain 1 2 mA "P" suffix: Positive Logic, ON state ON = Pin open or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current open collector/drain 1 2 mA						1
Control Current open collector/drain 1 2 mA "P" suffix: Positive Logic, ON state ON = Pin open or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current open collector/drain 1 2 mA						-
"P" suffix: Positive Logic, ON state ON = Pin open or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current open collector/drain 1 2 mA		·	3.5			
Positive Logic, ON state ON = Pin open or external voltage 3.5 13.5 V Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current open collector/drain 1 2 mA		open collector/drain		1	2	mA
Positive Logic, OFF state OFF = Pin grounded or external voltage 0 1 V Control Current open collector/drain 1 2 mA						
Control Current open collector/drain 1 2 mA						
			0			
Base Plate "B" suffix				1	2	mA
	Base Plate	"B" suffix				



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FUNCTIONAL SPECIFICATIONS (CONT.)

OUTPUT					
Total Output Power	See Derating	0.0		360	W
Voltage	9				'
Nominal Output Voltage	No trim	11.88	12.00	12.12	Vdc
Setting Accuracy	At 50% load	-1		1	% of Vnom.
Output Voltage Range	User-adjustable [6]	-10		10	% of Vnom.
Overvoltage Protection	Via magnetic feedback		14.5		Vdc
Current					'
Output Current Range		0		30	A
Minimum Load			No minimum load		
Current Limit Inception ⑤	98% of Vnom., after warmup	31.5	37	44	А
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within ±1% of Vout, non-latching		6.6		А
Short Circuit Duration	Output shorted to ground, no damage		Continuous		
(remove short for recovery)	1 0 /		Oonanaaa		
Short circuit protection method	Current limiting				
Regulation ®					
Line Regulation	Vin=min. to max. Vout=nom., 50% load			±0.05	%
Load Regulation	lout=min. to max. Vin=48V.			±0.1	%
Ripple and Noise ⑦	5 Hz- 20 MHz BW		100	200	mV pk-pk
Temperature Coefficient	At all outputs		0.02		% of Vnom./°C
Maximum Capacitive Loading	Cap. ESR=<0.02Ω, Full resistive load	0		10,000	E
(10% ceramic, 90% Oscon)	Cap. ESR=<0.0211, Full resistive load	U		10,000	μF
MECHANICAL (Through Hole Models)					
Outline Dimensions (no baseplate)	Cxx case		2.3 X 2.4 X 0.4		Inches
	WxLxH (Please refer to outline drawing)		58.4 X 60.96 X 10.2		mm
Outline Dimensions (with baseplate)			2.3 X 2.4 X 0.5		Inches
			36.8x58.4x12.7		mm
Weight (no baseplate)			TBD		Ounces
			TBD		Grams
Weight (with baseplate)					Ounces
					Grams
Through Hole Pin Diameter					Inches
					mm
Through Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate				μ-inches
	Gold overplate				μ-inches
Case or Baseplate Material			Aluminum		
ENVIRONMENTAL					
Operating Ambient Temperature Range	With derating, full power, natural convection, no baseplate	-40		85	°C
Operating Ambient Temperature Range with Baseplate	No derating, with baseplate, full power	-40		120	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown	Measured at hotspot		120		°C
Electromagnetic Interference	External filter required		В		Class
Conducted, EN55022/CISPR22	External mor roganou				
Radiated, EN55022/CISPR22			В		Class
Relative humidity, non-condensing	To +85°C	10		90	%RH
Altitude		-500		10,000	feet
(must derate -1%/1000 feet)		-152		3048	meters
RoHS rating			RoHS-6 or RoHS-5 (specify)		

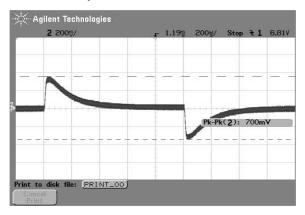
Notes

- ① Unless otherwise noted, all specifications are at nominal input voltage, nominal output voltage and full load. General conditions are +25° Celsius ambient temperature, near sea level altitude, natural convection airflow. All models are tested and specified with external parallel 1 μF and 10 μF multi-layer ceramic output capacitors. No external input capacitors are installed. All capacitors are low-ESR types wired close to the converter. These capacitors are necessary for our test equipment and may not be needed in the user's application.
- The module will operate when input voltage is within the 36-75V Operating Voltage Range, Output regulation at full load will be achieved only when Vin >= 39V
- ® Input (back) ripple current is tested and specified over 5 Hz to 20 MHz bandwidth. Input filtering is Cbus = 0 μ F, Cin = 100 μ F and Lbus = < 4.7 μ H.
- ④ The Remote On/Off Control is referred to -Vin.
- ⑤ Over-current protection is non-latching with auto reovery (Hiccup)
- ® Regulation specifications describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.
- ② Output Ripple & Noise is measured with 750 μF capacitance, 10% ceramic and 90% OSCON. 20 MHz bandwidth.

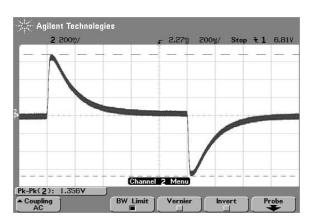
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TYPICAL PERFORMANCE DATA

Stepload Transient Response

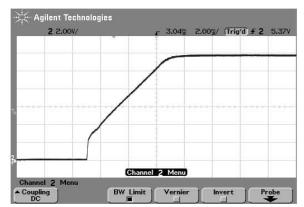


Transient Response (Load 50% to 75%)

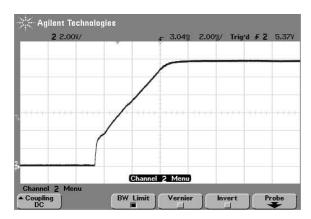


Transient Response (Load 50% to 100%)

On/Off Enable Start-up

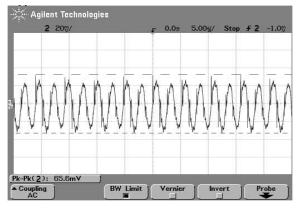


Enable Start-up (Vin=48V lout=30A)

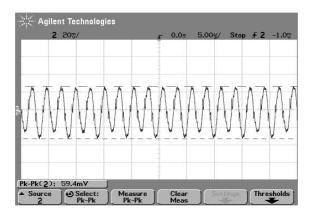


Enable Start-up (Vin=48V lout=0A)

Ripple and Noise

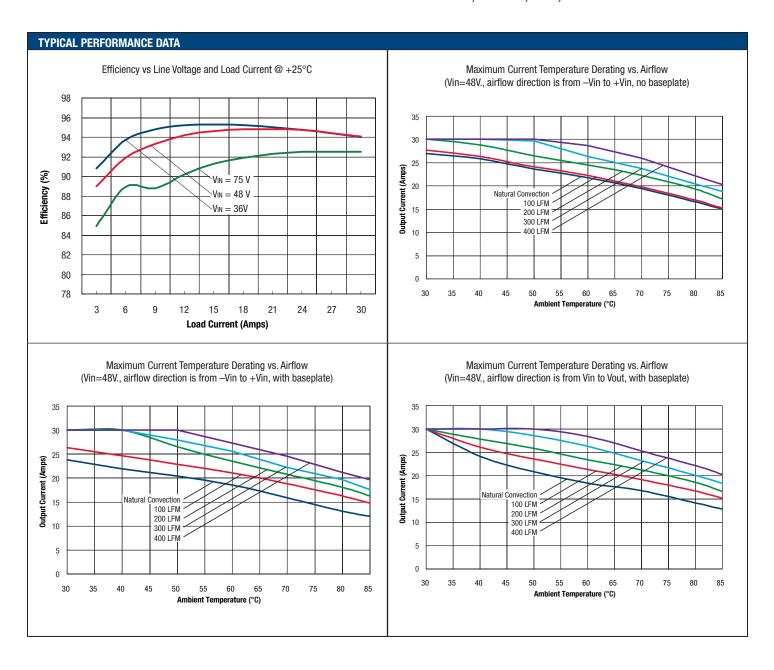


Ripple and Noise Waveform (Vin=48V lout=30A)

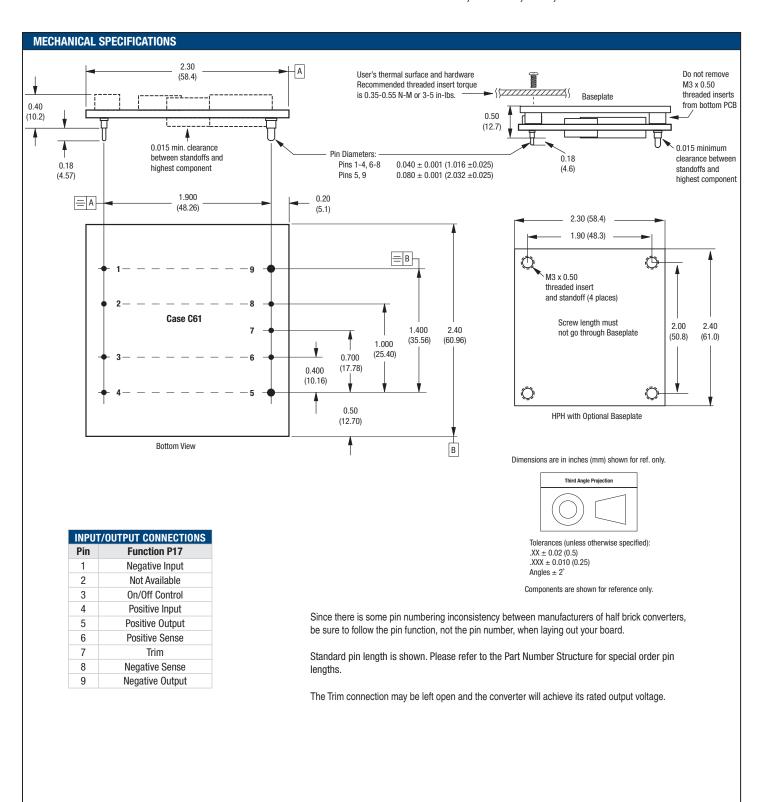


Ripple and Noise Waveform (Vin=48V lout=0A)

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TECHNICAL NOTES

Input Fusing

Certain applications and/or safety agencies may require fuses at the inputs of power conversion components. Fuses should also be used when there is the possibility of sustained input voltage reversal which is not current-limited. For greatest safety, we recommend a fast blow fuse installed in the ungrounded input supply line.

The installer must observe all relevant safety standards and regulations. For safety agency approvals, install the converter in compliance with the end-user safety standard, i.e. IEC/EN/UL 60950-1.

Input Reverse-Polarity Protection

If the input voltage polarity is reversed, an internal diode will become forward biased and likely draw excessive current from the power source. If this source is not current-limited or the circuit appropriately fused, it could cause permanent damage to the converter.

Input Under-Voltage Shutdown and Start-Up Threshold

Under normal start-up conditions, converters will not begin to regulate properly until the ramping-up input voltage exceeds and remains at the Start-Up Threshold Voltage (see Specifications). Once operating, converters will not turn off until the input voltage drops below the Under-Voltage Shutdown Limit. Subsequent restart will not occur until the input voltage rises again above the Start-Up Threshold. This built-in hysteresis prevents any unstable on/off operation at a single input voltage.

Users should be aware however of input sources near the Under-Voltage Shutdown whose voltage decays as input current is consumed (such as capacitor inputs), the converter shuts off and then restarts as the external capacitor recharges. Such situations could oscillate. To prevent this, make sure the operating input voltage is well above the UV Shutdown voltage AT ALL TIMES.

Start-Up Time

Assuming that the output current is set at the rated maximum, the Vin to Vout Start-Up Time (see Specifications) is the time interval between the point when the ramping input voltage crosses the Start-Up Threshold and the fully loaded regulated output voltage enters and remains within its specified accuracy band. Actual measured times will vary with input source impedance, external input capacitance, input voltage slew rate and final value of the input voltage as it appears at the converter.

These converters include a soft start circuit to moderate the duty cycle of its PWM controller at power up, thereby limiting the input inrush current.

The On/Off Remote Control interval from On command to Vout regulated assumes that the converter already has its input voltage stabilized above the Start-Up Threshold before the On command. The interval is measured from the On command until the output enters and remains within its specified accuracy band. The specification assumes that the output is fully loaded at maximum rated current. Similar conditions apply to the On to Vout regulated specification such as external load capacitance and soft start circuitry.

Input Source Impedance

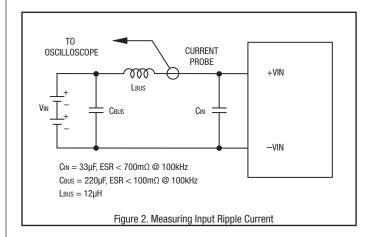
These converters will operate to specifications without external components, assuming that the source voltage has very low impedance and reasonable input voltage regulation. Since real-world voltage sources have finite

impedance, performance is improved by adding external filter components. Sometimes only a small ceramic capacitor is sufficient. Since it is difficult to totally characterize all applications, some experimentation may be needed. Note that external input capacitors must accept high speed switching currents.

Because of the switching nature of DC/DC converters, the input of these converters must be driven from a source with both low AC impedance and adequate DC input regulation. Performance will degrade with increasing input inductance. Excessive input inductance may inhibit operation. The DC input regulation specifies that the input voltage, once operating, must never degrade below the Shut-Down Threshold under all load conditions. Be sure to use adequate trace sizes and mount components close to the converter.

I/O Filtering, Input Ripple Current and Output Noise

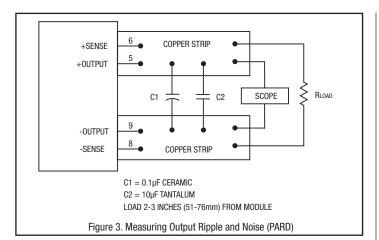
All models in this converter series are tested and specified for input reflected ripple current and output noise using designated external input/output components, circuits and layout as shown in the figures below. External input capacitors (Cin in the figure) serve primarily as energy storage elements, minimizing line voltage variations caused by transient IR drops in the input conductors. Users should select input capacitors for bulk capacitance (at appropriate frequencies), low ESR and high RMS ripple current ratings. In the figure below, the Cbus and Lbus components simulate a typical DC voltage bus. Your specific system configuration may require additional considerations. Please note that the values of Cin, Lbus and Cbus will vary according to the specific converter model.



In critical applications, output ripple and noise (also referred to as periodic and random deviations or PARD) may be reduced by adding filter elements such as multiple external capacitors. Be sure to calculate component temperature rise from reflected AC current dissipated inside capacitor ESR. Our Application Engineers can recommend potential solutions.

In figure 3, the two copper strips simulate real-world printed circuit impedances between the power supply and its load. In order to minimize circuit errors and standardize tests between units, scope measurements should be made using BNC connectors or the probe ground should not exceed one half inch and soldered directly to the fixture.

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Floating Outputs

Since these are isolated DC/DC converters, their outputs are "floating" with respect to their input. The essential feature of such isolation is ideal ZERO CURRENT FLOW between input and output. Real-world converters however do exhibit tiny leakage currents between input and output (see Specifications). These leakages consist of both an AC stray capacitance coupling component and a DC leakage resistance. When using the isolation feature, do not allow the isolation voltage to exceed specifications. Otherwise the converter may be damaged. Designers will normally use the negative output (-Output) as the ground return of the load circuit. You can however use the positive output (+Output) as the ground return to effectively reverse the output polarity.

Minimum Output Loading Requirements

These converters employ a synchronous rectifier design topology. All models regulate within specification and are stable under no load to full load conditions. Operation under no load might however slightly increase output ripple and noise.

Thermal Shutdown

To prevent many over temperature problems and damage, these converters include thermal shutdown circuitry. If environmental conditions cause the temperature of the DC/DC's to rise above the Operating Temperature Range up to the shutdown temperature, an on-board electronic temperature sensor will power down the unit. When the temperature decreases below the turn-on threshold, the converter will automatically restart. There is a small amount of hysteresis to prevent rapid on/off cycling. The temperature sensor is typically located adjacent to the switching controller, approximately in the center of the unit. See the Performance and Functional Specifications.

CAUTION: If you operate too close to the thermal limits, the converter may shut down suddenly without warning. Be sure to thoroughly test your application to avoid unplanned thermal shutdown.

Temperature Derating Curves

The graphs in this data sheetillustrate typical operation under a variety of conditions. The Derating curves show the maximum continuous ambient air temperature and decreasing maximum output current which is acceptable under increasing forced airflow measured in Linear Feet per Minute ("LFM"). Note that these are AVERAGE measurements. The converter will accept brief increases in temperature and/or current or reduced airflow as long as the average is not exceeded.

Note that the temperatures are of the ambient airflow, not the converter itself which is obviously running at higher temperature than the outside air. Also note that very low flow rates (below about 25 LFM) are similar to "natural convection", that is, not using fan-forced airflow.

MPS makes Characterization measurements in a closed cycle wind tunnel with calibrated airflow. We use both thermocouples and an infrared camera system to observe thermal performance. As a practical matter, it is quite difficult to insert an anemometer to precisely measure airflow in most applications. Sometimes it is possible to estimate the effective airflow if you thoroughly understand the enclosure geometry, entry/exit orifice areas and the fan flowrate specifications. If in doubt, contact MPS to discuss placement and measurement techniques of suggested temperature sensors.

<u>CAUTION:</u> If you routinely or accidentally exceed these Derating guidelines, the converter may have an unplanned Over Temperature shut down. Also, these graphs are all collected at slightly above Sea Level altitude. Be sure to reduce the derating for higher density altitude.

Output Overvoltage Protection

This converter monitors its output voltage for an over-voltage condition using an on-board electronic comparator. The signal is optically coupled to the primary side PWM controller. If the output exceeds OVP limits, the sensing circuit will power down the unit, and the output voltage will decrease. After a time-out period, the PWM will automatically attempt to restart, causing the output voltage to ramp up to its rated value. It is not necessary to power down and reset the converter for this automatic OVP-recovery restart.

If the fault condition persists and the output voltage climbs to excessive levels, the OVP circuitry will initiate another shutdown cycle. This on/off cycling is referred to as "hiccup" mode. It safely tests full current rated output voltage without damaging the converter.

Output Fusing

The converter is extensively protected against current, voltage and temperature extremes. However your output application circuit may need additional protection. In the extremely unlikely event of output circuit failure, excessive voltage could be applied to your circuit. Consider using an appropriate fuse in series with the output.

Output Current Limiting

As soon as the output current increases to its maximum rated value, the DC/DC converter will enter a current-limiting mode. The output voltage will decrease proportionally with increases in output current, thereby maintaining a somewhat constant power output. This is commonly referred to as power limiting.

Current limiting inception is defined as the point at which full power falls below the rated tolerance. See the Performance/Functional Specifications. Note particularly that the output current may briefly rise above its rated value. This enhances reliability and continued operation of your application. If the output current is too high, the converter will enter the short circuit condition.

Output Short Circuit Condition

When a converter is in current-limit mode, the output voltage will drop as the output current demand increases. If the output voltage drops too low, the magnetically coupled voltage used to develop primary side voltages will also drop, thereby shutting down the PWM controller. Following a time-out period, the PWM will restart, causing the output voltage to begin ramping up to its

Isolated, 12 Vout, 30A, Half-Brick DC/DC Converters

appropriate value. If the short-circuit condition persists, another shutdown cycle will initiate. This on/off cycling is called "hiccup mode". The hiccup cycling reduces the average output current, thereby preventing excessive internal temperatures. A short circuit can be tolerated indefinitely.

Remote Sense Input

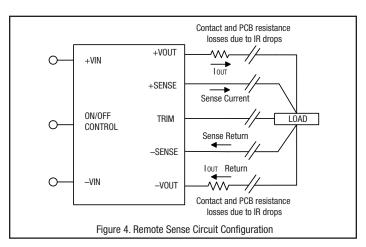
Sense inputs compensate for output voltage inaccuracy delivered at the load. This is done by correcting voltage drops along the output wiring such as moderate IR drops and the current carrying capacity of PC board etch. Sense inputs also improve the stability of the converter and load system by optimizing the control loop phase margin.

Note: The Sense input and power Vout lines are internally connected through low value resistors to their respective polarities so that the converter can operate without external connection to the Sense. Nevertheless, if the Sense function is not used for remote regulation, the user should connect +Sense to +Vout and -Sense to -Vout at the converter pins.

The remote Sense lines carry very little current. They are also capacitively coupled to the output lines and therefore are in the feedback control loop to regulate and stabilize the output. As such, they are not low impedance inputs and must be treated with care in PC board layouts. Sense lines on the PCB should run adjacent to DC signals, preferably Ground. In cables and discrete wiring, use twisted pair, shielded tubing or similar techniques.

Please observe Sense inputs tolerance to avoid improper operation:

 $[Vout(+) - Vout(-)] - [Sense(+) - Sense(-)] \le 10\%$ of Vout



Output overvoltage protection is monitored at the output voltage pin, not the Sense pin. Therefore excessive voltage differences between Vout and Sense together with trim adjustment of the output can cause the overvoltage protection circuit to activate and shut down the output.

Power derating of the converter is based on the combination of maximum output current and the highest output voltage. Therefore the designer must insure:

(Vout at pins) x (lout) \leq (Max. rated output power)

Trimming the Output Voltage

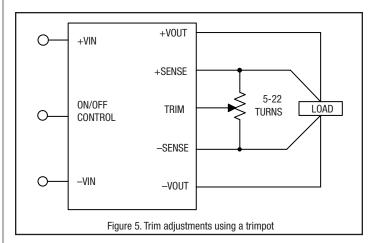
The Trim input to the converter allows the user to adjust the output voltage over the rated trim range (please refer to the Specifications). In the trim equations and circuit diagrams that follow, trim adjustments use either a trimpot or

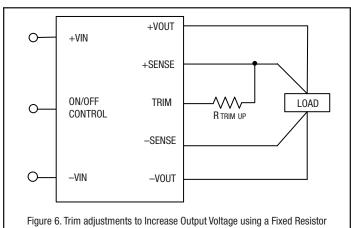
a single fixed resistor connected between the Trim input and either the +Sense or -Sense terminals. (On some converters, an external user-supplied precision DC voltage may also be used for trimming). Trimming resistors should have a low temperature coefficient (±100 ppm/deg.C or less) and be mounted close to the converter. Keep leads short. If the trim function is not used, leave the trim unconnected. With no trim, the converter will exhibit its specified output voltage accuracy.

There are two CAUTION's to be aware for the Trim input:

<u>CAUTION:</u> To avoid unplanned power down cycles, do not exceed EITHER the maximum output voltage OR the maximum output power when setting the trim. Be particularly careful with a trimpot. If the output voltage is excessive, the OVP circuit may inadvertantly shut down the converter. If the maximum power is exceeded, the converter may enter current limiting. If the power is exceeded for an extended period, the converter may overheat and encounter overtemperature shut down.

<u>CAUTION:</u> Be careful of external electrical noise. The Trim input is a senstive input to the converter's feedback control loop. Excessive electrical noise may cause instability or oscillation. Keep external connections short to the Trim input. Use shielding if needed. Also consider adding a small value ceramic capacitor between the Trim and –Vout to bypass RF and electrical noise.





Isolated, 12 Vout, 30A, Half-Brick DC/DC Converters

O +VIN +VOUT +SENSE
O ON/OFF TRIM R TRIM DOWN LOAD
-SENSE
O -VIN -VOUT

Figure 7. Trim adjustments to Decrease Output Voltage using a Fixed Resistor

$$\begin{split} R_{adj_up} \left(\text{in } \mathsf{k} \Omega \right) &= \frac{V_{nominal} \ x \ (1 + \Delta)}{1.225 \ x \ \Delta} - \frac{1}{\Delta} - 2 \\ \text{where } \Delta &= \frac{V_{out} - V_{nominal}}{V_{nominal}} \end{split}$$

$$R_{adj_down}$$
 (in k Ω) = $\frac{1}{\Delta}$ - 2
where Δ = $\frac{V_{nominal} - V_{out}}{V_{nominal}}$

Trim Equations

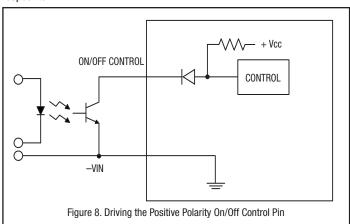
Where Vref = +1.225 Volts and Δ is the desired output voltage change. Note that " Δ " is given as a small fraction, not a percentage.

A single resistor connected between Trim and +Sense will increase the output voltage. A resistor connected between Trim and -Sense will decrease the output.

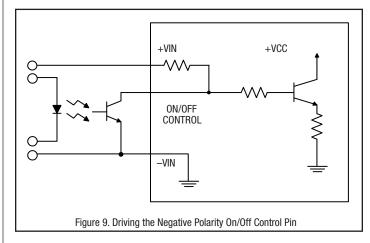
Remote On/Off Control

On the input side, a remote On/Off Control can be ordered with either polarity.

<u>Positive</u>: Standard models are enabled when the On/Off pin is left open or is pulled high to +Vin with respect to –Vin. An internal bias current causes the open pin to rise to +Vin. Some models will also turn on at lower intermediate voltages (see Specifications). Positive-polarity devices are disable when the On/Off is grounded or brought to within a low voltage (see Specifications) with respect to –Vin.



<u>Negative:</u> Optional negative-polarity devices are on (enabled) when the 0n/0ff is grounded or brought to within a low voltage (see Specifications) with respect to -Vin. The device is off (disabled) when the 0n/0ff is pulled high to +Vin with respect to -Vin.



Dynamic control of the On/Off function should be able to sink appropriate signal current when brought low and withstand appropriate voltage when brought high. Be aware too that there is a finite time in milliseconds (see Specifications) between the time of On/Off Control activation and stable, regulated output. This time will vary slightly with output load type and current and input conditions.

There are two CAUTIONs for the On/Off Control:

<u>CAUTION:</u> While it is possible to control the On/Off with external logic if you carefully observe the voltage levels, the preferred circuit is either an open drain/open collector transistor or a relay (which can thereupon be controlled by logic).

<u>CAUTION:</u> Do not apply voltages to the On/Off pin when there is no input power voltage. Otherwise the converter may be permanently damaged.

Soldering Guidelines

Murata Power Solutions recommends the specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Your production environment may differ; therefore please thoroughly review these guidelines with your process engineers.

Wave Solder Operations for through-hole mounted products (THMT)							
For Sn/Ag/Cu based solders: For Sn/Pb based solders:							
Maximum Preheat Temperature	115° C.	Maximum Preheat Temperature	105° C.				
Maximum Pot Temperature	270° C.	Maximum Pot Temperature	250° C.				
Maximum Solder Dwell Time	7 seconds	Maximum Solder Dwell Time	6 seconds				

Isolated, 12 Vout, 30A, Half-Brick DC/DC Converters

Emissions Performance

Murata Power Solutions measures its products for radio frequency emissions against the EN 55022 and CISPR 22 standards. Passive resistance loads are employed and the output is set to the maximum voltage. If you set up your own emissions testing, make sure the output load is rated at continuous power while doing the tests.

The recommended external input and output capacitors (if required) are included. Please refer to the fundamental switching frequency. All of this information is listed in the Product Specifications. An external discrete filter is installed and the circuit diagram is shown below.

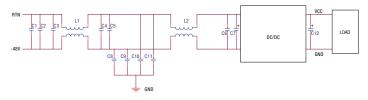


Figure 10. Conducted Emissions Test Circuit

[1] Conducted Emissions Parts List

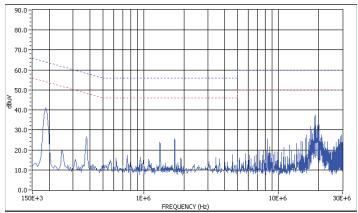
Reference	Part Number	Description	Vendor
C1, C2, C3, C4, C5	GRM32ER72A105KA01L	SMD CERAMIC-100V- 1000nF-X7R-1210	Murata
C6	GRM319R72A104KA01D	SMD CERAMIC 100V-100nF- ±10%-X7R-1206	Murata
L1, L2	PG0060T	COMMON MODE-473uH- ±25%-14A	Pulse
C8, C9, C10, C11	GRM55DR72J224KW01L	SMD CERAMIC630V-0.22μF- ±10%-X7R-2220	Murata
C7	UHE2A221MHD	Aluminum100V-220µf- ±10%-long lead	Nichicon
C12	NA		

[2] Conducted Emissions Test Equipment Used

Hewlett Packard HP8594L Spectrum Analyzer - S/N 3827A00153

2Line V-networks LS1-15V 50Ω/50Uh Line Impedance Stabilization Network

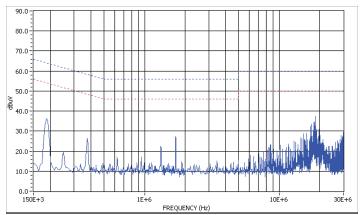
[3] Conducted Emissions Test Results



Peak Detection Value

QP Limit	$\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda_{\alpha}(\lambda_{\alpha}(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda_{\alpha}(\lambda_{\alpha}(\lambda_{\alpha}(\lambda_{\alpha}(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda_{\alpha}(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda)(\lambda_{\alpha}(\lambda$
Average Limit	/ /
Peak Vaule	

Graph 1. Conducted emissions performance, Positive Line, CISPR 22, Class B, full load at 48Vin



Peak Detection Value

QP Limit	/ (
Average Limit	プレスペプレスペプレスペプレスペプレ
Peak Vaule	

Graph 2. Conducted emissions performance, Negative Line, CISPR 22, Class B, full load at 48Vin

[4] Layout Recommendations

Most applications can use the filtering which is already installed inside the converter or with the addition of the recommended external capacitors. For greater emissions suppression, consider additional filter components and/or shielding. Emissions performance will depend on the user's PC board layout, the chassis shielding environment and choice of external components. Please refer to Application Note GEAN-02 for further discussion.

Since many factors affect both the amplitude and spectra of emissions, we recommend using an engineer who is experienced at emissions suppression.

Isolated, 12 Vout, 30A, Half-Brick DC/DC Converters

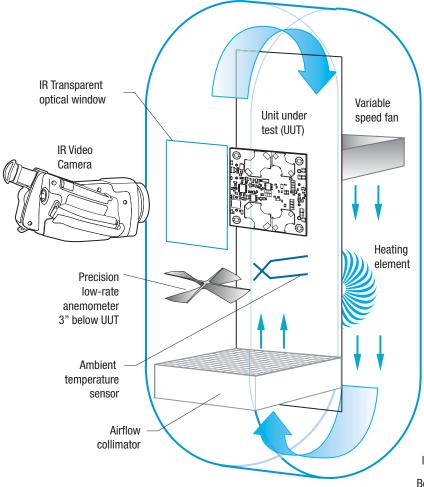


Figure 11. Vertical Wind Tunnel

Vertical Wind Tunnel

Murata Power Solutions employs a custom-designed enclosed vertical wind tunnel, infrared video camera system and test instrumentation for accurate airflow and heat dissipation analysis of power products. The system includes a precision low flow-rate anemometer, variable speed fan, power supply input and load controls, temperature gauges and adjustable heating element.

The IR camera can watch thermal characteristics of the Unit Under Test (UUT) with both dynamic loads and static steady-state conditions. A special optical port is used which is transparent to infrared wavelengths. The computer files from the IR camera can be studied for later analysis.

Both through-hole and surface mount converters are soldered down to a host carrier board for realistic heat absorption and spreading. Both longitudinal and transverse airflow studies are possible by rotation of this carrier board since there are often significant differences in the heat dissipation in the two airflow directions. The combination of both adjustable airflow, adjustable ambient heat and adjustable Input/Output currents and voltages mean that a very wide range of measurement conditions can be studied.

The airflow collimator mixes the heat from the heating element to make uniform temperature distribution. The collimator also reduces the amount of turbulence adjacent to the UUT by restoring laminar airflow. Such turbulence can change the effective heat transfer characteristics and give false readings.

Excess turbulence removes more heat from some surfaces and less heat from others, possibly causing uneven overheating.

Both sides of the UUT are studied since there are different thermal gradients on each side. The adjustable heating element and fan, built-in temperature gauges and no-contact IR camera mean that power supplies are tested in real-world conditions.



This product is subject to the following <u>operating requirements</u> and the <u>Life and Safety Critical Application Sales Policy</u>:

Refer to: http://www.murata-ps.com/requirements/

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