



Model Number	Input Range (Vdc)		Vout (Vdc)	Iout (A)
	Min	Max		
ICH0130V1xC	9	36	12	30
ICH0415V1xC	9	36	24	15
ICH0513V1xC	9	36	28	13

Features

- 4:1 Input voltage range of 9-36V
- Single outputs of 12V, 24V or 28V
- 2250V Isolation voltage (Input-to-Output)
- Industry Standard half brick package
2.4" x 2.5" x 0.52" (61mm x 64mm x 13.2mm)
- Efficiency up to 95.6%
- Excellent thermal performance
- Over-Current and Short Circuit Protection
- Over-Temperature protection
- Monotonic startup into pre-bias loads
- 200kHz Fixed switching frequency
- Remote On/Off control (Positive or Negative logic)
- Good shock and vibration damping
- Operating Temperature Range -40°C to +105°C
- RoHS Compliant

Product Overview

The 4:1 input voltage 360 Watt single output ICH DC-DC converter provides a precisely regulated dc output. The output voltage is fully isolated from the input, allowing the output to be positive or negative polarity and with various ground connections. The enclosed half brick package meets the most rigorous performance standards in an industry standard footprint for mobile (12V_{in}), process control (24V_{in}), and Commercial-Off-The-Shelf (28V_{in}) applications.

The ICH Series includes an external TRIM adjust and remote ON/OFF control. Threaded through holes are provided to allow easy mounting or the addition of a heat sink for extended

temperature operation.

The converters high efficiency and high power density are accomplished through use of high-efficiency synchronous rectification technology, advanced electronic circuit, packaging and thermal design thus resulting in a high reliability product. The converter operates at a fixed frequency of 200kHz and follows conservative component derating guidelines.

Product is designed and manufactured in the USA.

Part Number Structure and Ordering Guide

Description	Part Number Structure										Definition and Options		
Product Family	I	C									IC= Industrial Class		
Form Factor			H								H = Half Brick		
Vout*				0	1						01 = 12Vout, 02 = 5Vout, 03 = 3.3Vout, 04 = 24Vout, 05 = 28Vout		
Output Current						3	0				Max Iout in Amps		
Vin Range								V	1		V1 = 9 to 36V		
On/Off Control Logic										P	N = Negative, P = Positive (Standard)		
Specific Customer Configuration										X	X	Customer Code, Omit for Standard	
RoHS Compliant												C	RoHS 6/6 Compliant

*NOTE: Some part number combinations might not be available. Please contact the factory for non-standard or special order products.

Electrical Specifications – All Models

Conditions: $T_a = 25^\circ\text{C}$, airflow = 300 LFM (1.5m/s), $V_{in} = 24\text{VDC}$, unless otherwise specified. Specifications subject to change without notice.

Parameter	Notes	Min	Typ	Max	Units
Absolute Maximum Ratings					
Input Voltage	Continuous	0		40	V
	Transient (100ms)			50	V
Operating Temperature (See Note 1)	Baseplate (100% load)	-40		105	$^\circ\text{C}$
Storage Temperature		-55		125	$^\circ\text{C}$
Isolation Characteristics and Safety					
Isolation Voltage	Input to Output	2250			V
	Input to Baseplate & Output to Baseplate	1500			V
Isolation Capacitance			4500		pF
Isolation Resistance		10	20		M Ω
Insulation Safety Rating			Basic		
Agency Approvals	Designed to meet UL/cUL 60950, IEC/EN 60950-1				
Feature Characteristics					
Fixed Switching Frequency	Output Voltage Ripple has twice this frequency		200		kHz
Output Voltage Trim Range				± 10	%
Remote Sense Compensation	This function is not provided		N/A		%
Output Overvoltage Protection	Non-latching	117	124	130	%
Overtemperature Shutdown (Baseplate)	Non-latching		110	120	$^\circ\text{C}$
Auto-Restart Period	Applies to all protection features	450	500	550	ms
Turn-On Time from V_{in}	Time from UVLO to $V_o=90\% V_{out(NOM)}$ Resistive load		517	530	ms
Turn-On time from ON/OFF Control	Trim from ON to $V_o=90\% V_{out(NOM)}$ Resistive load		17	20	ms
Rise Time	V_{out} from 10% to 90%	4	7.5	11	ms
ON/OFF Control - Positive Logic					
ON state	Pin open = ON or external voltage applied	2		12	V
Control Current	Leakage current			0.16	mA
OFF state		0		0.8	V
Control Current	Sinking	0.3		0.36	mA
ON/OFF Control - Negative Logic					
ON state	Pin shorted to -INPUT or			0.8	V
OFF state	Pin open = Off or	2		12	V
Thermal Characteristics					
Thermal resistance Baseplate to Ambient	Converter soldered to 3.95" x 2.5" x 0.07" 4 layer/ 2oz copper FR4 PCB.		5.2		$^\circ\text{C/W}$

1. A thermal management device, such as a heatsink, is required to ensure proper operation of this device. The thermal management medium is required to maintain baseplate $< 105^\circ\text{C}$ for full rated power.

Electrical Specifications – ICH0130V1

Conditions: $T_a = 25^\circ\text{C}$, airflow = 300 LFM (1.5m/s), $V_{in} = 24\text{VDC}$, unless otherwise specified. Specifications subject to change without notice.

Parameter	Notes	Min	Typ	Max	Units	
Input Characteristics						
Operating Input Voltage Range		9	24	36	V	
Input Under Voltage Lockout	Non-latching					
Turn-on Threshold		8.2	8.5	8.8	V	
Turn-off Threshold		7.7	8	8.3	V	
Lockout Hysteresis Voltage		0.4	0.55	0.7	V	
Maximum Input Current	$V_{in} = 9\text{V}$, 80% Load			45.3	A	
	$V_{in} = 12\text{V}$, 100% Load			33.2	A	
	$V_{in} = 24\text{V}$, Output Shorted		65		mA_{RMS}	
Input Stand-by Current	Converter Disabled		2	4	mA	
Input Current @ No Load	Converter Enabled		240	280	mA	
Minimum Input Capacitance (external)	ESR < 0.1 Ω	470			μF	
Inrush Transient	$V_{in} = 36\text{V}$ (0.4V/ μsec), no external input cap		0.4	1	A^2s	
Input Terminal Ripple Current, i_c	25 MHz bandwidth, 100% Load (Fig. 2)		560		mA_{RMS}	
Output Characteristics						
Output Voltage Range		11.64	12.00	12.36	V	
Output Voltage Set Point Accuracy	(50% load)	11.88	12.00	12.12	V	
Output Regulation						
Over Line	$V_{in} = 9\text{V}$ to 36V		0.05	0.15	%	
Over Load	$V_{in} = 24\text{V}$, Load 0% to 100%		0.08	0.15	%	
Temperature Coefficient			0.015	0.03	%/ $^\circ\text{C}$	
Overvoltage Protection		14.0		15.6	V	
Output Ripple and Noise - 20MHz bandwidth	(Fig. 3) 100% Load, $C_{\text{ext}} = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		120	180	$\text{mV}_{\text{PK-PK}}$	
			30	60	mV_{RMS}	
External Load Capacitance	Full Load (resistive) $-40^\circ\text{C} < T_a < +105^\circ\text{C}$	C_{ext} ESR	470		4700	μF
			10		100	$\text{m}\Omega$
Output Current Range (See Fig. A)	$V_{in} = 9\text{V} - 36\text{V}$	0		30	A	
Current Limit Inception	$V_{in} = 9\text{V} - 36\text{V}$	33	36	39	A	
RMS Short-Circuit Current	Non-latching, Continuous		4	7	A_{RMS}	
Dynamic Response						
Load change 50% - 75% - 50%, $di/dt = 1\text{A}/\mu\text{s}$	$C_o = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		± 200	± 320	mV	
Load change 50% - 100% - 50%, $di/dt = 1\text{A}/\mu\text{s}$	$C_o = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		± 450		mV	
Setting Time to 1% of V_{out}			400		μs	
Efficiency						
100% Load	$V_{in} = 24\text{V}$	93.7	94.4	95.1	%	
	$V_{in} = 12\text{V}$	92.9	93.6	94.3	%	
50% Load	$V_{in} = 24\text{V}$	94.1	94.8	95.5	%	
	$V_{in} = 12\text{V}$	94.0	94.7	95.1	%	

Electrical Specifications – ICH0415V1

Conditions: $T_a = 25^\circ\text{C}$, airflow = 300 LFM (1.5m/s), $V_{in} = 24\text{VDC}$, unless otherwise specified. Specifications subject to change without notice.

Parameter	Notes	Min	Typ	Max	Units
Input Characteristics					
Operating Input Voltage Range		9	24	36	V
Input Under Voltage Lockout	Non-latching				
Turn-on Threshold		8.2	8.5	8.8	V
Turn-off Threshold		7.7	8.0	8.3	V
Lockout Hysteresis Voltage		0.4	0.55	0.7	V
Maximum Input Current	$V_{in} = 9\text{V}$, 80% Load			45	A
	$V_{in} = 12\text{V}$, 100% Load			42	A
	$V_{in} = 24\text{V}$, Output Shorted		75		mA_{RMS}
Input Stand-by Current	Converter Disabled		2	4	mA
Input Current @ No Load	Converter Enabled		240	300	mA
Minimum Input Capacitance (external)	ESR < 0.1 Ω	470			μF
Inrush Transient	$V_{in} = 36\text{V}$ (0.4V/ μsec), no external input cap		0.4	1	A^2s
Input Terminal Ripple Current, i_c	25 MHz bandwidth, 100% Load (Fig. 2)		600		mA_{RMS}
Output Characteristics					
Output Voltage Range		23.28	24.00	24.72	V
Output Voltage Set Point Accuracy	(50% load)	23.76	24.00	24.24	V
Output Regulation					
Over Line	$V_{in} = 9\text{V}$ to 36V		0.05	0.15	%
Over Load	$V_{in} = 24\text{V}$, Load 0% to 100%		0.08	0.15	%
Temperature Coefficient			0.015	0.03	%/ $^\circ\text{C}$
Overvoltage Protection		28.1		31.2	V
Output Ripple and Noise - 20MHz bandwidth	(Fig. 3) 100% Load, $C_{\text{ext}} = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		240	360	$\text{mV}_{\text{PK-PK}}$
			50	80	mV_{RMS}
External Load Capacitance	Full Load (resistive) $-40^\circ\text{C} < T_a < +105^\circ\text{C}$	C_{ext}	470	2200	μF
		ESR	10	100	$\text{m}\Omega$
Output Current Range (See Fig. A)	$V_{in} = 9\text{V} - 36\text{V}$	0		15	A
Current Limit Inception	$V_{in} = 9\text{V} - 36\text{V}$	16.5	18	19.5	A
RMS Short-Circuit Current	Non-latching, Continuous		3.8	6	A_{RMS}
Dynamic Response					
Load change 50% - 75% - 50%, $di/dt = 1\text{A}/\mu\text{s}$	$C_o = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		± 280	± 420	mV
Load change 50% - 100% - 50%, $di/dt = 1\text{A}/\mu\text{s}$	$C_o = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		± 500		mV
Setting Time to 1% of V_{out}			600		μs
Efficiency					
100% Load	$V_{in} = 24\text{V}$	94.5	95.2	95.9	%
	$V_{in} = 12\text{V}$	93.8	94.5	95.2	%
50% Load	$V_{in} = 24\text{V}$	94.5	95.4	96.1	%
	$V_{in} = 12\text{V}$	94.6	95.2	95.9	%

Electrical Specifications – ICH0513V1

Conditions: $T_a = 25^\circ\text{C}$, airflow = 300 LFM (1.5m/s), $V_{in} = 24\text{VDC}$, unless otherwise specified. Specifications subject to change without notice.

Parameter	Notes	Min	Typ	Max	Units
Input Characteristics					
Operating Input Voltage Range		9	24	36	V
Input Under Voltage Lockout	Non-latching				
Turn-on Threshold		8.2	8.5	8.8	V
Turn-off Threshold		7.7	8	8.3	V
Lockout Hysteresis Voltage		0.4	0.55	0.7	V
Maximum Input Current	$V_{in} = 9\text{V}$, 80% Load			45	A
	$V_{in} = 12\text{V}$, 100% Load			42	A
	$V_{in} = 24\text{V}$, Output Shorted		55		mA_{RMS}
Input Stand-by Current	Converter Disabled		2	4	mA
Input Current @ No Load	Converter Enabled		240	280	mA
Minimum Input Capacitance (external)	ESR < 0.1 Ω	470			μF
Inrush Transient	$V_{in} = 36\text{V}$ (0.4V/ μsec), no external input cap		0.4	1	A^2s
Input Terminal Ripple Current, i_c	25 MHz bandwidth, 100% Load (Fig. 2)		560		mA_{RMS}
Output Characteristics					
Output Voltage Range		27.16	28.00	28.84	V
Output Voltage Set Point Accuracy	(50% load)	27.72	28.00	28.28	V
Output Regulation					
Over Line	$V_{in} = 9\text{V}$ to 36V		0.05	0.15	%
Over Load	$V_{in} = 24\text{V}$, Load 0% to 100%		0.08	0.15	%
Temperature Coefficient			0.015	0.03	$\%/^\circ\text{C}$
Overvoltage Protection		32.8		36.4	V
Output Ripple and Noise - 20MHz bandwidth	(Fig. 3) 100% Load, $C_{\text{ext}} = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		280	380	$\text{mV}_{\text{PK-PK}}$
			50	85	mV_{RMS}
External Load Capacitance	Full Load (resistive) $-40^\circ\text{C} < T_a < +105^\circ\text{C}$	C_{ext}	470	2200	μF
		ESR	10	100	$\text{m}\Omega$
Output Current Range (See Fig. A)	$V_{in} = 9\text{V} - 36\text{V}$	0		13	A
Current Limit Inception	$V_{in} = 9\text{V} - 36\text{V}$	14.3	15.6	16.9	A
RMS Short-Circuit Current	Non-latching, Continuous		2.2	6	A_{RMS}
Dynamic Response					
Load change 50% - 75% -50%, $di/dt = 1\text{A}/\mu\text{s}$	$C_o = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		± 180	± 300	mV
Load change 50% - 100% - 50%, $di/dt = 1\text{A}/\mu\text{s}$	$C_o = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F}$ ceramic		± 400		mV
Setting Time to 1% of V_{out}			500		μs
Efficiency					
100% Load	$V_{in} = 24\text{V}$	94.3	95.4	96.1	%
	$V_{in} = 12\text{V}$	93.7	94.4	95.1	%
50% Load	$V_{in} = 24\text{V}$	94.3	95.0	95.7	%
	$V_{in} = 12\text{V}$	94.0	94.7	95.1	%

Environmental and Mechanical Specifications: Specifications subject to change without notice.

Parameter	Notes	Min	Typ	Max	Units
Environmental					
Operating Humidity	Non-condensing			95	%
Storage Humidity	Non-condensing			95	%
RoHS Compliance	See Murata Website http://www.murata-ps.com/en/support/rohs-compliance.html for the complete RoHS Compliance Statement				
Shock and Vibration (See Note 1)	Designed to meet MIL-STD-810G for functional shock and vibration				
Water Washability	Not recommended for water wash process. Contact the factory for more information.				
Mechanical					
Unit Weight		3.85			Ounces
		109.2			Grams
PCB					
Operating Temperature				130	°C
Tg		170			°C
Through Hole Pin Diameters	Pins 1,4,5 and 9	0.079	0.081	0.083	Inches
		2.006	2.057	2.108	mm
	Pins 3 and 7	0.038	0.04	0.042	Inches
		0.965	1.016	1.067	mm
Through Hole Pins Material	Pins 1,4,5 and 9	Copper Alloy			
	Pins 3 and 7	Brass Alloy TB3 or "Eco Brass"			
Through Hole Pin Finish	All pins	10µ" Gold over Nickel			
Case Dimensions		2.4 x 2.5 x 0.52			Inches
		60.96 x 63.50 x 13.21			mm
Case Material	Plastic: Vectra LCP FIT30: 1/2-16 EDM Finish				
Baseplate	Material	Aluminum			
	Flatness		0.008		Inches
			0.20		mm
Reliability					
MTBF	Telcordia SR-332, Method I Case 1 50% electrical stress, 40°C components	5.4			MHrs
EMI and Regulatory Compliance					
Conducted Emissions	MIL-STD-461F C102 with external EMI filter network (see Figures, 34 and 35)				

1. The unit must be properly secured to the interface medium (PCB/Chassis) by use of the threaded inserts of the unit.

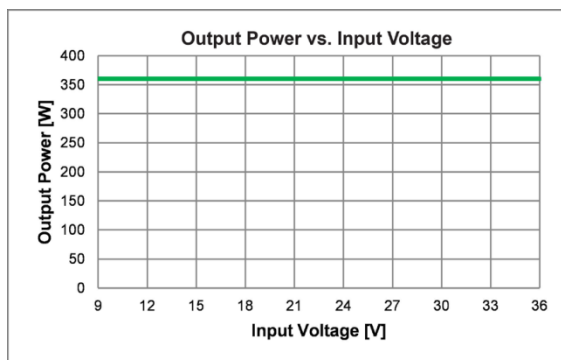


Figure A: Output Power as function of input voltage.

TECHNICAL NOTES

Input and Output Capacitance

In many applications, the inductance associated with the distribution from the power source to the input of the converter can affect the stability of the converter. This becomes of great consideration for input voltage at 12V or below. In order to enable proper operation of the converter, in particular during load transients, an additional input capacitor is required. Minimum required input capacitance, mounted close to the input pins, is 1000µF with ESR < 0.1 Ω. Since inductance of the input power cables could have significant voltage drop due to rate of change of input current $di(in)/dt$ during transient load operation an external capacitance on the output of the converter is required to reduce $di(in)/dt$. It is required to use at least 470 µF (ESR < 0.07Ω) on the output. Another constraint is minimum rms current rating of the input and output capacitors which is application dependent. One component of input rms current handled by input capacitor is high frequency component at switching frequency of the converter (typ. 400kHz) and is specified under “Input terminal ripple current” i_c . Typical values at full rated load and 24 Vin are provided in Section “Characteristic Waveforms” for each model and are in range of 0.56A - 0.6A. Second component of the ripple current is due to reflected step load current on the input of the converter. Similar consideration needs to be taken into account for output capacitor and in particular step load ripple current component. Consult the factory for further application guidelines.

Additionally, for EMI conducted measurement it is necessary to use 5µH LISNs instead of typical 50µH LISNs.

ON/OFF (Pin 3)

The ON/OFF pin is used to turn the power converter on or off remotely via a system signal and has positive logic. A typical connection for remote ON/OFF function is shown in Fig. 1.

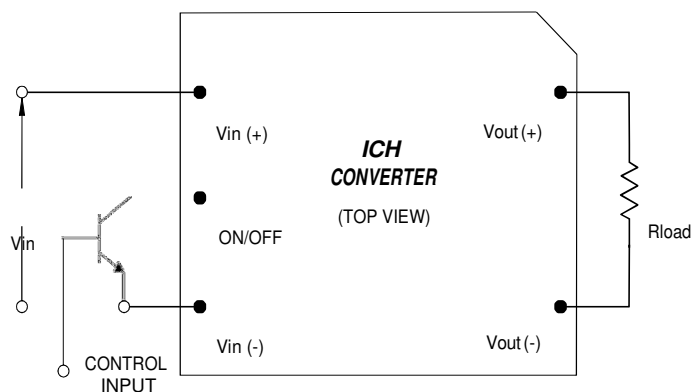


Fig. 1: Circuit configuration for ON/OFF function.

The positive logic version turns on when the ON/OFF pin is at logic high and turns off when at logic low. The converter is on when the ON/OFF pin is either left open, or external voltage not more than 12V is applied between ON/OFF pin and -INPUT pin. See the Electrical Specifications for logic high/low definitions.

The negative logic version turns on when the ON/OFF pin is at logic low and turns off when at logic high. The converter is on when the ON/OFF pin is either shorted to -INPUT pin or kept below 0.8V. The converter is off when the ON/OFF pin is either left open or external voltage greater than 2V and not more than 12V is applied between ON/OFF pin and -INPUT pin. See the Electrical Specifications for logic high/low definitions.

The ON/OFF pin is internally pulled up to typically 4.5V via resistor and connected to internal logic circuit via RC circuit in order to filter out noise that may occur on the ON/OFF pin. A properly de-bounced mechanical switch, open-collector transistor, or FET can be used to drive the input of the ON/OFF pin. The device must be capable of sinking up to 0.36mA at a low level voltage of $\leq 0.8V$. During logic high, the typical maximum voltage at ON/OFF pin (generated by the converter) is 4.5V, and the maximum allowable leakage current is 160µA. If not using the remote on/off feature leave the ON/OFF pin open.

TTL Logic Level - The range between 0.81V as maximum turn off voltage and 2V as minimum turn on voltage is considered the dead-band. Operation in the dead-band is not recommended.

External voltage for ON/OFF control should not be applied when there is no input power voltage applied to the converter.

Protection Features:

Input Undervoltage lockout (UVLO)

Input undervoltage lockout is standard with this converter. The converter will shut down when the input voltage drops below a pre-determined voltage.

The input voltage must be typically above 8.5V for the converter to turn on. Once the converter has been turned on, it will shut off when the input voltage drops typically below 8V. If the converter is started by input voltage (ON/OFF (pin 3) left open) there is typically 500msec delay from the moment when input voltage is above 8.5V turn-on voltage and the time when output voltage starts rising. This delay is intentionally provided to prevent potential startup issues especially at low input voltages.

Output Overcurrent Protection (OCP)

The converter is protected against overcurrent or short circuit conditions. Upon sensing an overcurrent condition, the converter will switch to constant current operation and thereby begin to reduce output voltage. When the output voltage drops below approx. 75% of the nominal value of output voltage, the converter will shut down.

Once the converter has shut down, it will attempt to restart nominally every 500msec with a typical 3% duty cycle. The attempted restart will continue indefinitely until the overload or short circuit conditions are removed or the output voltage rises above 75% of its nominal value.

Once the output current is brought back into its specified range, the converter automatically exits the hiccup mode and continues normal operation.

During initial startup, if output voltage does not exceed typical 75% of nominal output voltage within 20 msec after the converter is enabled, the converter will be shut down and will attempt to restart after 500 msec.

Output Overvoltage Protection (OVP)

The converter will shut down if the output voltage across Vout (+) (Pin 5) and Vout (-) (Pin 9) exceeds the threshold of the OVP circuitry. The OVP circuitry contains its own reference, independent of the output voltage regulation loop. Once the converter has shut down, it will attempt to restart every 500 msec until the OVP condition is removed.

Overtemperature Protection (OTP)

The ICH converters have non-latching overtemperature protection. It will shut down and disable the output if temperature at the center of the base plate exceeds a threshold of 114°C (typical).

The converter will automatically restart when the base temperature has decreased by approximately 20°C

Safety Requirements

Basic Insulation is provided between input and the output.

The converters have no internal fuse. To comply with safety agencies requirements, a fast-acting or time-delay fuse is to be provided in the unearthed lead.

Recommended fuse values are:

- a) 50A for 9V < V_{in} < 18V
- b) 25A for 18V < V_{in} < 36V

Electromagnetic Compatibility (EMC)

EMC requirements must be met at the end-product system level, as no specific standards dedicated to EMC characteristics of board mounted component dc-dc converters exist.

With the addition of a two stage external filter, the ICH converters will pass the requirements of MIL-STD-461F CE102 Base Curve for conducted emissions.

Absence of the Remote Sense Pins

Customers should be aware that ICH converters do not have a Remote Sense feature. Care should be taken to minimize voltage drop on the user's motherboard as well as if trim function is used.

Output Voltage Adjust/TRIM (pin7)

The TRIM pin allows user to adjust output voltage 10% up or down relative to rated nominal voltage by addition of external trim resistor. Due to absence of Remote Sense Pins, an external trim resistor should be connected to output pins using Kelvin connection. If trimming is not used, the TRIM pin should be left open.

Trim Down - Decrease Output Voltage

Trimming down is accomplished by connecting an external resistor, *R_{trim-down}*, between the TRIM (pin7) and the Vout(-) (pin 9) using Kelvin connection, with a value of:

$$R_{trim-down} = \left(\frac{3010}{\Delta} - 60.2 \right) [k\Omega]$$

Where,

R_{trim-down} = Required value of the trim-down resistor [kΩ] V_{O(NOM)}
 = Nominal value of output voltage [V]
 V_{O(req)} = Required value of output voltage [V]

$$\Delta = \left| \frac{V_O(REQ) - V_O(NOM)}{V_O(NOM)} \right| [%]$$

To trim the output voltage 10% (Δ=10) down, required external trim resistance is:

$$R_{trim-down} = \left(\frac{3010}{10} - 60.2 \right) = 240.8 [k\Omega]$$

Trim Up - Increase Output Voltage

Trimming up is accomplished by connecting an external resistor, *R_{trim-up}*, between the TRIM (pin 7) and the Vout(+) (pin 5) using Kelvin connection, with a value of:

$$R_{trim-up} = 30.1 * \left\{ \left[\frac{V_O(NOM) * (100 + \Delta)}{1.225\Delta} \right] - \frac{(100 + 2\Delta)}{\Delta} \right\} [k\Omega]$$

To trim the output voltage up, for example 24V to 26.4V, Δ=10 and required external resistor is:

$$R_{trim-up} = 30.1 * \left\{ \left[\frac{24 * (100 + 10)}{1.225 * 10} \right] - \frac{(100 + 2 * 10)}{10} \right\} = 6125 [k\Omega]$$

Note that trimming output voltage more than 10% is not recommended and OVP may be tripped.

Active Voltage Programming

In applications where output voltage needs to be adjusted actively, an external voltage source, such as for example a Digital-to-Analog converter (DAC), capable of both sourcing and sinking current can be used. It should be connected with series resistor R_g across TRIM (pin 7) and Vout (-) (pin 9) using Kelvin connection. Please contact Murata technical representative for more details.

Characteristic Curves - Efficiency and Power Dissipation

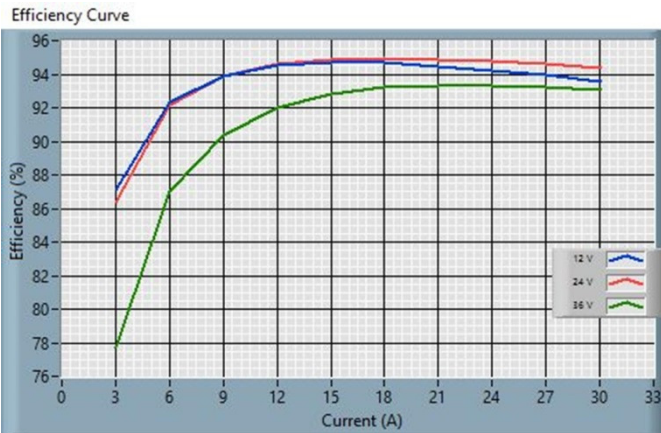


Fig. 4: ICH0130V1PC Efficiency Curve

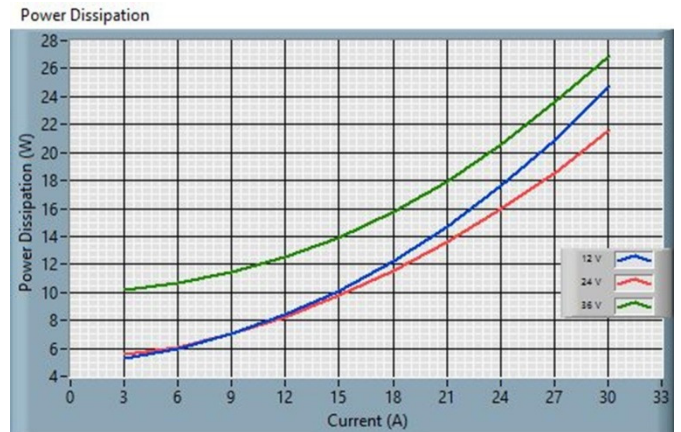


Fig. 5: ICH0130V1PC Power Dissipation

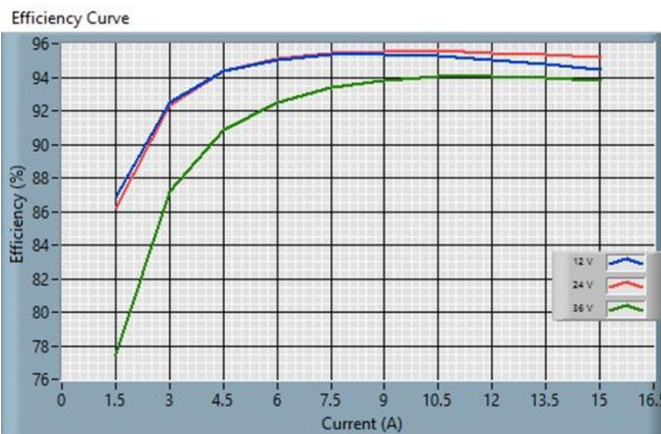


Fig. 6: ICH0415V1PC Efficiency Curve

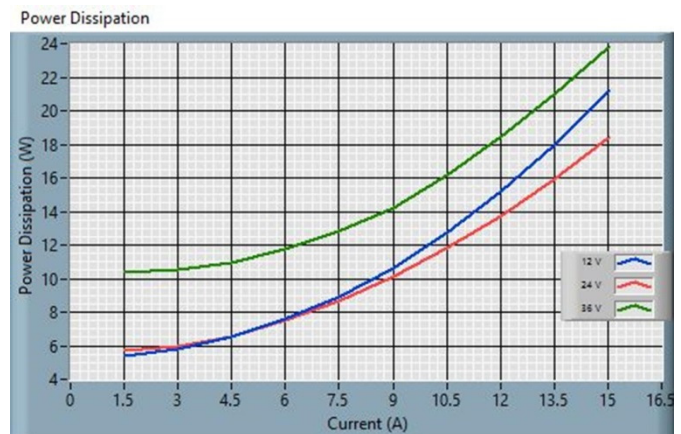


Fig. 7: ICH0415V1PC Power Dissipation

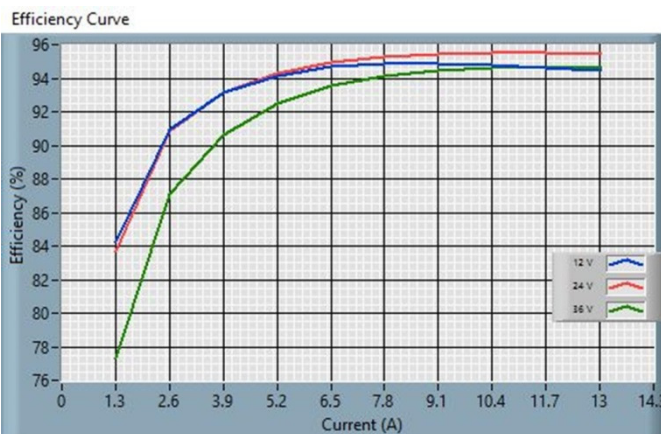


Fig. 8: ICH0513V1PC Efficiency Curve

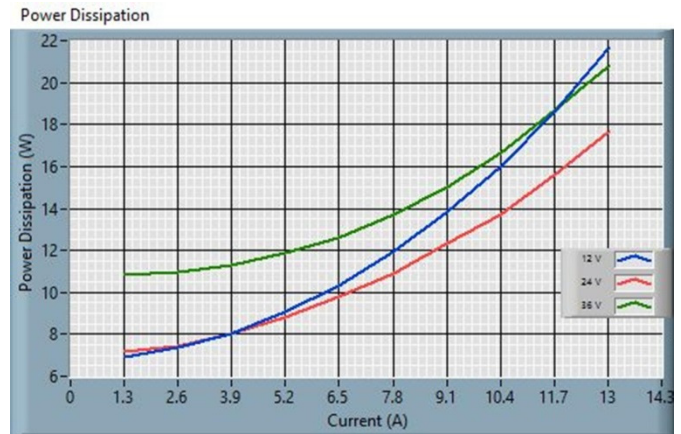


Fig. 9: ICH0513V1PC Power Dissipation

Characteristic Curves – Derating vs. Baseplate Temperature

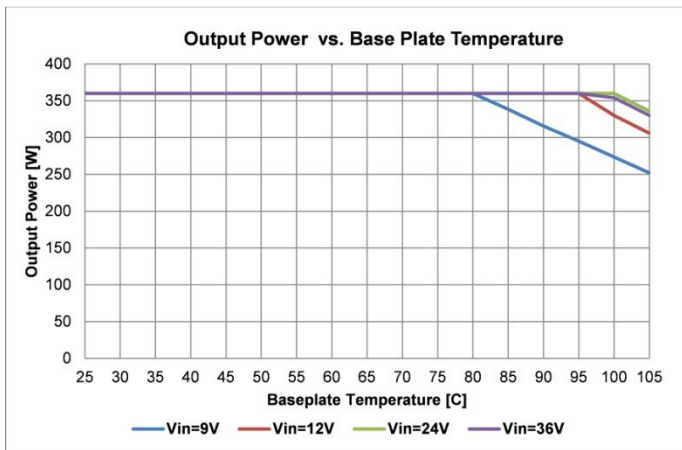


Fig. 10: ICH0130V1PC Derating Curve (P_{out})

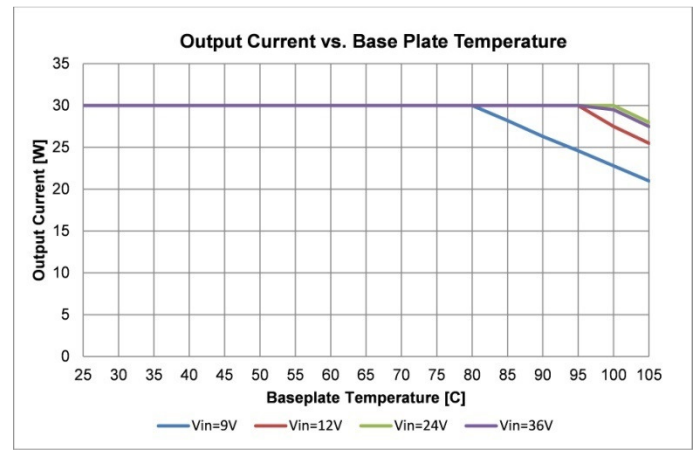


Fig. 11: ICH0130V1PC Derating Curve (I_{out})

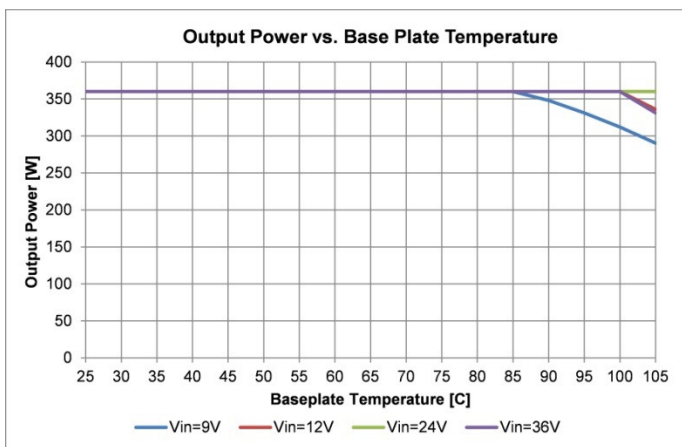


Fig. 12: ICH0415V1PC Derating Curve (P_{out})

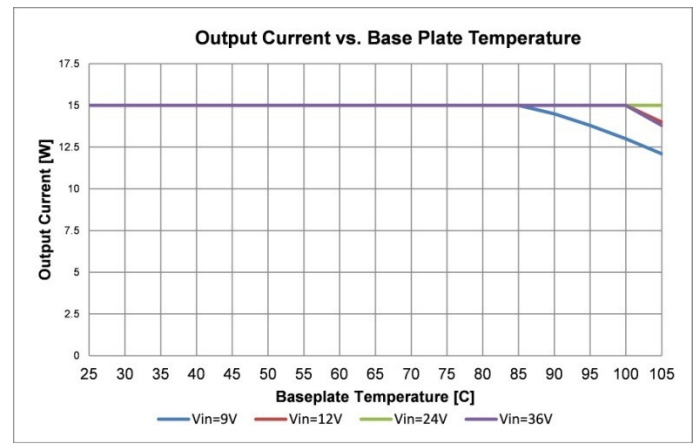


Fig. 13: ICH0415V1PC Derating Curve (I_{out})

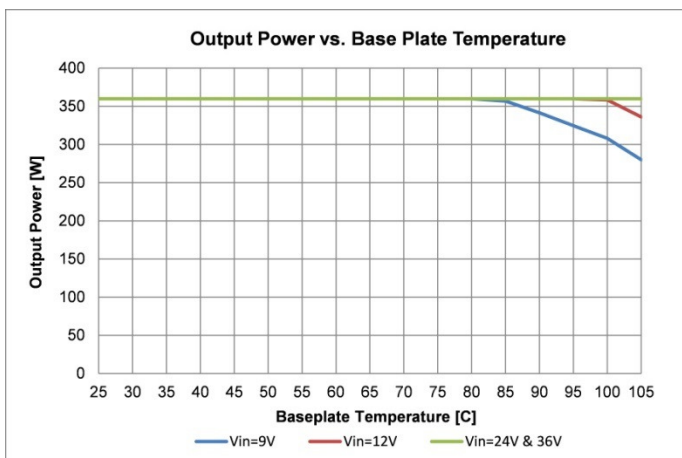


Fig. 14: ICH0513V1PC Derating Curve (P_{out})

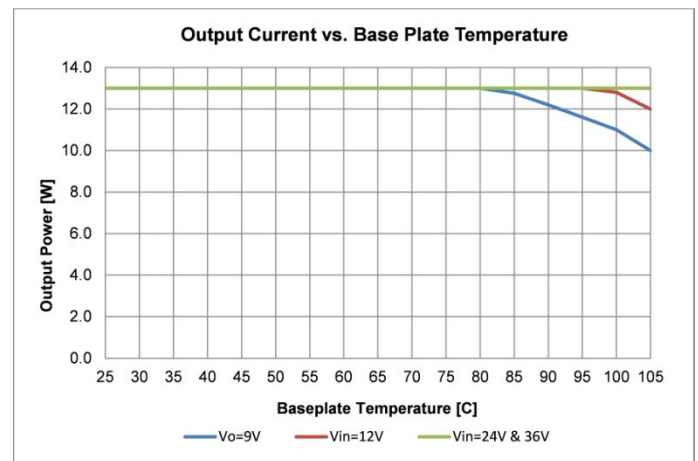


Fig. 15: ICH0513V1PC Derating Curve (I_{out})

Characteristic Waveforms - ICH0130V1PC

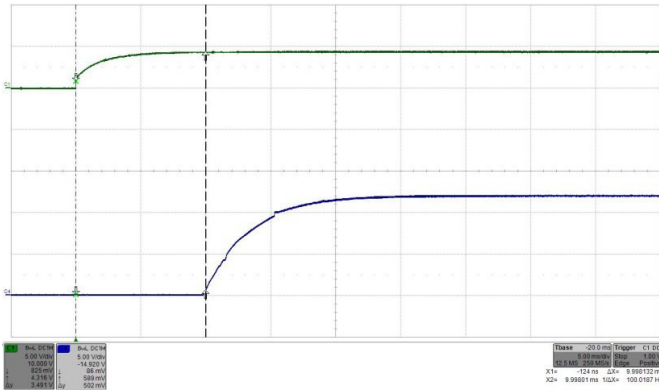


Fig. 16: Turn-on by ON/OFF transient (with V_{in} applied) at full rated load current (resistive) at $V_{in} = 24V$. Top trace (C1): ON/OFF signal (5 V/div.). Bottom trace (C4): Output voltage (5 V/div.). Time: 5 ms/div.

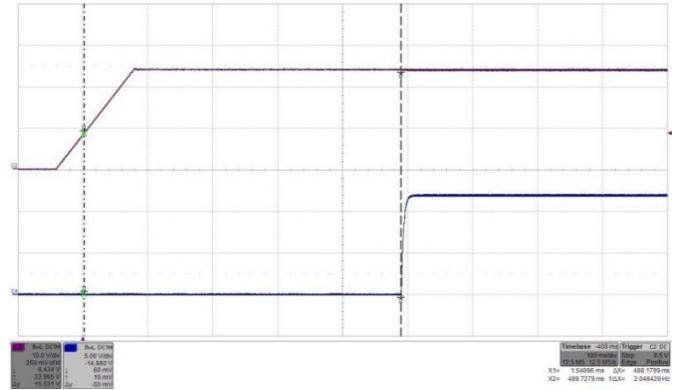


Fig. 17: Turn-on by V_{in} (ON/OFF high) transient at full rated load current (resistive) at $V_{in} = 24V$. Top trace (C2): Input voltage V_{in} (10 V/div.). Bottom trace (C4): Output voltage (5 V/div.). Time: 100 ms/div.

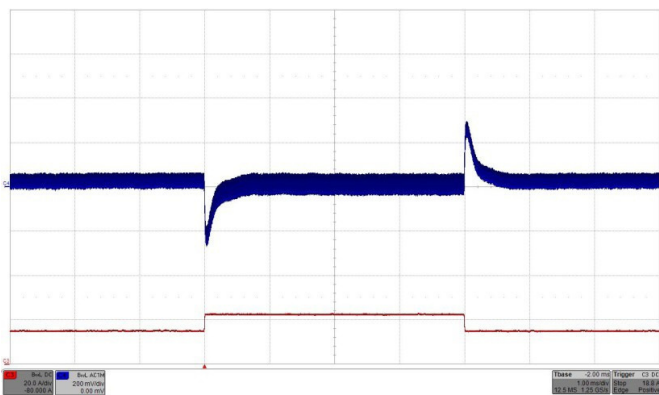


Fig. 18: Output voltage response to load current step change 50% - 75% - 50% (15A-22.5A-15A) with $di/dt = 1A/\mu s$ at $V_{in} = 24V$. Top trace (C4): Output voltage (200 mV/div.). Bottom trace (C3): Load current (20A/div.). $C_o = 470 \mu F/70m\Omega$. Time: 1ms/div.

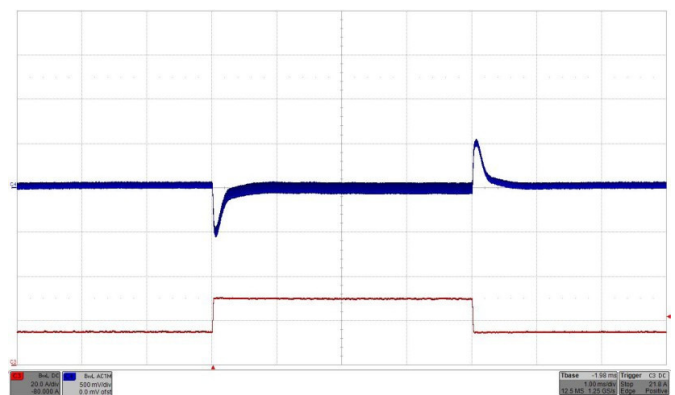


Fig. 19: Output voltage response to load current step change 50% - 100% - 50% (15A-30A-15A) with $di/dt = 1A/\mu s$ at $V_{in} = 24V$. Top trace (C4): Output voltage (500 mV/div.). Bottom trace (C3): Load current (20A/div.). $C_o = 470 \mu F/70m\Omega$. Time: 1ms/div.

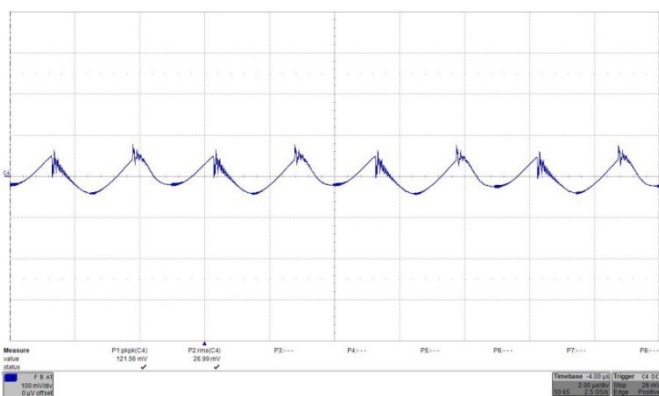


Fig. 20: Output voltage ripple (100 mV/div.) at full rated load current into a resistive load at $V_{in} = 24V$. $C_o = 470 \mu F/70m\Omega$. Time: 2 μs /div.

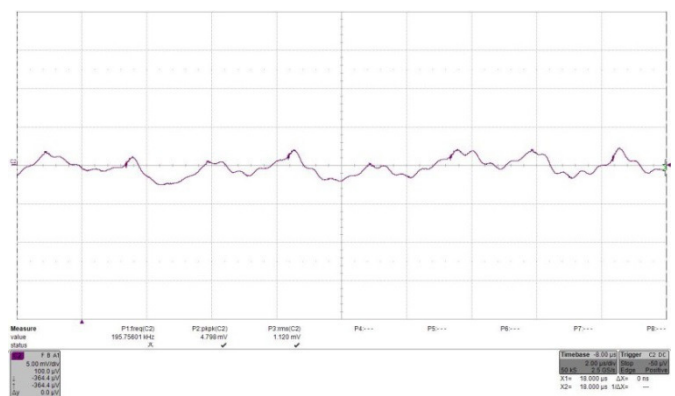


Fig. 21: Input reflected ripple current, i_c (500 mA/mV), measured at input terminals at full rated load current at $V_{in} = 24V$. Refer to Fig. 2 for test setup. Time: 2 μs /div. RMS input ripple current is $1.125 \times 500mA = 560 mA$.

Characteristic Waveforms - ICH0415V1PC

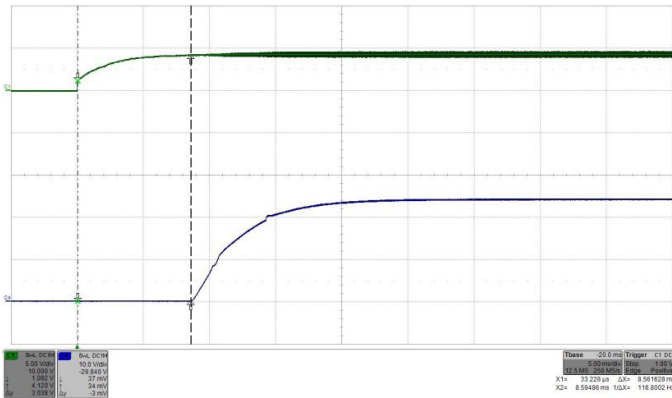


Fig. 22: Turn-on by ON/OFF transient (with Vin applied) at full rated load current (resistive) at Vin = 24V. Top trace (C1): ON/OFF signal (5 V/div.). Bottom trace (C4): Output voltage (10 V/div.). Time: 5 ms/div.

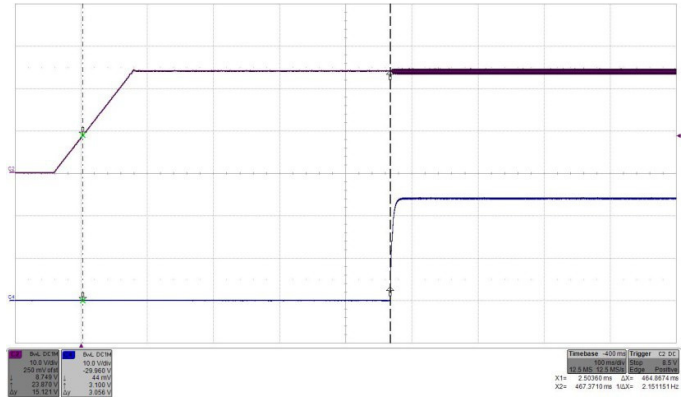


Fig. 23: Turn-on by Vin transient (ON/OFF high) at full rated load current (resistive) at Vin = 24V. Top trace (C2): Input voltage Vin (10 V/div.). Bottom trace (C4): Output voltage (10 V/div.). Time: 100 ms/div.

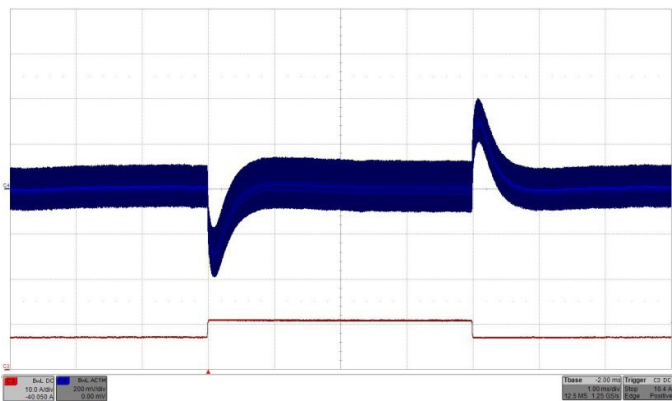


Fig. 24: Output voltage response to load current step change 50% - 75% - 50% (7.5A-11.25A-7.5A) with di/dt = 1A/μs at Vin = 24V. Top trace (C4): Output voltage (200 mV/div.). Bottom trace (C3): Load current (10A/div.). Co = 470 μF/70mΩ. Time: 1ms/div.

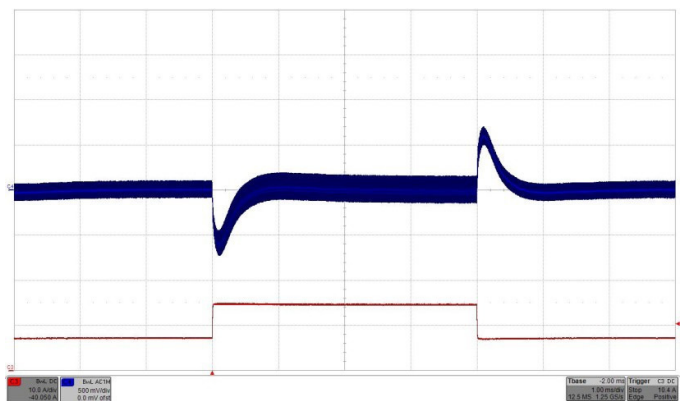


Fig. 25: Output voltage response to load current step change 50% - 100% - 50% (7.5A-15A-7.5A) with di/dt = 1A/μs at Vin = 24V. Top trace (C4): Output voltage (500 mV/div.). Bottom trace (C3): Load current (10 A/div.). Co = 470 μF/70mΩ. Time: 1ms/div.

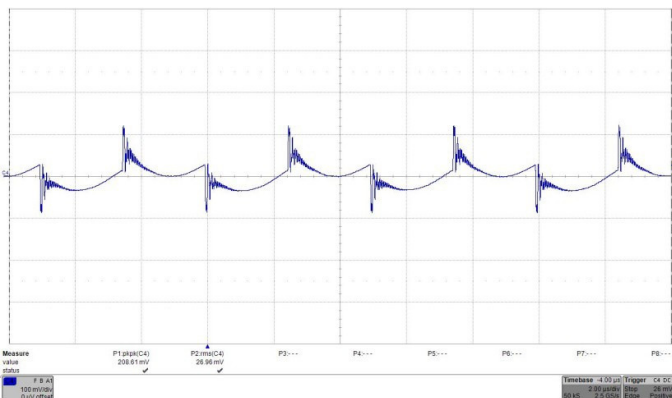


Fig. 26: Output voltage ripple (200 mV/div.) at full rated load current into a resistive load at Vin = 24V. Co= 470 μF/70mΩ. Time: 2 μs/div.

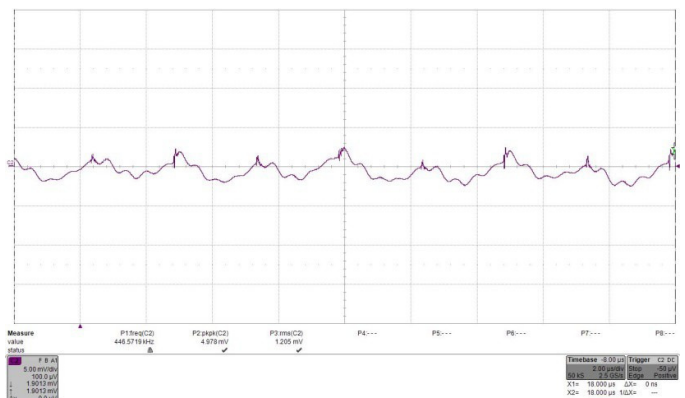


Fig. 27: Input reflected ripple current, ic (500 mA/mV), measured at input terminals at full rated load current at Vin = 24V. Refer to Fig. 2 for test setup. Time: 2 μs/div. RMS input ripple current is 1.205*500mA = 602.5mA.

Characteristic Waveforms - ICH0513V1PC

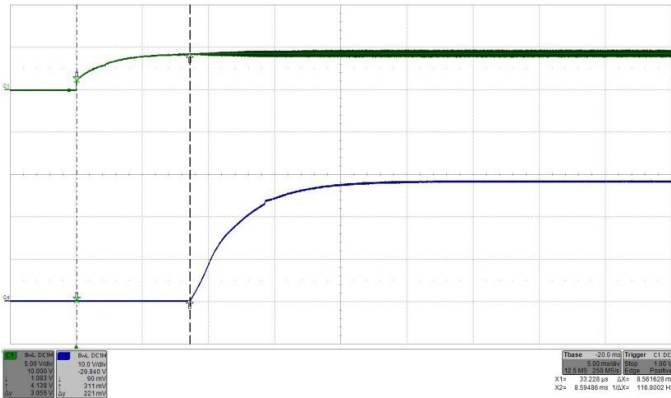


Fig. 28: Turn-on by ON/OFF transient (with Vin applied) at full rated load current (resistive) at Vin = 24V. Top trace (C1): ON/OFF signal (5 V/div.). Bottom trace (C4): Output voltage (10 V/div.). Time: 5 ms/div.

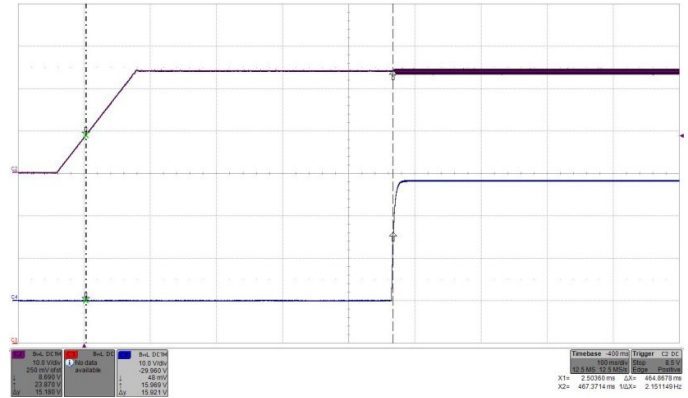


Fig. 29: Turn-on by Vin transient (ON/OFF high) at full rated load current (resistive) at Vin = 24V. Top trace (C2): Input voltage Vin (10 V/div.). Bottom trace (C4): Output voltage (10 V/div.). Time: 100 ms/div.

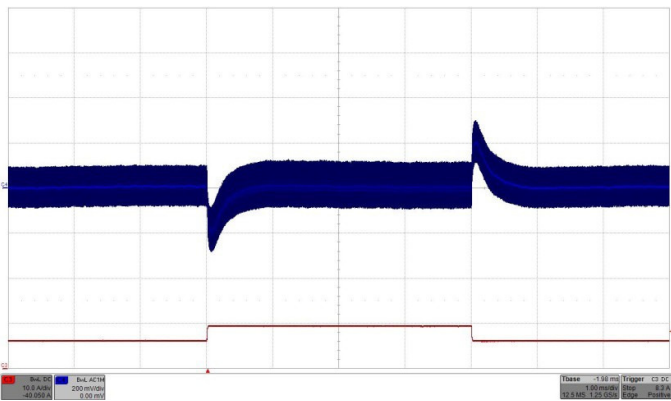


Fig. 30: Output voltage response to load current step change 50% - 75% - 50% (6.5A-9.75A-6.5A) with di/dt = 1A/μs at Vin = 24V. Top trace (C4): Output voltage (200 mV/div.). Bottom trace (C3): Load current (10A/div.). Co = 470μf/70mΩ. Time: 1ms/div.

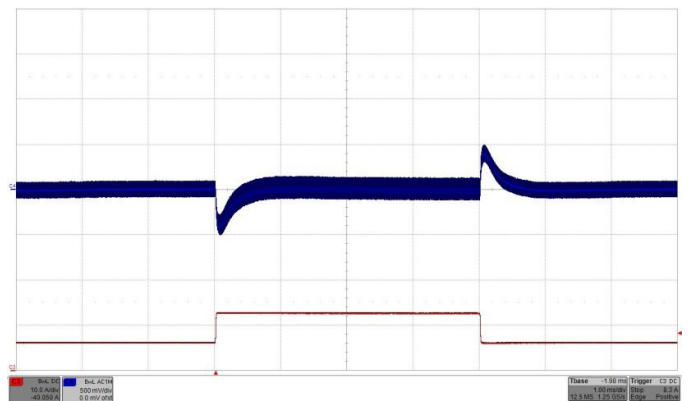


Fig. 31: Output voltage response to load current step change 50% - 100% - 50% (6.5A-13A-6.5A) with di/dt = 1A/μs at Vin = 24V. Top trace (C4): Output voltage (500 mV/div.). Bottom trace (C3): Load current (10A/div.). Co = 470μf/70mΩ. Time: 1ms/div.

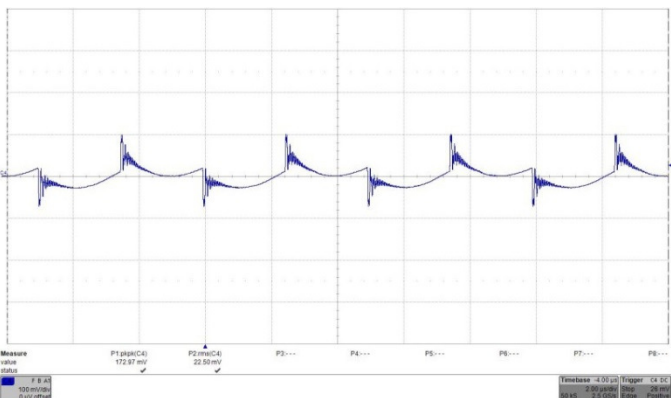


Fig. 32: Output voltage ripple (200mV/div.) at full rated load current into a resistive load at Vin = 24V. Co = 470 μF/70mΩ. Time: 2 μs/div.

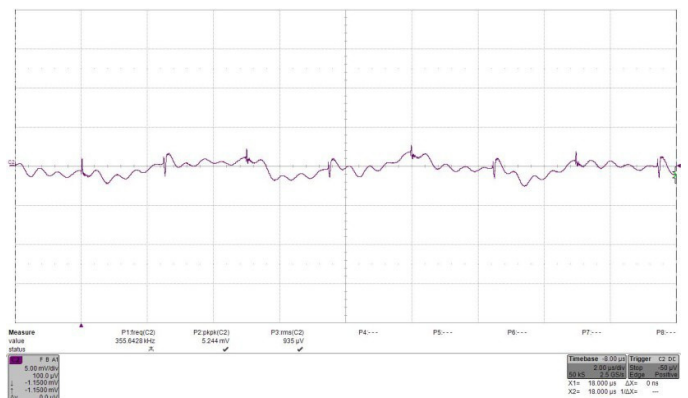
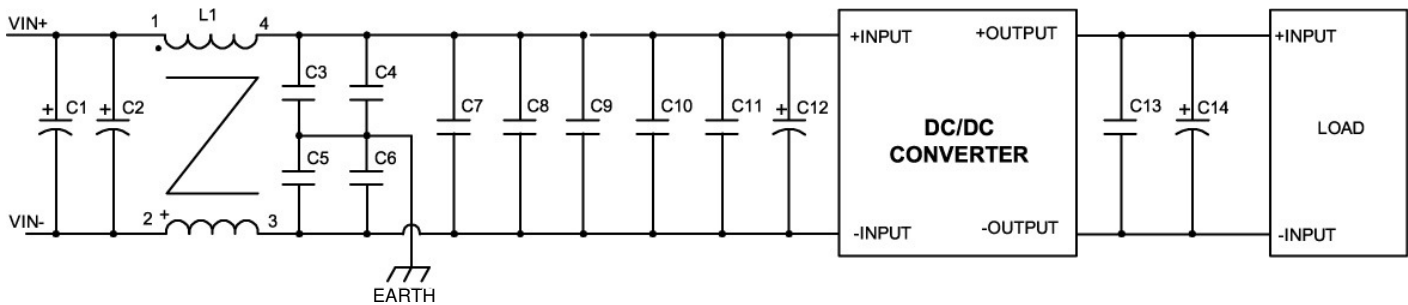


Fig. 33: Input reflected ripple current, ic (500mA/mV), measured at input terminals at full rated load current at Vin = 24V. Refer to Fig. 2 for test setup. Time: 2μs/div. RMS input ripple current is 0.935*500mA = 549mA.

EMC Consideration:

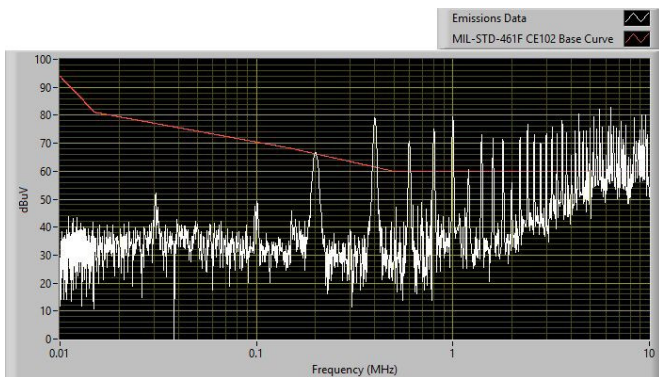
The filter schematic for suggested input filter configuration as tested to meet the conducted emission limits of MIL-STD 461F CE102 Base Curve is shown in Fig. 34. The plots of conducted EMI spectrum are shown in Fig. 35.

Note: Customer is ultimately responsible for the proper selection, component rating and verification of the suggested parts based on the end application.

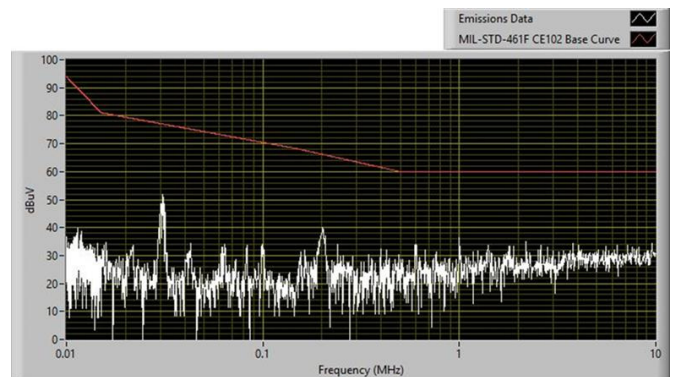


Comp. Des.	Description
C1, C2, C12, C14	470μF/50V/70mΩ Electrolytic Capacitor (Vishay MAL214699108E3 or equivalent)
C3, C4, C5, C6	4.7nF/1210/X7R/1500V Ceramic Capacitor
C7, C8, C9, C10, C11, C13	10μF/1210/X7R/50V Ceramic Capacitor
L1	CM choke: L = 130μH, L _{lkg} = 0.6μH (4 turns on toroid 22.1mm x 13.7mm x 7.92mm)

Fig. 34: Typical input EMI filter circuit to attenuate conducted emissions per MIL-STD-461F CE102 Base Curve.



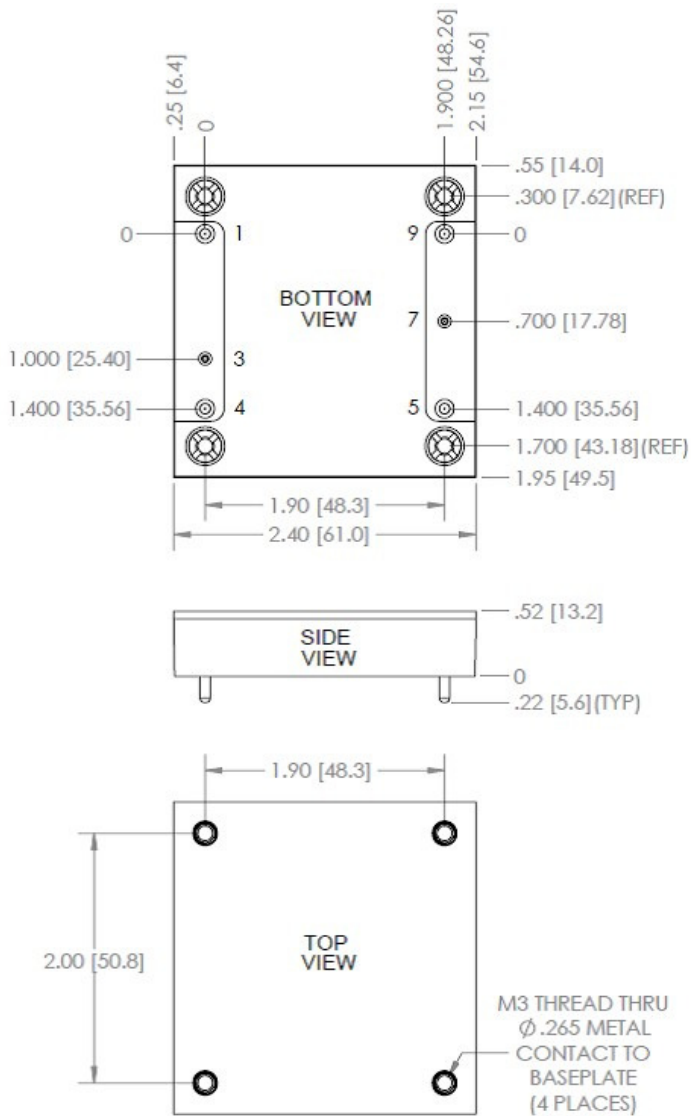
a) Without input filter. C_{IN} = 2 x 470μF/50V/70mΩ.



b) With input filter from Fig. 34.

Fig.35: Input conducted emissions measurement (Typ.) of ICH0415V1PC

Mechanical Specifications:



NOTES:

Unless otherwise specified:

All dimensions are in inches [millimeters]

Tolerances: x.xx in. ± 0.02 in. [x.x mm ± 0.5 mm]

x.xxx in. ± 0.010 in [x.xx mm ± 0.25 mm]

Torque fasteners into threaded mounting inserts at 10in.lbs. or less. Greater torque may result in damage to unit and void the warranty.

Input/Output Connections:

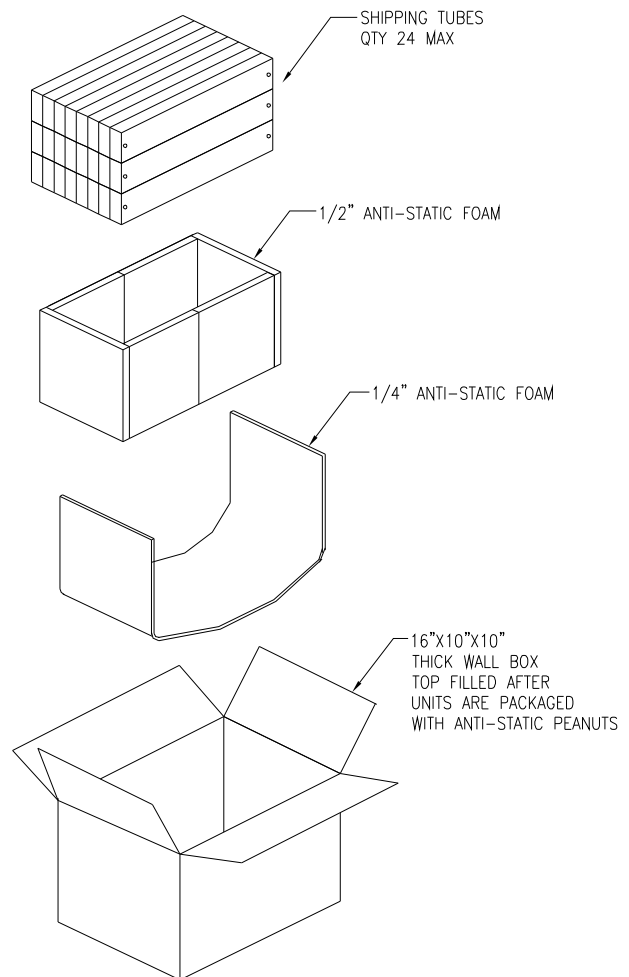
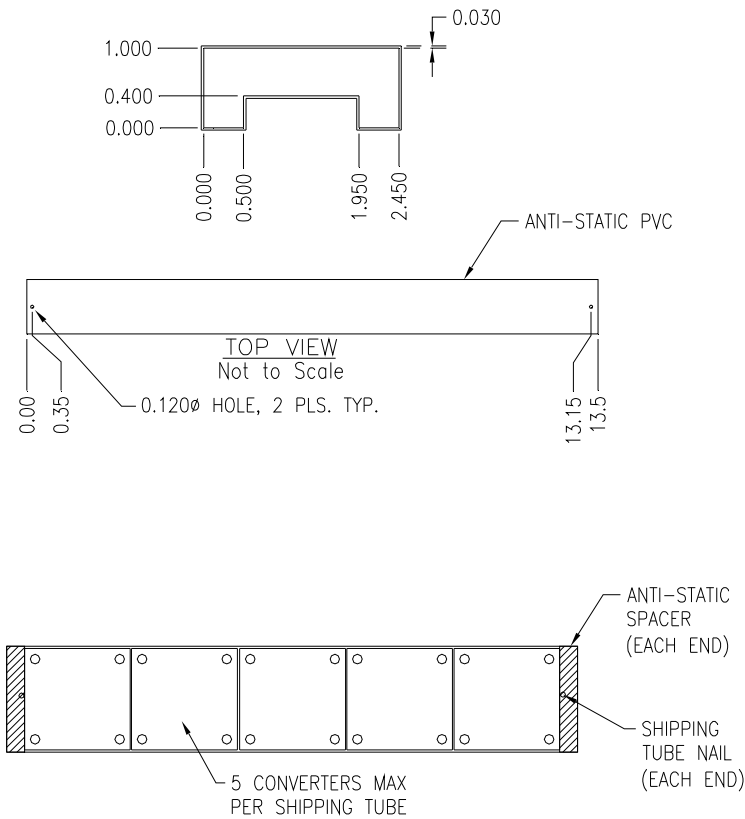
Pin	Name	Function
1	-INPUT	Negative input voltage
3	ON/OFF	TTL input with internal pull up, referenced to -INPUT, used to turn converter on and off
4	+INPUT	Positive input voltage
5	+OUTPUT	Positive output voltage
7	TRIM	Output voltage trim
9	- OUTPUT	Negative output voltage

Notes:

1) Pinout is inconsistent between manufacturers of the half brick converters. Make sure to follow the pin function and the pin number, when laying out your board.

2) Pin diameter for the input pins of the ICH converters has diameter 0.081" due to high input current at low line, and is different from other manufacturers of the half brick. Make sure to follow pin dimensions in your application.

Packaging Information:



1. SHIPPING TUBE MATERIAL: ANTI-STATIC PVC
2. ALL END VIEW DIMENSIONS ARE INSIDE DIMENSIONS.
3. ALL DIMENSIONS ARE ± 0.010 ".
4. CARDBOARD SHIPPING BOX IS 16" X 10" X 10"
5. MAXIMUM NUMBER OF UNITS (MPQ) PER BOX IS 120 CONVERTERS.
6. BOX IS TOP FILLED WITH ANTI-STATIC SHIPPING PEANUTS

Murata Power Solutions, Inc.
129 Flanders Road, Westborough MA 01581 U.S.A.
ISO 9001 and 14001 REGISTERED



This product is subject to the following operating requirements and the Life and Safety Critical Application Sales Policy: Refer to: <http://www.murata-ps.com/requirements/>

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