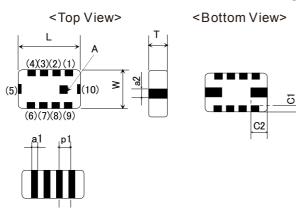


DUAL MATCHING FILTER (Preliminary)

1. Characteristics (at -40~+85°C)

Tentative Part Number	LFD21891MMF3D931TEMP		
Frequency Range (BW)	869.50 ± 6.50 MHz		
ANT Port Impedance (Nominal)	50 Ω		
Tx/Rx/TRx Port Impedance (Nominal)	Conjugate match with Si-Lab 446x series		
	Тx	1.7 dB Max at +25°C. 1.95 dB Max at -40~+85°C	
Insertion Loss in BW	Rx	3.3 dB Max at +25°C. 3.6 dB Max at -40~+85°C	
Attenuation (Absolute value)	Тх	34.0 dB Min. at 2f0 34.0 dB Min. at 3f0	
ANT Port V.S.W.R. in Tx BW	1.54 Max.		
ANT Port V.S.W.R. in Rx BW	2.60 Max.		
Power Capacity	500 mW max.		

2. Construction, Dimensions & Marking



Mark	Meaning	
Α	Directional Input Mark	
		(in mm)

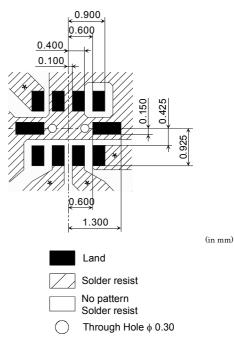
Mark	Dimension	Mark	Dimension
L	2.00±0.15	a2	0.30±0.15
W	1.25 ± 0.15	c1	0.20±0.15
Т	0.7max	c2	0.40±0.15
a1	0.25±0.15	p1	0.50±0.15

TERMINAL CONFIGURATION

Terminal No.	Terminal Name	Terminal No.	Terminal Name
(1)	NC	(6)	NC
(2)	GND	(7)	ТХ
(3)	GND	(8)	RX_N
(4)	ANT	(9)	RX_P
(5)	GND	(10)	GND

Terminal of "NC" should be fixed to the no connected pattern.

3. Land Pattern



*Line width to be designed to match 50\Omega characteristic impedance, depending on PCB material and thickness.

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