# **<b>∴**Caution/Notice

# **⚠**Caution

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## **⚠Caution**

### ■ Storage and Operation Conditions

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. Also avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 degrees centigrade and 20 to 70%. Use capacitors within 6 months after delivery.

## Rating

#### 1. Operating Voltage

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the V0-p which contains DC bias within the rated voltage range.

When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

When DC-rated capacitors are to be used in input circuits from commercial power source (AC filter), be sure to use Safety Recognized Capacitors because various regulations on withstand voltage or impulse withstand established for all equipment should be taken into consideration.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)	
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p	

### 2. Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may have self-generated heat due to dielectric loss. In the case of "High Dielectric Constant Type Capacitors," applied voltage load should be such that self-generated heat is within 20 °C under the condition where the capacitor is subjected at an atmosphere temperature of 25 °C. Please contact us if self-generated heat occurs with "Temperature Compensating Type Capacitors".

When measuring, use a thermocouple of small thermal capacity -K of Ø0.1mm under conditions where the capacitor is not affected by radiant heat from other components or wind from surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.

## 3. Fail-Safe

Be sure to provide an appropriate fail-safe function on your product to prevent a second damage that may be caused by the abnormal function or the failure of our product.

# **⚠**Caution



Continued from the preceding page.

### Soldering and Mounting

#### 1. Vibration and Impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

#### 2. Soldering

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

#### 3. Bonding, Resin Molding and Coating

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of the capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case the amount of application, dryness/ hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor may be damaged by the organic solvents and may result, worst case, in a short circuit.

The variation in thickness of adhesive or molding resin or coating may cause an outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

4. Treatment after Bonding, Resin Molding and Coating When the outer coating is hot (over 100 degrees centigrade) after soldering, it becomes soft and fragile, so please be careful not to give it mechanical stress.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

## ■ Rating

## 1. Capacitance change of capacitor

In case of F/X7R/X7S/X7T/X8L/Y5V/Z5U char.

Capacitors have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor is left on for a long time. Moreover, capacitance might change greatly depending on the surrounding temperature or an applied voltage.

## ■ Soldering and Mounting

## 1. Cleaning (ultrasonic cleaning)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

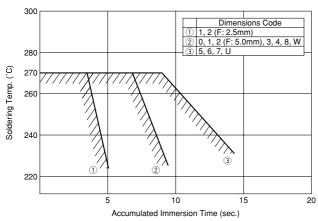
Rinsing time: 5 min. maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

### 2. Soldering and Mounting

## (1) Allowable Conditions for Soldering Temperature and Time



Perform soldering within tolerance range (shaded portion).

#### (2) Insertion of the Lead Wire

- · When soldering, insert the lead wire into the PCB without mechanically stressing the lead wire.
- $\cdot$  Insert the lead wire into the PCB with a distance appropriate to the lead space.

Code Temp. Char.		X7T		
2	(№ 683 K47	(M 153) K97	(M 153)	
3, 8	(M 334 K47	(M 104 K97	(M 223 K77	
5, U	(225 M47	(M) 474 K97	(M) 474 M77	
Temperature Characteristics	Marked with code (X7T char.: 7)			
Nominal Capacitance	Marked with 3 figures			
Capacitance Tolerance	Marked with code			
Rated Voltage	Marked with code (DC250V: 4, DC450V: 9, DC630V: 7)			
Manufacturer's Identification	Marked with M			

1	Operating Temperature Range		-55 to +125°C		-	
2	2 Appearance		No defects or abnormalities	Visual inspection		
3	3 Dimension and Marking		See previous pages	Visual inspection,	Vernier Caliper	
		Between Terminals	No defects or abnormalities		ld not be damaged when voltabetween the terminations current ≤ 50mA)  Test Voltage 200% of the rated voltage 150% of the rated voltage 120% of the rated voltage	
4	4 Dielectric Strength  Body Insulation		No defects or abnormalities	The capacitor is place container with meta diameter so that each short-circuit, is kep 2mm from the balls the figure, and 200 DC voltage is impresed. between capa and metal balls. (Charge/Discharge ≤ 50mA)	al balls of 1mm ach terminal, bit approximately s as shown in 1% of the rated essed for 1 to 5 acitor terminals	
5	Insulation Between Resistance Terminals		More than 10,000M $\Omega$ or 100M $\Omega \cdot \mu F$ , Whichever is smaller	The insulation resistance should be measured DC500±50V (DC250±25V in case of rated volta DC250V,DC450V) at normal temperature and and within 2 min. of charging. (Charge/Discharge current ≤ 50mA)		
6	Capacitance		Within the specified tolerance	The capacitance/D.F. should be measured a		
7	7 Dissipation Factor (D.F.)		0.01 max.	frequency of 1±0.1kHz and a voltage of AC1±0.2V(r.m.s.).		
8	Capacitance 8 Temperature Characteristics		Within +22/-33%		hange should be measured af cified temperature stage.  Temperature (°C)  25±2  -55±3  25±2  125±3  25±2	
9	Terminal Strength	Tensile Strength	Termination not to be broken or loosened	As in the figure, fix the capacitor body, apply gradually to each lead in the radial direction of capacitor until reaching 10N and then keep the applied for 10±1 sec.  Each lead wire should be subjected to a force and then bent 90° at the point of egress in one		
		Strength Termination not to be broken or loosened	Termination not to be broken or loosened	and then bent 90° at the point of egress in a direction. Each wire is then returned to the position and bent 90° in the opposite directionate of one bend per 2 to 3 sec.		
		Appearance	No defects or abnormalities		uld be firmly soldered to the	
10	Vibration	Capacitance	Within the specified tolerance	supporting lead wire and vibrated at a frequent of 10 to 55Hz, 1.5mm in total amplitude, with		
10	Resistance	D.F.	0.01 max.	minute rate of vibration change from 10Hz to back to 10Hz. Apply for a total of 6 hrs., 2 hrs mutually perpendicular directions.		

Continued on the following p



11	11 Solderability of Leads		Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The terminal of a capacitor is dipped into a solu ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25 in weight proportion) and then into molten solde Z-3282) for 2±0.5 sec. In both cases the depth dipping is up to about 1.5 to 2mm from the term body.  Temp. of solder: 245±5°C Lead Free Solder (Sn-3.04) 235±5°C H60A or H63A Eutectic Sc		(-5902) (25° olten solder the depth o m the termin ler (Sn-3.0Aç	
		Appearance	No defects or abnormalities	The lead wi	ro ie imm	arand in the melt	ad colder 1
	Resistance to	Capacitance Change	Within ±10%	The lead wire is immersed in the melted solder 1 2mm from the main body at 350±10°C for 3.5±0. The specified items are measured after 24±2 hrs			
12	Soldering Heat	Dielectric Strength (Between Terminals)	No defects	Pretreatment     Perform a heat treatment at 150+0/-10°C f     then let sit at room temperature for 24±2 f			
		Appearance	No defects or abnormalities	The capacitor should be subjected to 5 t		5 temperat	
		Capacitance Change	Within ±7.5%	cycles. Step		perature (°C)	Time (m
		D.F.	0.01 max.	1		-55±3	30±3
13	Temperature	Insulation	o.or max.	3		om Temp. 125±3	3 max 30±3
13	Cycle	Resistance	More than 10,000M $\Omega$ or 100M $\Omega \cdot \mu F$ (Whichever is smaller)	4		om Temp.	30±3
		Dielectric Strength (Between Terminals)	No defects or abnormalities	Pretreatment Perform a heat treatment at 150+0/-10°C for then let sit at room temperature for 24±2 hrs.			
		Appearance	No defects or abnormalities	Set the capacitor at 40±2°C and relative humi			
14	Humidity (Steady	Capacitance Change	Within ±12.5%	to 95% for 500 $\pm^2$ 0 hrs. Remove and set for 24± at room temperature, then measure.			
14	State)	D.F.	0.02 max.	Pretreatment	ent		
	o.u.o,	Insulation Resistance	More than 1,000M $\Omega$ or 10M $\Omega \cdot \mu F$ (Whichever is smaller)	Perform a heat treatment at 150+0/-10°C for 1 hi then let sit at room temperature for 24±2 hrs.			
	Humidity	Appearance	No defects or abnormalities	Apply the rated voltage at 40±2°C and relative			d relative hu
		Capacitance Change	Within ±12.5%	of 90 to 95% for 500 ±26 hrs. Remove and set 24±2 hrs. at room temperature, then measure. (Charge/Discharge current ≤ 50mA)			
15	Load	D.F.	0.02 max.				
		Insulation Resistance	More than 1,000M $\Omega$ or 10M $\Omega$ · $\mu$ F (Whichever is smaller)	Pretreatment     Perform a heat treatment at 150+0/-10°C for then let sit at room temperature for 24±2 hrs			
		Appearance	No defects or abnormalities	Apply voltage in Table for 1000 ±48		e for 1000 <sup>+48</sup> <sub>0</sub> h	nrs. at the
		Capacitance Change	Within ±12.5%	24±2 hrs. a	t room ter	temperature. Rer mperature, then urrent ≦ 50mA)	
		D.F.	0.02 max.	l `		·	- l+
16 Tem	High Temperature Load	Insulation Resistance	More than 1,000M $\Omega$ or $10M\Omega \cdot \mu F$ (Whichever is smaller)	Rated V DC250V DC450V DC630V  • Pretreatme Apply test verification	ent	150% of the 130% of the 120% of the 1 hr., at test tem	rated voltag rated voltag
				and set for	24±2 hrs.	at room temper	ature.
		Appearance	No defects or abnormalities			be fully immerse	, ,
17	Solvent Resistance	Marking	Legible	reagent at 20 to 25 °C for 30±5 sec. and then gently. Marking on the surface of the capacitor immediately be visually examined.  Reagent:  Isopropyl alcohol			



## Packaging

Two types of packaging for monolithic ceramic capacitors are available.

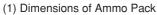
## 1. Bulk Packaging

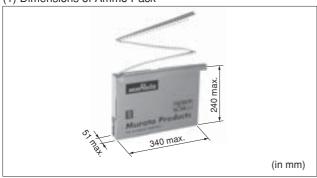
Minimum Quantity

Dimensions Code	Dimensions (L×W)	Minimum Quantity (pcs./Bag)*	
0	3.6×3.5mm or 4.0×3.5mm or 5.0×3.5mm (Depends on Part Number)		
1	4.0×3.5mm or 4.5×3.5mm or 5.0×3.5mm (Depends on Part Number)		
2	5.0×3.5mm or 5.5×4.0mm or 5.7×4.5mm (Depends on Part Number)		
3	5.0×4.5mm or 5.5×5.0mm or 6.0×5.5mm (Depends on Part Number)	500	
4 7.5×5.5mm		500	
5	7.5×7.5mm or 7.5×8.0mm (Depends on Part Number)		
6	10.0×10.0mm		
8	7.5×5.5mm		
7	12.5×12.5mm	100	
U	7.7×12.5mm or 7.7×13.0mm (Depends on Part Number)	200	
W	5.5×7.5mm or 6.0×8.0mm (Depends on Part Number)	500	

Please order with an integral multiple of the minimum quantity above.

## 2. Tape Carrier Packaging





## (2) Minimum Quantity

Dimensions Code	Dimensions (L×W)	Minimum Quantity (pcs./Ammo Pack)*	
0	3.6×3.5mm or 4.0×3.5mm or 5.0×3.5mm (Depends on Part Number)		
1	4.0×3.5mm or 4.5×3.5mm or 5.0×3.5mm (Depends on Part Number)		
2	5.0×3.5mm or 5.5×4.0mm or 5.7×4.5mm (Depends on Part Number)	2000	
3	5.0×4.5mm or 5.5×5.0mm or 6.0×5.5mm (Depends on Part Number)		
4	7.5×5.5mm		
5	7.5×7.5mm or 7.5×8.0mm (Depends on Part Number)	2000	
6	10.0×10.0mm	1500	
8	7.5×5.5mm	1500	
U	7.7×12.5mm or 7.7×13.0mm (Depends on Part Number)	1000	
W	5.5×7.5mm or 6.0×8.0mm (Depends on Part Number)	1500	

Please order with an integral multiple of the minimum quantity above.

Please check our website 'Product details'.

"Minimum Quantity" means the numbers of units of each delivery or order. The quantity should be an integral multiple of the "minimum quantity". (Please note that the actual delivery quantity in a package may change sometimes.)

<sup>\*</sup> Minimum Quantity may change depends on part number.

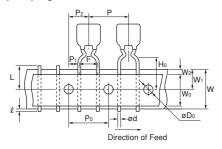
Please check our website 'Product details'.

<sup>\*</sup> Minimum Quantity may change depends on part number.

 $\begin{tabular}{|c|c|c|c|c|c|} \hline \end{tabular}$  Continued from the preceding page.

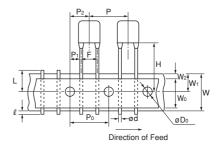
## ■ Taping Dimensions

## Inside Crimp Taping



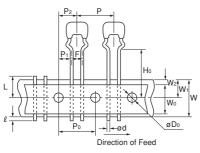
Dimensions and Lead Style Code
0M1
1M1
2M1
2M2
3M1
3M2
4M1
4M2
8M1
8M2
WM1

## Straight Taping

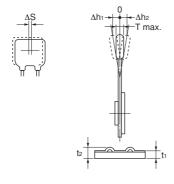


Dimensions and Lead Style Code
1DB
2DB
3DB
5E1
5E2
6E1
6E2
UE1

# Outside Crimp Taping



Dimensions and Lead Style Code		
0\$1		
1\$1		
2\$1		
2\$2		
3S1		
3S2		



Itom	Code	Dim	anaiana (mm)	
Item		DIII	nensions (mm)	
Pitch of Component	Р	12.7±1.0		
Pitch of Sprocket Hole	P <sub>0</sub>	12.7±0.2		
Lead Spacing	F	2.5 <sup>+0.4</sup> <sub>-0.2</sub> (DB) (S1) (S2)		
Lead opacing	'		5.0 <sup>+0.6</sup> -0.2	
Length from Hole Center to Component Center	P <sub>2</sub>		6.35±1.3	
	P <sub>1</sub>		3.85±0.7	
Length from Hole Center to Lead	P1	5.1±0	.7 (DB) (S1) (S2)	
Leau	254±1.5	5 Total length	Total length of components pitch × 20	
Body Dimension	[	Depends o	n Part Number	
Deviation Along Tape, Left or Right Defect	ΔS		±2.0	
Carrier Tape Width	W		18.0±0.5	
Position of Sprocket Hole	W <sub>1</sub>	9.0+0		
Lead Distance between	Ho	16.0±0.5 (M1) (S1)		
Reference and Bottom Plane	ПО	20.0±0.5 (M2) (S2)		
For Straight Lead Type	Н	20±0.5 (E2),17.5±0.5 (E1),16±0.5 (D		
Diameter of Sprocket Hole	Do	4.0±0.1		
Lead Diameter	d	0.5±0.05		
Total Tape Thickness	t1	0.6±0.3		
Total Thickness of Tape and Lead Wire	t2	1.5 max.		
Body Thickness	Т	Depend	ls on Part Number	
		2.0 max.	Dimensions Code: W, U	
Deviation Across Tape	Δh1 Δh2	1.5 max.	RHD Series	
		1.0 max.	except as above	
Portion to Cut in Case of Defect	L		11.0 +0	
Protrusion Length	l		0.5 max.	
Hold Down Tape Width	Wo	9.5 min.		
Hold Down Tape Position	W <sub>2</sub>	1.5±1.5		
Coating Extension		Depends on Dimensions		

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