muRata

Reference Specification

Leaded MLCC for General Purpose RDE Series

Product specifications in this catalog are as of Dec. 2017, and are subject to change or obsolescence without notice.

Please consult the approval sheet before ordering. Please read rating and Cautions first.

⚠ CAUTION

1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.

When DC-rated capacitors are to be used in input circuits from commercial power source (AC filter), be sure to use Safety Recognized Capacitors because various regulations on withstand voltage or impulse withstand established for each equipment should be taken into considerations.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage(1)	Pulse Voltage(2)
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. In case of Class 2 capacitors (Temp.Char. : X7R,X7S,X8L, etc.), applied voltage should be the load such as self-generated heat is within 20 °C on the condition of atmosphere temperature 25 °C. Please contact us if self-generated heat is occurred with Class 1 capacitors (Temp.Char. : C0G,U2J,X8G, etc.). When measuring, use a thermocouple of small thermal capacity-K of ϕ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.

3. Fail-safe

Be sure to provide an appropriate fail-safe function on your product to prevent a second damage that may be caused by the abnormal function or the failure of our product.

4. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 °C and 20 to 70%. Use capacitors within 6 months.

5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

7. BONDING AND RESIN MOLDING, RESIN COAT

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of a bonded or molded product in the intended equipment. In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive or molding resin may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

8. TREATMENT AFTER BONDING AND RESIN MOLDING, RESIN COAT

When the outer coating is hot (over 100 °C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9. LIMITATION OF APPLICATIONS

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

1. Aircraft equipment 2. Aerospace equipment

3. Undersea equipment 4. Power plant control equipment

5. Medical equipment6. Transportation equipment (vehicles, trains, ships, etc.)7. Traffic signal equipment8. Disaster prevention / crime prevention equipment

9. Data-processing equipment exerting influence on public

10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

NOTICE

1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

2. Soldering and Mounting

Insertion of the Lead Wire

- When soldering, insert the lead wire into the PCB without mechanically stressing the lead wire.
- Insert the lead wire into the PCB with a distance appropriate to the lead space.

3. CAPACITANCE CHANGE OF CAPACITORS

Class 2 capacitors (Temp.Char. : X7R,X7S,X8L, etc.)

Class 2 capacitors an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit.

Please contact us if you need a detail information.

⚠ NOTE

- 1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

1. Application

This product specification is applied to Leaded MLCC RDE series used for General Electronic equipment. Do not use these products in any automotive power train or safety equipment including battery chargers for electric vehicles and plug-in hybrids.

2. Rating

• Part number configuration

ex.) RDE	D7	2E	104	K	3	K1	H03	В
Series	Temperature	Rated	Capacitance	Capacitance	Dimension	Lead	Individual	Packing
	Characteristic	voltage		tolerance	code	code	specification	style
							code	code

• Temperature characteristic

Code	Temp. Char.	Temp. Range	Cap. Change (Within%)	Standard Temp.	Operating Temp.Range
D7	X7T	-55~125°C	+22/-33	25°C	-55~125°C

Rated voltage

Code	Rated voltage
2E	DC250V
2W	DC450V
2J	DC630V

Capacitance

The first two digits denote significant figures; the last digit denotes the multiplier of 10 in pF. ex.) In case of 104

 $10 \times 10^4 = 100000 pF$

• Capacitance tolerance

Code	Capacitance Tolerance
K	+/-10%
M	+/-20%

• Dimension code

Code	Dimensions (LxW) mm max.
2	5.5 x 4.0
3	5.5 x 5.0
4	7.5 x 5.5
5	7.5 x 7.5 *
U	7.7 x12.5 *

*DC630V: W+0.5mm

• Lead code

_			
	Code	Lead style	Lead spacing (mm)
	B1	Straight type	5.0+/-0.8
	E1	Straight taping type	5.0+0.6/-0.2
	K1	Inside crimp type	5.0+/-0.8
	M1	Inside crimp taping type	5.0+0.6/-0.2

Lead wire is solder coated CP wire.

• Individual specification code Murata's Control Code Please refer to [Part number list].

• Packing style code

Code	Packing style
Α	Taping type of Ammo
В	Bulk type

3. Marking

Temp. Char. : Letter code : 7 (X7T char.) Temp. Char. : Letter code : 7 (X)
Capacitance : 3 digit numbers

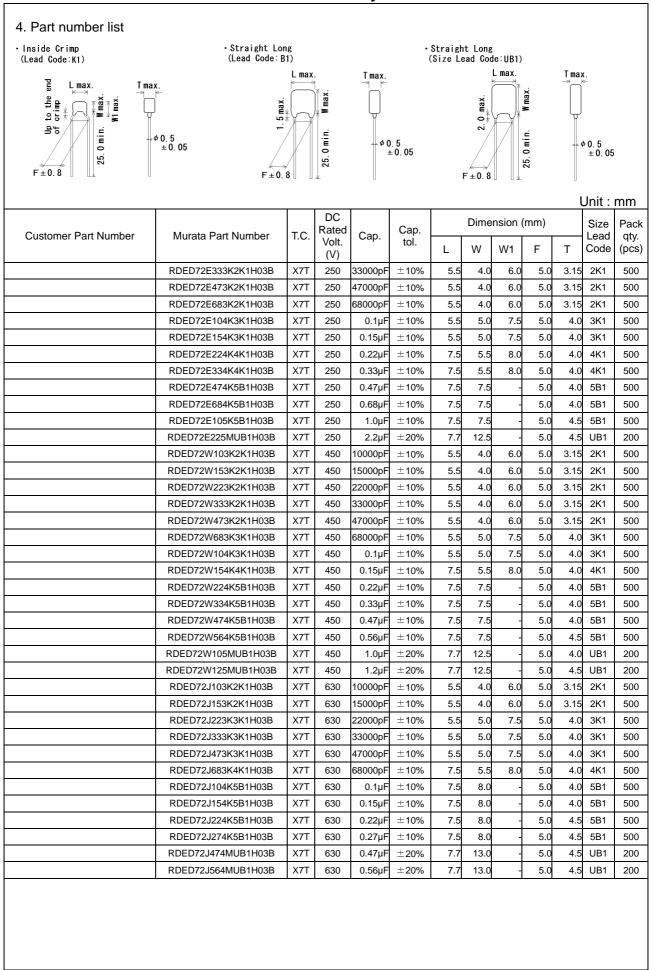
Capacitance Tolerance: Code

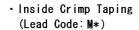
Rated voltage : Letter code : 4 (DC250V)

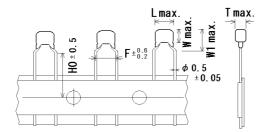
Letter code: 9 (DC450V) Letter code: 7 (DC630V)

Company name code : Abbreviation : [M

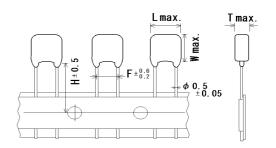
(Ex.) Rated voltage DC450V DC630V DC250V Dimensions **€**683 K47 **€**153 K77 **€**153 K97 2 **€**334 **(**104 **€** 223 3, 4 K97 K77 K47 **(**≁ 474 **⋈** 474 **€** 225 5, U M47 K97 M77







Staight Taping (Lead Code:E*)



		,											
Customer Bort Number	Museum Deut Nusseline	T.C.	DC Rated	Cap.	0 4-1		Di	mensi	on (mn	n)		Size	Pack
Customer Part Number	Murata Part Number	1.0.	volt. (V)	Сар.	Cap. tol.	L	W	W1	F	Т	H/H0	Lead Code	qty. (pcs)
	RDED72E333K2M1H03A	X7T	250	33000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72E473K2M1H03A	X7T	250	47000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72E683K2M1H03A	X7T	250	68000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72E104K3M1H03A	X7T	250	0.1µF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RDED72E154K3M1H03A	X7T	250	0.15µF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RDED72E224K4M1H03A	X7T	250	0.22µF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RDED72E334K4M1H03A	X7T	250	0.33µF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RDED72E474K5E1H03A	X7T	250	0.47µF	±10%	7.5	7.5	-	5.0	4.0	17.5	5E1	1500
	RDED72E684K5E1H03A	X7T	250	0.68µF	±10%	7.5	7.5	-	5.0	4.0	17.5	5E1	1500
	RDED72E105K5E1H03A	X7T	250	1.0µF	±10%	7.5	7.5	-	5.0	4.5	17.5	5E1	1500
	RDED72E225MUE1H03A	X7T	250	2.2µF	±20%	7.7	12.5	-	5.0	4.5	17.5	UE1	1000
	RDED72W103K2M1H03A	X7T	450	10000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72W153K2M1H03A	X7T	450	15000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72W223K2M1H03A	X7T	450	22000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72W333K2M1H03A	X7T	450	33000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72W473K2M1H03A	X7T	450	47000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72W683K3M1H03A	X7T	450	68000pF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RDED72W104K3M1H03A	X7T	450	0.1µF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RDED72W154K4M1H03A	X7T	450	0.15µF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RDED72W224K5E1H03A	X7T	450	0.22µF	±10%	7.5	7.5	-	5.0	4.0	17.5	5E1	1500
	RDED72W334K5E1H03A	X7T	450	0.33µF	±10%	7.5	7.5	-	5.0	4.0	17.5	5E1	1500
	RDED72W474K5E1H03A	X7T	450	0.47µF	±10%	7.5	7.5	-	5.0	4.0	17.5	5E1	1500
	RDED72W564K5E1H03A	X7T	450	0.56µF	±10%	7.5	7.5	-	5.0	4.5	17.5	5E1	1500
	RDED72W105MUE1H03A	X7T	450	1.0µF	±20%	7.7	12.5	-	5.0	4.0	17.5	UE1	1500
	RDED72W125MUE1H03A	X7T	450	1.2µF	±20%	7.7	12.5	-	5.0	4.5	17.5	UE1	1000
	RDED72J103K2M1H03A	X7T	630	10000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72J153K2M1H03A	X7T	630	15000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RDED72J223K3M1H03A	X7T	630	22000pF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RDED72J333K3M1H03A	X7T	630	33000pF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RDED72J473K3M1H03A	X7T	630	47000pF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RDED72J683K4M1H03A	X7T	630	68000pF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RDED72J104K5E1H03A	X7T	630	0.1µF	±10%	7.5	8.0	-	5.0	4.0	17.5	5E1	1500
	RDED72J154K5E1H03A	X7T	630	0.15µF	±10%	7.5	8.0	-	5.0	4.0	17.5	5E1	1500
	RDED72J224K5E1H03A	X7T	630	0.22µF	±10%	7.5	8.0	-	5.0	4.5	17.5	5E1	1500
	RDED72J274K5E1H03A	X7T	630	0.27µF	±10%	7.5	8.0	-	5.0	4.5	17.5	5E1	1500
	RDED72J474MUE1H03A	X7T	630	0.47µF	±20%	7.7	13.0		5.0	4.5	17.5	UE1	1000
	RDED72J564MUE1H03A	X7T	630	0.56µF	±20%	7.7	13.0		5.0	4.5	17.5	UE1	1000

	1		TEST METHODS	-					
No.		Item Specification			Test Method Visual inspection.				
2	Appearance Dimension	e and Marking	No defects or abnormalities Within the specified dimensions and Marking				Jsing Caliper.		
3	Dielectric Strength	Between Terminals	No defects or abnormalities		able is app	lied	lld not be damaged when we between the terminations for harge/Discharge current ≤	or	
					Rated vol	age	Test voltage		
					DC250	V	200% of the rated voltage		
					DC450	V	150% of the rated voltage		
					DC630	V	120% of the rated voltage		
Body Insulation		Body Insulation	No defects or abnormalities	of 1 is k in t ca bal (Ch	The capacitor is placed in a container with meta of 1mm diameter so that each terminal, short-ci is kept approximately 2mm from the balls, and vin table is impressed for 1 to 5 seconds between capacitor terminals and metal balls. (Charge/Discharge current ≤ 50mA.) Rated voltage Test voltage DC250V · DC450V 200% of the rated voltage			-circuit d voltaç een	
					DC630	V	DC1300V		
4	Insulation Resistance (I.R.)		10,000M Ω or 100M Ω ·μF min. (Whichever is smaller)	DC DC hur (Ch	The insulation resistance should be measured on DC500V (DC250V in case of rated voltage: DC250V,DC450V) at normal temperature and humidity and within 2 minutes of charging. (Charge/Discharge current is ≤ 50mA)			I	
5	Capacitano	e	Within the specified tolerance		The capacitance, D.F. should at the frequency and voltage s		nd voltage shown in the tab		
6	Dissipation Factor		actor 0.01 max.		Item	Jilai.	X7T		
	(D.F.)				Frequen	су	1±0.1kHz		
					Voltage	Э	AC1±0.2Vrms		
7	Capacitance Temperature Characteristics		Tormination not to be broken or leasoned	• Pi	Step 1 2 3 4 5 retreatmenterform a head our and the bours.	erat teat tr	Temperature(°C) 25±2 -55±3 25±2 125±3 25±2 25±2 eatment at 150+0/-10°C fot at *room condition for 24±	r one	
8	Terminal Tensile Strength Strength				oly the force he radial di il reaching	e gra recti 10N	the capacitor body, dually to each lead on of the capacitor and then keep or 10±1 seconds.		
		Bending Strength	Termination not to be broken or loosened	2.5 one orig	N and then direction. It is direction. It is a single position.	be b Each n an	ould be subjected to a force pent 90° at the point of egra n wire is then returned to the d bent 90° in the opposite e of one bend per 2 to 3 se	ess in ne	
9	Vibration	Appearance	No defects or abnormalities	The	e capacitor	shou	ald be subjected to a simple	Э	
	Resistance	Capacitance	Within the specified tolerance				aving a total amplitude of 1 g varied uniformly betweer		
		D.F.	0.01max.	app The to 1 Thi	proximate li e frequency 0Hz, shall s motion sh ch 3 mutual	mits rang be tr nall b	g valued difficulting between of 10Hz and 55Hz. ge, from 10Hz to 55Hz and aversed in approximately 1 e applied for a period of 2 prendicular directions (total	return minut hours	

ESRDE109A

No.		Item Specification derability of Lead Solder is deposited on unintermittently				Test Meth			
10	Solderability of Lead		Solder is deposited on unintermittently immersed portion in axial direction covering 3/4 or more in circumferential direction of lead wires.	The terminal of capacitor is dipped into a solution of ethanol (JIS K 8101) and rosin (JIS K 5902) (25% rosin in weight propotion). Immerse in solder solutio for 2±0.5 seconds. In both cases the depth of dippir is up to about 1.5 to 2mm from the terminal body. Temp. of solder: 245±5°C Lead Free Solder (Sn-3.0Ag-0.5Cu) 235±5°C H60A or H63A Eutectic Solder					
11-1	Resistance	Appearance	No defects or abnormalities				ersed in the m	nelted	
	to Soldering Heat	Capacitance Change	Within ±10%		.5 to 2.0mm f c for 10±1 sec		oot of termina	ıl at	
	(Non- Preheat)	Dielectric Strength (Between terminals)	No defects	hour, to before • Post-	itor should be hen place at initial measu treatment itor should be	*room cor irement.	ut 150+0/-10°0 dition for 24± or 24±2 hours	-2 houi	
11-2	Resistance	Appearance	No defects or abnormalities	First the	capacitor sh	ould be st	ored at 120+	0/-5°C	
	to Soldering Heat (On-	Capacitance Change	Within ±10%	Then, th			immersed in		
	Preheat)	Dielectric Strength	No defects		for 7.5+0/-1			O TTTTTT	
		(Between terminals)		hour, to before • Post-tr	itor should be hen place at initial measu eatment itor should be	*room cor irement.	at 150+0/-10°0 dition for 24± or 24±2 hours	-2 houi	
1-3	Resistance	Appearance	No defects or abnormalities	Test condition Termperature of iron-tip: 350±10°C					
	to Soldering Heat (soldering	Capacitance Change	Within ±10%	Termpe Solderi Solderin					
	iron method)	Dielectric Strength (Between terminals)	No defects	Straight Lead:1.5 to 2.0mm from the root of Crimp Lead:1.5 to 2.0mm from the end of lead: • Pre-treatment Capacitor should be stored at 150+0/-10°C hour, then place at *room condition for 24±2 before initial measurement. • Post-treatment Capacitor should be stored for 24±2 hours condition.					
12	Temperature Cycle		No defects or abnormalities Within ±12.5%	listed in	the following	table.	ne 4 heat treat then	atment	
		Capacitance Change	WILLIIII ±12.5%	measure		11 101 2412	. Hours, then		
		D.F.	0.01max.	Step	1	2	3	4	
		I.R.	1 ,000MΩ or 50MΩ·μF min. (Whichever is smaller)	Temp. (°C)	Min. Operating Temp. ±3	Room Temp.	Max. Operating Temp. ±3	Roc Tem	
			(Whichever is smaller)	Time (min.)	30±3	3 max.	30±3	3 m	
		Dielectric Strength (Between Terminals)	No defects or abnormalities	Pretrea Perfori	m a heat trea		50+0/-10°C fondition for 24		
13	Humidity (Steady	Appearance	No defects or abnormalities	Set the o	capacitor at 4 90 to 95% fo	r 500+24/	-0 hours.		
	State)	Capacitance Change	Within ±12.5%	Remove then me		room cond	lition for 24±2	2 hours	
		D.F.	0.02 max.	Pretrea					
		I.R.	1,000MΩ or 50MΩ·μF min. (Whichever is smaller)		m a heat trea nd then set a		50+0/-10°C f		

^{* &}quot;room condition" Temperature:15 to 35°C, Relative humidity:45 to 75%, Atmosphere pressure:86 to 106kPa

ESRDE109A

No.	Ite	em	Specification	Test Method					
14	Humidity Load	Appearance Capacitance	No defects or abnormalities Within ±12.5%	Apply the rated voltage at 40±2°C and relative humidity of 90 to 95% for 500+24/-0 hours. Remove and set at *room condition for 24±2 hours.					
		Change		then measure.					
		D.F.	0.02 max.	(Charge/Discharge current ≤ 50mA)					
		I.R.	500M Ω or 25M Ω ·μF min. (Whichever is smaller)	Pretreatment Perform a heat treatment at 150+0/-10°C for one hour and then set at *room condition for 24±2 hours.					
15	High Temperature	Appearance	No defects or abnormalities	Apply voltage in Table for 1000+48/-0 hours at the maximum operating temperature ±3°C.					
	Load	Capacitance Change	Within ±12.5%	Remove and set at *room condition for 24±2 hours, then measure.					
		D.F.	0.02 max.	(Charge/Discharge current ≤ 50mA)					
		I.R.	1 ,000M Ω or 50M Ω ·μF min. (Whichever is smaller)	Rated voltage Test voltage					
				DC250V 150% of the rated voltage					
				DC450V 130% of the rated voltage					
				DC630V 120% of the rated voltage					
				 Pretreatment Apply test voltage for one hour at test temperature. Remove and set at *room condition for 24±2 hours. 					
16	Solvent Resistance	Appearance	No defects or abnormalities	The capacitor should be fully immersed, unagitated, in reagent at 20 to 25°C for 30±5 sec. and then					
		Marking	Legible	remove gently. Marking on the surface of the capacitor shall immendiately be visually examined.					
				Regent : Isopropyl alcohol					

^{* &}quot;room condition" Temperature:15 to 35°C, Relative humidity:45 to 75%, Atmosphere pressure:86 to 106kPa

ESRDE109A

6. Packing specification

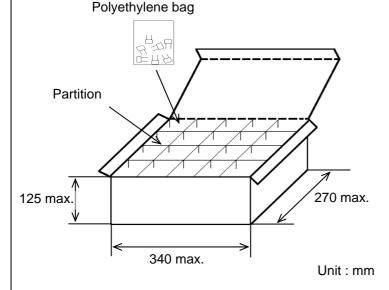
•Bulk type (Packing style code : B)

The size of packing case and packing way

The number of packing = *1 Packing quantity *2 n

*1 : Please refer to [Part number list].

*2 : Standard n = 20 (bag)

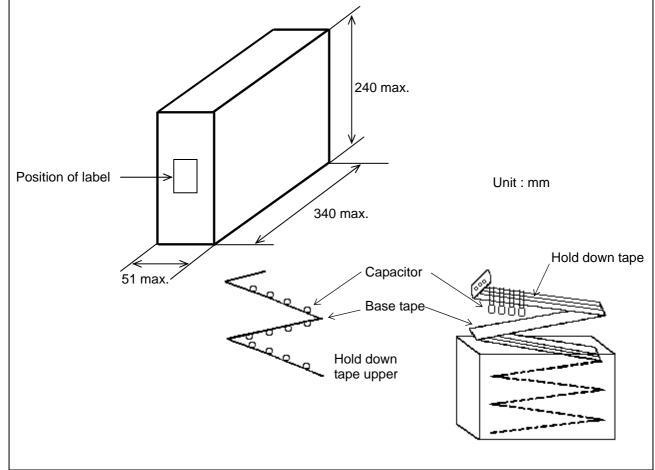


Note)

The outer package and the number of outer packing be changed by the order getting amount.

- •Ammo pack taping type (Packing style code : A)
 - · A crease is made every 25 pitches, and the tape with capacitors is packed zigzag into a case.
 - · When body of the capacitor is piled on other body under it.

The size of packing case and packing way



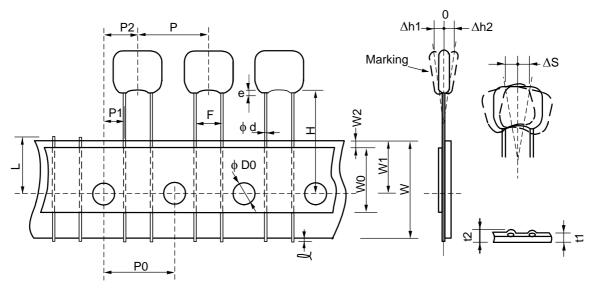
EKBCRPE01

7. Taping specification

7-1. Dimension of capacitors on tape

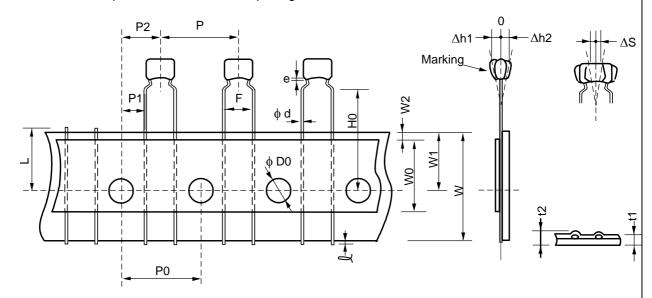
Straight taping type < Lead code : E1 >

Pitch of component 12.7mm / Lead spacing 5.0mm



Item	Code	Dimensions	Remarks
Pitch of component	Р	12.7+/-1.0	
Pitch of sprocket hole	P0	12.7+/-0.2	
Lead spacing	F	5.0+0.6/-0.2	
Length from hole center to component center	P2	6.35+/-1.3	Deviation of progress direction
Length from hole center to lead	P1	3.85+/-0.7	
Deviation along tape, left or right defect	ΔS	0+/-2.0	They include deviation by lead bend .
Carrier tape width	W	18.0+/-0.5	
Position of sprocket hole	W1	9.0+0/-0.5	Deviation of tape width direction
For straight lead type	Н	17.5+/-0.5	
Protrusion length	l	0.5 max.	
Diameter of sprocket hole	D0	4.0+/-0.1	
Lead diameter	φd	0.50+/-0.05	
Total tape thickness	t1	0.6+/-0.3	They include hold down tape thickness.
Total thickness of tape and lead wire	t2	1.5 max.	
Deviation across tape	∆h1	2.0 max. (Dimension code : U)	
	∆h2	1.0 max. (except as above)	
Portion to cut in case of defect	L	11.0+0/-1.0	
Hold down tape width	W0	9.5 min.	
Hold down tape position	W2	1.5+/-1.5	
Coating extension on lead	е	2.0 max. (Dimension code : U) 1.5 max. (except as above)	

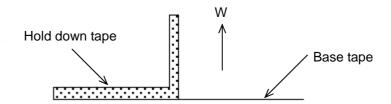
Inside crimp taping type < Lead code : M1 > Pitch of component 12.7mm / Lead spacing 5.0mm



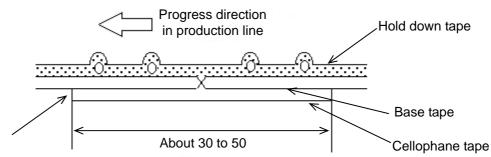
Item	Code	Dimensions	Remarks
Pitch of component	Р	12.7+/-1.0	
Pitch of sprocket hole	P0	12.7+/-0.2	
Lead spacing	F	5.0+0.6/-0.2	
Length from hole center to component center	P2	6.35+/-1.3	Deviation of progress direction
Length from hole center to lead	P1	3.85+/-0.7	
Deviation along tape, left or right defect	ΔS	0+/-2.0	They include deviation by lead bend .
Carrier tape width	W	18.0+/-0.5	
Position of sprocket hole	W1	9.0+0/-0.5	Deviation of tape width direction
Lead distance between reference and bottom plane	Н0	16.0+/-0.5	
Protrusion length	l	0.5 max.	
Diameter of sprocket hole	D0	4.0+/-0.1	
Lead diameter	φd	0.50+/-0.05	
Total tape thickness	t1	0.6+/-0.3	They include hold down tape thickness.
Total thickness of tape and lead wire	t2	1.5 max.	
Deviation across tape	∆h1	2.0 max. (Dimension code : W)	
	∆h2	1.0 max. (except as above)	
Portion to cut in case of defect	L	11.0+0/-1.0	
Hold down tape width	W0	9.5 min.	
Hold down tape position	W2	1.5+/-1.5	
Coating extension on lead	е	Up to the end of crimp	

7-2. Splicing way of tape

1) Adhesive force of tape is over 3N at test condition as below.



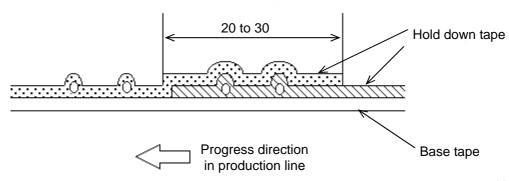
- 2) Splicing of tape
 - a) When base tape is spliced
 - •Base tape shall be spliced by cellophane tape. (Total tape thickness shall be less than 1.05mm.)



No lifting for the direction of progressing

Unit: mm

- b) When hold down tape is spliced
 - •Hold down tape shall be spliced with overlapping. (Total tape thickness shall be less than 1.05mm.)



- c) When both tape are spliced
 - •Base tape and hold down tape shall be spliced with splicing tape.

EU RoHS and Halogen Free

This products of the following crresponds to EU RoHS and Halogen Free

(1) RoHS

EU RoHs 2011/65/EC compliance

maximum concentration values tolerated by weight in homogeneous materials

- •1000 ppm maximum Lead
- •1000 ppm maximum Mercury
- •100 ppm maximum Cadmium
- •1000 ppm maximum Hexavalent chromium
- •1000 ppm maximum Polybrominated biphenyls (PBB)
- •1000 ppm maximum Polybrominated diphenyl ethers (PBDE)

(2) Halogen-Free

The International Electrochemical Commission's (IEC) Definition of Halogen-Free (IEC 61249-2-21) compliance

- •900 ppm maximum chlorine
- •900 ppm maximum bromine
- •1500 ppm maximum total chlorine and bromine

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Multilayer Ceramic Capacitors MLCC - Leaded category:

Click to view products by Murata manufacturer:

Other Similar products are found below:

010-007220-002REV A M39014/011207 M39014/011225 M39014/011267 M39014/011277 M39014/01-1281V M39014/01-1284V M39014/01-1293 M39014/011299 M39014/011327 M39014/011332 M39014/01-1333V M39014/011335 M39014/01-1339V M39014/011443 M39014/01-1443V M39014/01-1467 M39014/011489 M39014/01-1489V M39014/011514 M39014/011572 M39014/01-1581V M39014/01-1593 M39014/021218 M39014/021223 M39014/021225 M39014/02-1240V M39014/02-1241V M39014/02-1262V M39014/02-1300V M39014/021303 M39014/02-1315V M39014/02-1338 M39014/02-1354V M39014/021411 M39014/02-1418V M39014/05-2103 M39014/05-2127 M39014/220697 M39014/221162 M39014/230073 M39014/230319 M39014/230355 Q52-DK NCD332M1KVZ5UF C410C201J1G5TATR C410C221K1G5TATR C420C102J1G5TATR C430C104M1U5TATR SL217C103KAB