

Caution/Notice

Caution

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⚠ Caution

■ Storage and Operation Conditions

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. Also avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 degrees centigrade and 20 to 70%. Use capacitors within 6 months after delivery.

■ Rating

1. Operating Voltage

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the V_{p-p} value of the applied voltage or the V_{0-p} which contains DC bias within the rated voltage range.

When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

When DC-rated capacitors are to be used in input circuits from commercial power source (AC filter), be sure to use Safety Recognized Capacitors because various regulations on withstand voltage or impulse withstand established for all equipment should be taken into consideration.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement					

2. Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may have self-generated heat due to dielectric loss. In the case of "High Dielectric Constant Type Capacitors," applied voltage load should be such that self-generated heat is within 20 °C under the condition where the capacitor is subjected at an atmosphere temperature of 25 °C. Please contact us if self-generated heat occurs with "Temperature Compensating Type Capacitors".


When measuring, use a thermocouple of small thermal capacity -K of $\phi 0.1\text{mm}$ under conditions where the capacitor is not affected by radiant heat from other components or wind from surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.

3. Fail-Safe

Be sure to provide an appropriate fail-safe function on your product to prevent a second damage that may be caused by the abnormal function or the failure of our product.

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Caution

 Continued from the preceding page.

■ Soldering and Mounting

1. Vibration and Impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

2. Soldering

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

3. Bonding, Resin Molding and Coating

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of the capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case the amount of application, dryness/hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor may be damaged by the organic solvents and may result, worst case, in a short circuit.

The variation in thickness of adhesive or molding resin or coating may cause an outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

4. Treatment after Bonding, Resin Molding and Coating

When the outer coating is hot (over 100 degrees centigrade) after soldering, it becomes soft and fragile, so please be careful not to give it mechanical stress.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

Notice

■ Rating

1. Capacitance change of capacitor

In case of F/X7R/X7S/X7T/X8L/Y5V/Z5U char.

Capacitors have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor is left on for a long time. Moreover, capacitance might change greatly depending on the surrounding temperature or an applied voltage.

■ Soldering and Mounting

1. Cleaning (ultrasonic cleaning)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

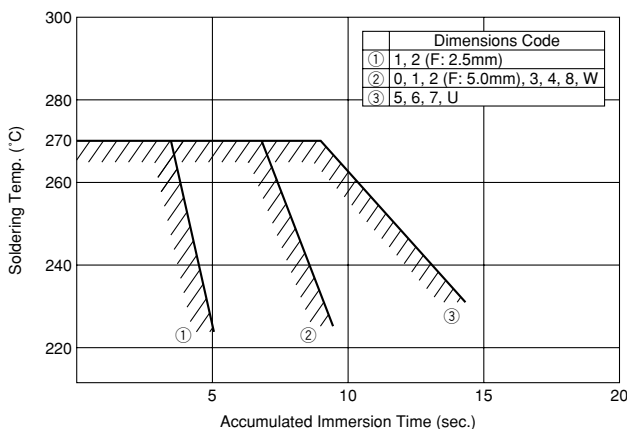
Rinsing time: 5 min. maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

2. Soldering and Mounting


















(1) Allowable Conditions for Soldering Temperature and Time



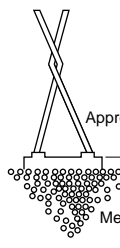
Perform soldering within tolerance range (shaded portion).

(2) Insertion of the Lead Wire

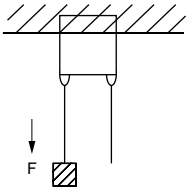
- When soldering, insert the lead wire into the PCB without mechanically stressing the lead wire.
- Insert the lead wire into the PCB with a distance appropriate to the lead space.

Dimensions Code	Temp. Char.	C0G	X7R	Z5U	Y5V
1					
2	Individual Specification Code A□□ B□□ Z□□	 Marked on both sides			
	Individual Specification Code Except A□□ B□□ Z□□				
3, 4, 8					
5, 6, 7					
Temperature Characteristics	Marked with code (C0G char.: A, X7R char.: C, Z5U char.: E, Y5V char.: F) A part is omitted (Please refer to the marking example.)				
Nominal Capacitance	Under 100pF: Actual value 100pF and over: marked with 3 figures				
Capacitance Tolerance	Marked with code				
Rated Voltage	Marked with code (DC25V: 2, DC50V: 5, DC100V: 1) A part is omitted (Please refer to the marking example.)				
Manufacturer's Identification	Marked with  A part is omitted (Please refer to the marking example.)				

No.	Item	Temperature Compensating Type	High Dielectric Constant Type	Test Method												
1	Operating Temperature Range	-55 to +125°C	Char. X7R: -55 to +125°C Char. Z5U: +10 to +85°C Char. Y5V: -30 to +85°C	-												
2	Rated Voltage	See previous pages		The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage or V ^{0-P} , whichever is larger, should be maintained within the rated voltage range.												
3	Appearance	No defects or abnormalities		Visual inspection												
4	Dimension and Marking	See previous pages		Visual inspection, Vernier Caliper												
5	Dielectric Strength	Between Terminals	No defects or abnormalities	The capacitors should not be damaged when DC voltages of 300%* of the rated voltage are applied between the terminals for 1 to 5 sec. (Charge/Discharge current ≤ 50mA) *250% for char. X7R, Z5U, Y5V												
		Body Insulation	No defects or abnormalities	The capacitor is placed in a container with metal balls of 1mm diameter so that each terminal, short-circuited, is kept approximately 2mm from the balls as shown in the figure, and 250% of the rated DC voltage is impressed for 1 to 5 sec. between capacitor terminals and metal balls. (Charge/Discharge current ≤ 50mA)												
6	Insulation Resistance	Between Terminals	C≤0.047μF: 10,000MΩ min. C>0.047μF: 500MΩ • μF min. C: Nominal capacitance	The insulation resistance should be measured with DC voltage not exceeding the rated voltage at normal temperature and humidity and within 2 min. of charge (Charge/Discharge current ≤ 50mA)												
7	Capacitance	Within the specified tolerance		The capacitance, Q/D.F. should be measured at the frequency and voltage shown in the table.												
8	Q/Dissipation Factor (D.F.)	30pF min.: Q≥1,000 30pF max.: Q≥400+20C C: Nominal capacitance (pF)	Char. X7R : 0.025 max. Char. Z5U): 0.05 max. Char. Y5V)	<table border="1"> <thead> <tr> <th>Capacitance Item</th> <th>1000pF and below</th> <th>more than 1000pF</th> </tr> </thead> <tbody> <tr> <td>Frequency</td> <td>1±0.1MHz</td> <td>1±0.1kHz</td> </tr> <tr> <td>Voltage</td> <td>AC0.5 to 5V (r.m.s.)</td> <td>AC1±0.2 (r.m.s.)</td> </tr> </tbody> </table>	Capacitance Item	1000pF and below	more than 1000pF	Frequency	1±0.1MHz	1±0.1kHz	Voltage	AC0.5 to 5V (r.m.s.)	AC1±0.2 (r.m.s.)			
Capacitance Item	1000pF and below	more than 1000pF														
Frequency	1±0.1MHz	1±0.1kHz														
Voltage	AC0.5 to 5V (r.m.s.)	AC1±0.2 (r.m.s.)														
9	Capacitance Temperature Characteristics	Capacitance Change	Within the specified tolerance (Table A on last column)	Within the specified tolerance (Table B on last column)												
		Temperature Coefficient	Within the specified tolerance (Table A on last column)													
		Capacitance Drift	Within ±0.2% or ±0.05pF (whichever is larger)													
				<p>The capacitance change should be measured after 1 min. at each specified temperature stage.</p> <p>(1) Temperature Compensating Type The temperature coefficient is determined using capacitance measured in step 3 as a reference. By cycling the temperature sequentially from step 1 through 5 (-55 to +125°C) the capacitance should be within the specified tolerance for the temperature coefficient and capacitance change as shown in Table A. The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in step 1, 3 and 5 by the cap. value in step 3.</p> <table border="1"> <thead> <tr> <th>Step</th> <th>Temperature (°C)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>25±2</td> </tr> <tr> <td>2</td> <td>-55±3</td> </tr> <tr> <td>3</td> <td>25±2</td> </tr> <tr> <td>4</td> <td>125±3</td> </tr> <tr> <td>5</td> <td>25±2</td> </tr> </tbody> </table> <p>(2) High Dielectric Constant Type The ranges of capacitance change compared with the 25°C value over the temperature ranges as shown in Table B should be within the specified ranges.</p>	Step	Temperature (°C)	1	25±2	2	-55±3	3	25±2	4	125±3	5	25±2
Step	Temperature (°C)															
1	25±2															
2	-55±3															
3	25±2															
4	125±3															
5	25±2															



Continued on the following page

No.	Item	Temperature Compensating Type	High Dielectric Constant Type	Test Method	
10	Terminal Strength	Tensile Strength	Termination not to be broken or loosened		<p>As in the figure, fix the capacitor body, apply the force gradually to each lead in the radial direction of the capacitor until reaching 10N and then keep the force applied for 10±1 sec.</p> 
		Bending Strength	Termination not to be broken or loosened		Each lead wire should be subjected to a force of 10N and then bent 90° at the point of egress in one direction. Each wire is then returned to the original position and bent 90° in the opposite direction at a rate of one bend per 2 to 3 sec.
11	Vibration Resistance	Appearance	No defects or abnormalities		The capacitor is soldered securely to a supporting terminal and a 10 to 55Hz vibration of 1.5mm peak amplitude is applied for 6 hrs. total, 2 hrs. in mutually perpendicular direction. Allow 1 min. to the frequency from 10Hz to 55Hz and the conversion.
		Capacitance	Within the specified tolerance		
		Q/D.F.	30pF min.: $Q \geq 1,000$ 30pF max.: $Q \geq 400+20C$ C: Nominal capacitance (pF)	Char. X7R : 0.025 max. Char. Z5U) Char. Y5V): 0.05 max.	
12	Solderability of Leads	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.		The terminal of a capacitor is dipped into a 25% e (JIS-K-8101) solution of rosin (JIS-K-5902) and then into molten solder for 2±0.5 sec. In both cases depth of dipping is up to about 1.5mm to 2mm from terminal body. Temp. of solder: 245±5°C Lead Free Solder (Sn-3.0Ag) 235±5°C H60A or H63A Eutectic Sold	
13	Resistance to Soldering Heat	Appearance	No defects or abnormalities		The lead wire is immersed in the melted solder 1 to 2mm from the main body at 270±5°C for 3±0.5 (L3.5 x W3.0 (mm) type) or 350±10°C for 3.5±0.5 (all other types). The specified items are measured after 24±2 hrs. (temperature compensating type) 48±4 hrs. (high dielectric type). • Initial measurement for high dielectric constant The capacitors are heat treated for 1 hr. at 150± allowed to set at room temperature for 48±4 hrs. given an initial measurement.
		Capacitance Change	Within ±2.5% or ±0.25pF (whichever is larger)	Char. X7R : Within ±7.5% Char. Z5U) Char. Y5V): Within ±20%	
		Dielectric Strength (Between Terminals)	No defects		
14	Temperature and Immersion Cycle	Appearance	No defects or abnormalities		First, repeat the following temperature/time cycle times : >> lowest operating temperature ±3°C/30±3 min. >> ordinary temperature/3 min. max. >> highest operating temperature ±3°C/30±3 min. >> ordinary temperature/3 min. max. Next, repeat twice the successive cycles of immersion each cycle consisting of immersion in a fresh water 65±5°C for 15 min. and immersion in a saturated aqueous solution of salt at 0±3°C for 15 min. The capacitor is then promptly washed in running water, dried with a drying cloth, and allowed to sit at room temperature for 24±2 hrs. (temperature compensating type) or 48±4 hrs. (high dielectric type). • Initial measurement for high dielectric constant The capacitors are heat treated for 1 hr. at 150±10°C, allowed to sit at room temperature for 48±4 hrs., and given an initial measurement.
		Capacitance Change	Within ±5% or ±0.5pF (whichever is larger)	Char. X7R: Within ±12.5% Char. Z5U) Char. Y5V): Within ±30%	
		Q/D.F.	30pF min.: $Q \geq 350$ 10pF to 30pF: $Q \geq 275+5C/2$ 10pF max.: $Q \geq 200+10C$ C: Nominal capacitance (pF)	Char. X7R: 0.05 max. Char. Z5U) Char. Y5V): 0.075 max.	
		Insulation Resistance	1,000MΩ or 50MΩ • μF min. (whichever is smaller)		
		Dielectric Strength (Between Terminals)	No defects or abnormalities		

Continued on the following page

No.	Item	Temperature Compensating Type	High Dielectric Constant Type	Test Method	
15	Humidity (Steady State)	Appearance	No defects or abnormalities		Set the capacitor for $500 \pm 24_0^8$ hrs. at $40 \pm 2^\circ\text{C}$ in 90 to 95% humidity. Remove and set for 24 ± 2 hrs. (temperature compensating type) and 48 ± 4 hrs. (dielectric constant type) at room temperature, then measure. • Initial measurement for high dielectric constant type The capacitors are heat treated for 1 hr. at $150 \pm 2^\circ\text{C}$, allowed to sit at room temperature for 48 ± 4 hrs. after given an initial measurement.
		Capacitance Change	Within $\pm 5\%$ or $\pm 0.5\text{pF}$ (whichever is larger)	Char. X7R : Within $\pm 12.5\%$ Char. Z5U _J : Within $\pm 30\%$ Char. Y5V _J : Within $\pm 30\%$	
		Q/D.F.	30pF min.: $Q \geq 350$ 10pF to 30pF: $Q \geq 275 + 5C/2$ 10pF max.: $Q \geq 200 + 10C$ C: Nominal capacitance (pF)	Char. X7R : 0.05 max. Char. Z5U _J : 0.075 max. Char. Y5V _J : 0.075 max.	
		Insulation Resistance	1,000M Ω or 50M Ω • μF min. (whichever is smaller)		
16	Humidity Load	Appearance	No defects or abnormalities		Apply the rated voltage for $500 \pm 24_0^8$ hrs. at $40 \pm 2^\circ\text{C}$ in 90 to 95% humidity. Remove and set for 24 ± 2 hrs. (temperature compensating type) and 48 ± 4 hrs. (dielectric constant type) at room temperature, then measure. (Charge/Discharge current $\leq 50\text{mA}$)
		Capacitance Change	Within $\pm 7.5\%$ or $\pm 0.75\text{pF}$ (whichever is larger)	Char. X7R : Within $\pm 12.5\%$ Char. Z5U _J : Within $\pm 30\%$ Char. Y5V _J : Within $\pm 30\%$	
		Q/D.F.	30pF min.: $Q \geq 200$ 30pF max.: $Q \geq 100 + 10C/3$ C: Nominal capacitance (pF)	Char. X7R : 0.05 max. Char. Z5U _J : 0.075 max. Char. Y5V _J : 0.075 max.	
		Insulation Resistance	500M Ω or 25M Ω • μF min. (whichever is smaller)		
17	High Temperature Load	Appearance	No defects or abnormalities		Apply 200% of the rated voltage for $1000 \pm 48_0^8$ hrs. at the maximum operating temperature. Remove and set for 24 ± 2 hrs. (temperature compensating type) and 48 ± 4 hrs. (high dielectric constant type) at room temperature, then measure. (Charge/Discharge current $\leq 50\text{mA}$) • Initial measurement for high dielectric constant type A voltage treatment should be given to the capacitor which a DC voltage of 200% of the rated voltage is applied for 1 hr. at the maximum operating temperature $\pm 3^\circ\text{C}$. Then set for 48 ± 4 hrs. at room temperature, then conduct initial measurement.
		Capacitance Change	Within $\pm 3\%$ or $\pm 0.3\text{pF}$ (whichever is larger)	Char. X7R : Within $\pm 12.5\%$ Char. Z5U _J : Within $\pm 30\%$ Char. Y5V _J : Within $\pm 30\%$	
		Q/D.F.	30pF min.: $Q \geq 350$ 10pF to 30pF: $Q \geq 275 + 5C/2$ 10pF max.: $Q \geq 200 + 10C$ C: Nominal capacitance (pF)	Char. X7R : 0.04 max. Char. Z5U _J : 0.075 max. Char. Y5V _J : 0.075 max.	
		Insulation Resistance	1,000M Ω or 50M Ω • μF min. (whichever is smaller)		
18	Solvent Resistance	Appearance	No defects or abnormalities		The capacitor should be fully immersed, unagitated in reagent at 20 to 25°C for 30 ± 5 sec. and then removed gently. Marking on the surface of the capacitor should immediately be visually examined. Reagent: • Isopropyl alcohol
		Marking	Legible		

Table A

Char.	Nominal Values (ppm/ $^\circ\text{C}$) *1	Capacitance Change from 25°C (%)					
		-55°C		-30°C		-10°C	
		Max.	Min.	Max.	Min.	Max.	Min.
C0G	0 ± 30	0.58	-0.24	0.40	-0.17	0.25	-0.11

*1: Nominal values denote the temperature coefficient within a range of 25 to 125°C

Table B

Char.	Temp. Range	Reference Temp.	Cap. Change
X7R	-55 to $+125^\circ\text{C}$	25°C	Within $\pm 15\%$
Z5U	$+10$ to $+85^\circ\text{C}$		Within $\pm 20\%$
Y5V	-30 to $+85^\circ\text{C}$		Within $\pm 20\%$

■ Packaging

Two types of packaging for monolithic ceramic capacitors are available.

1. Bulk Packaging

Minimum Quantity

Dimensions Code	Dimensions (L×W)	Minimum Quantity (pcs./Bag)*
0	3.6×3.5mm or 4.0×3.5mm or 5.0×3.5mm (Depends on Part Number)	500
1	4.0×3.5mm or 4.5×3.5mm or 5.0×3.5mm (Depends on Part Number)	
2	5.0×3.5mm or 5.5×4.0mm or 5.7×4.5mm (Depends on Part Number)	
3	5.0×4.5mm or 5.5×5.0mm or 6.0×5.5mm (Depends on Part Number)	
4	7.5×5.5mm	
5	7.5×7.5mm or 7.5×8.0mm (Depends on Part Number)	
6	10.0×10.0mm	
8	7.5×5.5mm	
7	12.5×12.5mm	100
U	7.7×12.5mm or 7.7×13.0mm (Depends on Part Number)	200
W	5.5×7.5mm or 6.0×8.0mm (Depends on Part Number)	500

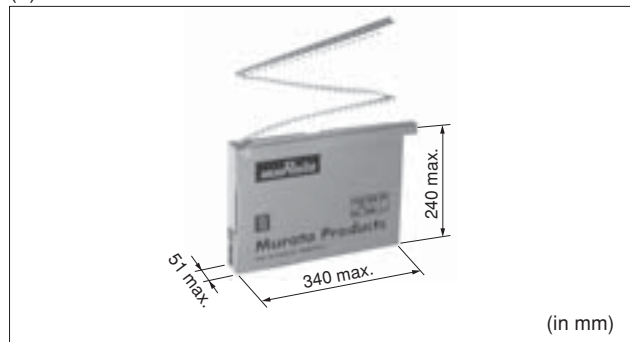
Please order with an integral multiple of the minimum quantity above.

* Minimum Quantity may change depends on part number.

Please check our website 'Product details'.

2. Tape Carrier Packaging

(1) Dimensions of Ammo Pack



(2) Minimum Quantity

Dimensions Code	Dimensions (L×W)	Minimum Quantity (pcs./Ammo Pack)*
0	3.6×3.5mm or 4.0×3.5mm or 5.0×3.5mm (Depends on Part Number)	2000
1	4.0×3.5mm or 4.5×3.5mm or 5.0×3.5mm (Depends on Part Number)	
2	5.0×3.5mm or 5.5×4.0mm or 5.7×4.5mm (Depends on Part Number)	
3	5.0×4.5mm or 5.5×5.0mm or 6.0×5.5mm (Depends on Part Number)	
4	7.5×5.5mm	2000
5	7.5×7.5mm or 7.5×8.0mm (Depends on Part Number)	
6	10.0×10.0mm	1500
8	7.5×5.5mm	
U	7.7×12.5mm or 7.7×13.0mm (Depends on Part Number)	1000
W	5.5×7.5mm or 6.0×8.0mm (Depends on Part Number)	1500

Please order with an integral multiple of the minimum quantity above.

* Minimum Quantity may change depends on part number.

Please check our website 'Product details'.

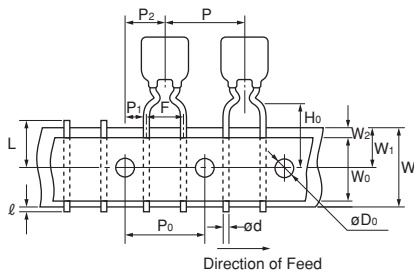
"Minimum Quantity" means the numbers of units of each delivery or order. The quantity should be an integral multiple of the "minimum quantity".
(Please note that the actual delivery quantity in a package may change sometimes.)

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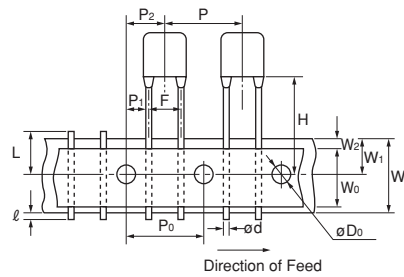
Taping Dimensions

Inside Crimp Taping



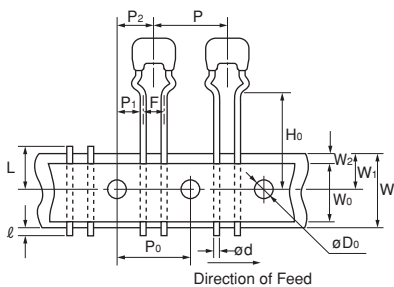
Dimensions and Lead Style Code
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1M1
2M1
2M2
3M1
3M2
4M1
4M2
8M1
8M2
WM1

Straight Taping



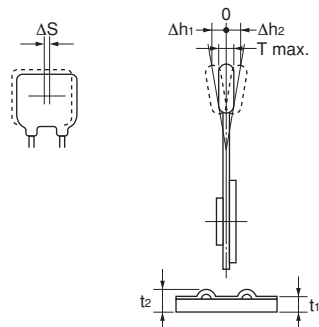
Dimensions and Lead Style Code
1DB
2DB
3DB
5E1
5E2
6E1
6E2
UE1

Outside Crimp Taping



Dimensions and Lead Style Code
0S1
1S1
2S1
2S2
3S1
3S2

Item	Code	Dimensions (mm)
Pitch of Component	P	12.7±1.0
Pitch of Sprocket Hole	P ₀	12.7±0.2
Lead Spacing	F	2.5 ^{+0.4} _{-0.2} (DB) (S1) (S2)
		5.0 ^{+0.6} _{-0.2}
Length from Hole Center to Component Center	P ₂	6.35±1.3
Length from Hole Center to Lead	P ₁	3.85±0.7
		5.1±0.7 (DB) (S1) (S2)
		254±1.5 Total length of components pitch X 20
Body Dimension	Depends on Part Number	
Deviation Along Tape, Left or Right Defect	ΔS	±2.0
Carrier Tape Width	W	18.0±0.5
Position of Sprocket Hole	W ₁	9.0 ⁺⁰ _{-0.5}
Lead Distance between Reference and Bottom Plane	H ₀	16.0±0.5 (M1) (S1)
		20.0±0.5 (M2) (S2)
For Straight Lead Type	H	20±0.5 (E2), 17.5±0.5 (E1), 16±0.5 (DB)
Diameter of Sprocket Hole	D ₀	4.0±0.1
Lead Diameter	d	0.5±0.05
Total Tape Thickness	t ₁	0.6±0.3
Total Thickness of Tape and Lead Wire	t ₂	1.5 max.
Body Thickness	T	Depends on Part Number
Deviation Across Tape	Δh ₁ Δh ₂	2.0 max. Dimensions Code: W, U
		1.5 max. RHD Series
		1.0 max. except as above
Portion to Cut in Case of Defect	L	11.0 ⁺⁰ _{-1.0}
Protrusion Length	ℓ	0.5 max.
Hold Down Tape Width	W ₀	9.5 min.
Hold Down Tape Position	W ₂	1.5±1.5
Coating Extension	Depends on Dimensions	



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