20V P-Channel Enhancement Mode MOSFET

Description

The NP2301BVR uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications.

General Features

- V_{DS} =-20V，I_D=-2.4A
  $R_{DS(ON)}$(Typ.)= 86mΩ @V_{GS} =-4.5V
  $R_{DS(ON)}$(Typ.)= 110mΩ @V_{GS}=-2.5V
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

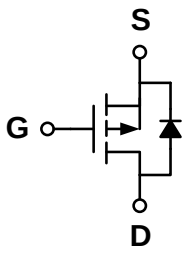
Application

- PWM applications
- Load switch

Package

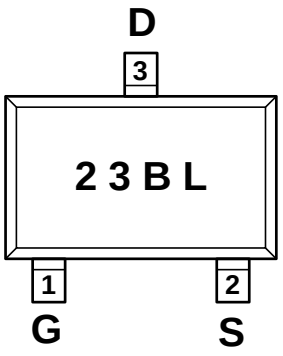
- SOT-23

Schematic diagram



Marking and pin assignment

SOT-23
(TOP VIEW)



Ordering Information

Part Number	Storage Temperature	Package	Devices Per Reel
NP2301BVR-G	-55°C to +150°C	SOT-23	3000

Absolute Maximum Ratings (TA=25℃ unless otherwise noted)

parameter	symbol	limit	unit
Drain-source voltage	V_{DS}	-20	V
Gate-source voltage	V_{GS}	±12	V
Drain current-continuous[a]@Tj=125℃	I_D	-2.4	A
-pulse *d*[b]	I_{DM}	-8	A
Drain-source Diode forward current	Is	-1.25	A
Maximum power dissipation	P_D	1	W
Operating junction Temperature range	Tj	-55—150	℃

Electrical Characteristics (TA=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF Characteristics						
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0V, I_D=-250μA	-20	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=-20V, V_{GS}=0V	-	-	-1	μA
Gate-body leakage	I_{GSS}	V_{DS}=0V, V_{GS}=±12V	-	-	±100	nA
ON Characteristics						
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=-250μA	-0.5	-0.7	-1.2	V
Drain-source on-state resistance	$R_{DS(ON)}$	V_{GS}=-4.5V, I_D=-2A	-	86	120	mΩ
		V_{GS}=-2.5V, I_D=-1A	-	110	150	
Forward transconductance	gfs	V_{GS}=-5V, I_D=-2A	-	5	-	S
Dynamic Characteristics						
Input capacitance	C_{ISS}	V_{DS}=-10V ,V_{GS}=0V f=1.0MHz	-	325	-	pF
Output capacitance	C_{OSS}		-	63	-	
Reverse transfer capacitance	C_{RSS}		-	37	-	
Switching Characteristics						
Turn-on delay time	$t_{D(ON)}$	V_{DD}=-10V I_D=-1A V_{GEN}=-4.5V R_L=10ohm R_{GEN}=-60ohm	-	11	-	ns
Rise time	tr		-	5,5	-	
Turn-off delay time	$t_{D(OFF)}$		-	22	-	
Fall time	tf		-	8	-	
Total gate charge	Qg	V_{DS}=-10V,I_D=-1A V_{GS}=-4.5V	-	3.2	-	nC
Gate-source charge	Qgs		-	0.6	-	
Gate-drain charge	Qgd		-	0.9	-	
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode forward voltage	V_{SD}	V_{GS}=0V,Is=-1.25A	-	-0.81	-1.2	V

Notes:

a. surface mounted on FR4 board,t≤10sec

b. pulse test: pulse width≤300μs,duty≤2%

c. guaranteed by design, not subject to production testing

Thermal Characteristics

Thermal Resistance junction-to ambient	Rth JA	100	℃/W

Typical Performance Characteristics

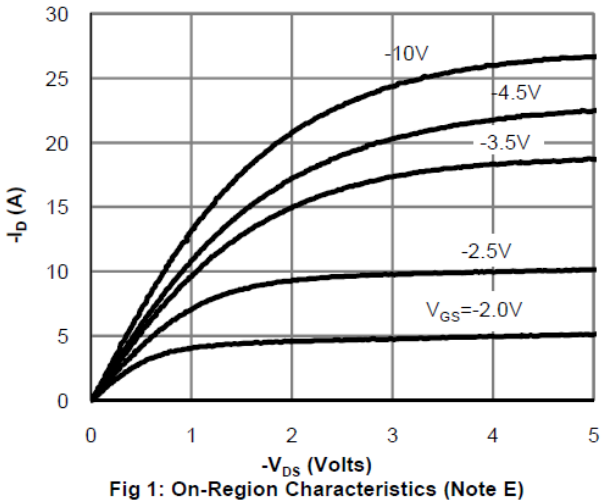


Fig 1: On-Region Characteristics (Note E)

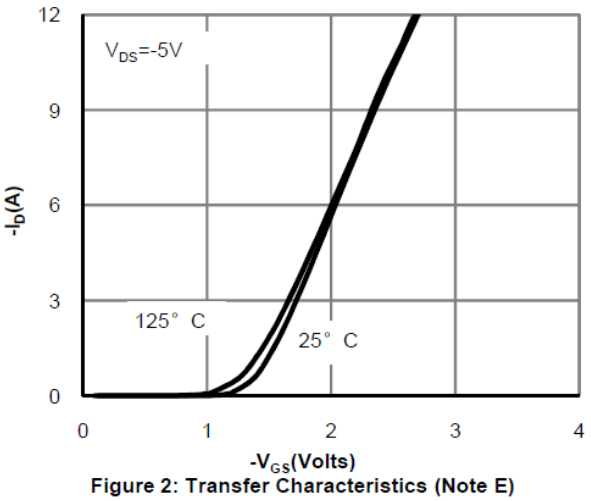


Figure 2: Transfer Characteristics (Note E)

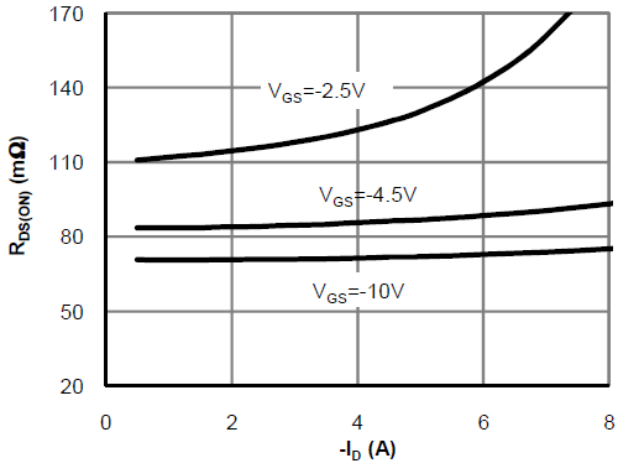


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

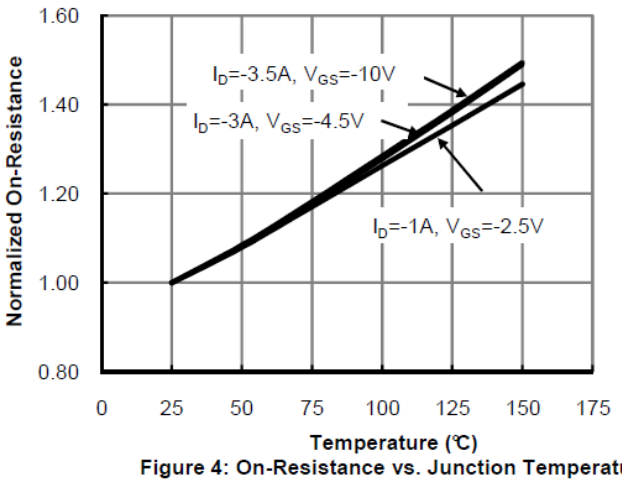


Figure 4: On-Resistance vs. Junction Temperature (Note E)

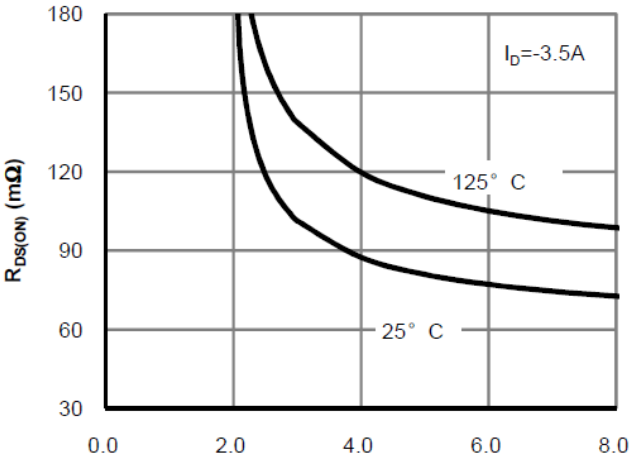


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

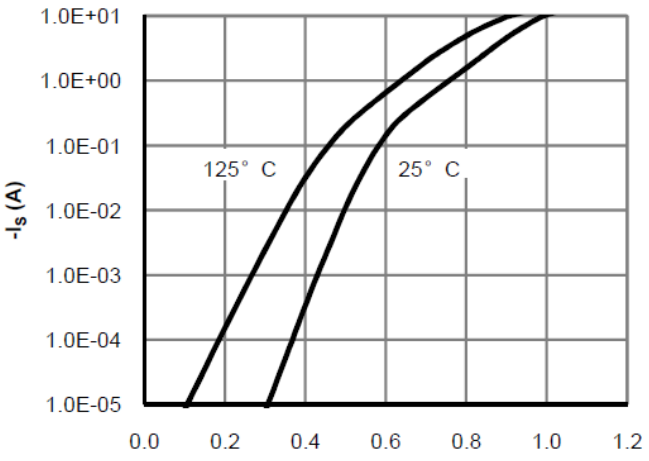


Figure 6: Body-Diode Characteristics (Note E)

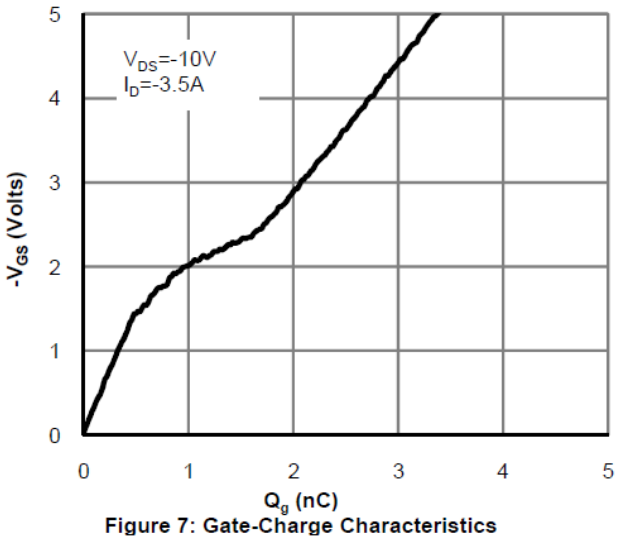


Figure 7: Gate-Charge Characteristics

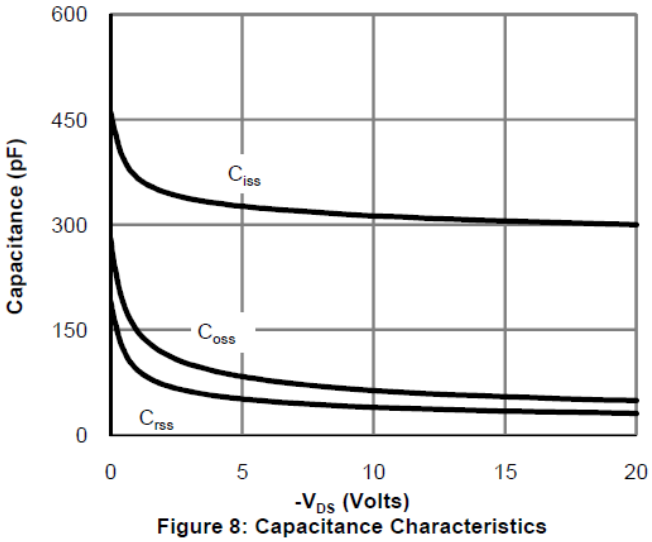


Figure 8: Capacitance Characteristics

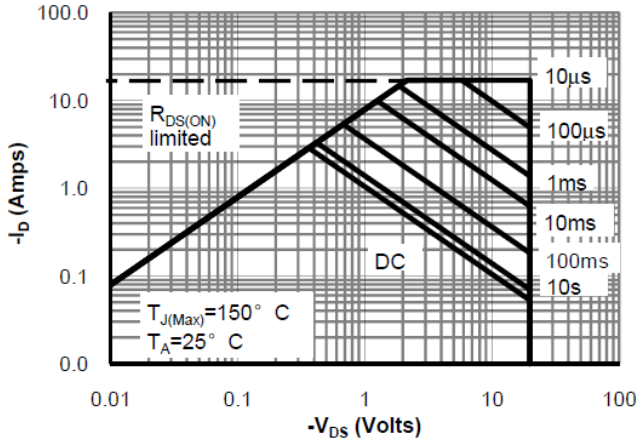


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

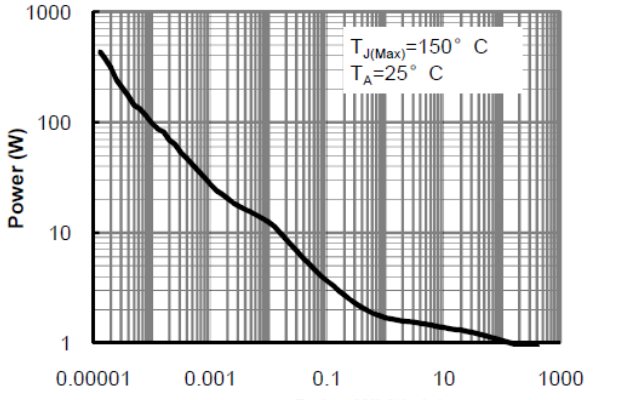


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

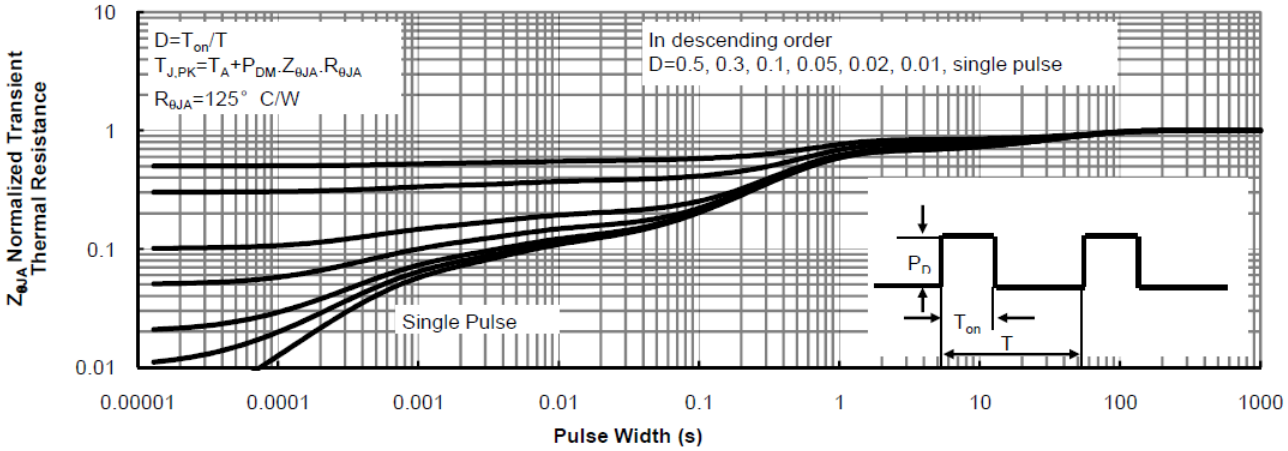
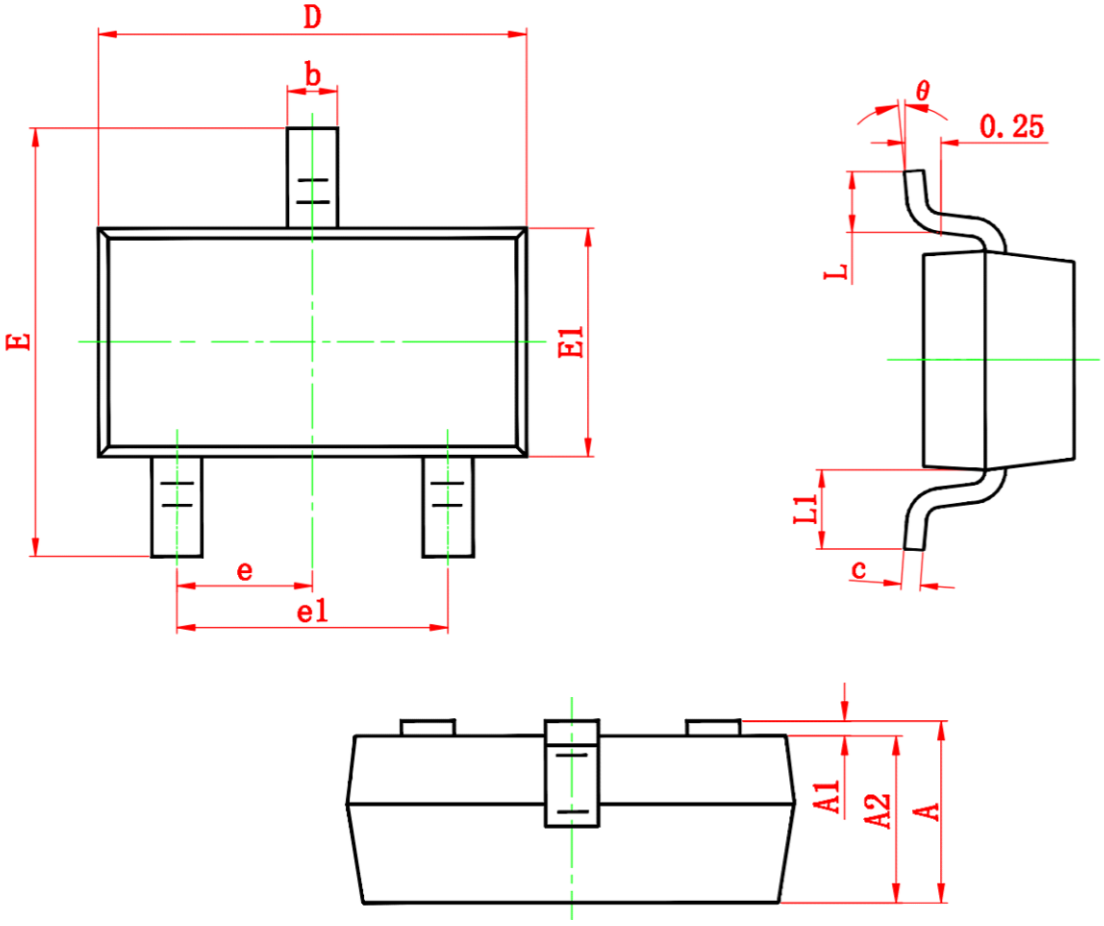


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

Package Information

- **SOT-23**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	2.250	2.550	0.089	0.100
E1	1.200	1.400	0.047	0.055
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.300	0.500	0.012	0.020
L1	0.550 REF.		0.022 REF.	
θ	0°	8°	0°	8°

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