100V N-Channel Enhancement Mode MOSFET

Description

The NP2N10VR uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and high density cell Design for ultra low on-resistance. This device is suitable for use as a load switch or in PWM applications.

General Features

- V_{DS} =100V，I_D =2A
  $R_{DS(ON)}$(Typ.)=200mΩ @V_{GS}=10V
  $R_{DS(ON)}$(Typ.)=220mΩ @V_{GS}=4.5V
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

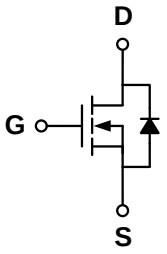
Application

- PWM applications
- Load switch

Package

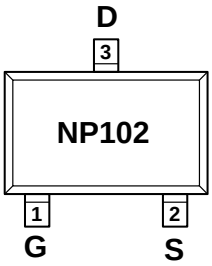
- SOT23

Schematic diagram



Marking and pin assignment

SOT23
(TOP VIEW)



Ordering Information

Part Number	Storage Temperature	Package	Devices Per Reel
NP2N10VR-G	-55°C to +150°C	SOT-23-3L	3000

Absolute Maximum Ratings (TA=25℃ unless otherwise noted)

parameter		symbol	limit	unit
Drain-source voltage		V_{DS}	100	V
Gate-source voltage		V_{GS}	±20	V
Continuous Drain Current (TJ = 150 °C)	T_C=25°C	I_D	2	A
	T_C=70°C		1.7	
	T_A=25°C		1.6[b,c]	
	T_A=70°C		1.3[b,c]	
Continuous Source-Drain Diode Current	T_C=25°C	I_S	2.1	
	T_A=25°C		1[b,c]	
Pulsed Drain Current (t = 300 µs)		I_{DM}	5	

Maximum power dissipation	T_C=25°C	P_D	2.5	W
	T_C=70°C		1.6	
	T_A=25°C		1.25[b,c]	
	T_A=70°C		0.8[b,c]	
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55—150	℃

Thermal Characteristics

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient[b, d]	t ≤ 5 s	$R_{\theta JA}$	100	130	℃/W
Maximum Junction-to-Foot (Drain)	Steady State	$R_{\theta JF}$	60	75	

Notes:

a:T_C = 25 °C. b:Surface mounted on 1" x 1" FR4 board.

c:t = 5 s. d: Maximum under steady state conditions is 175 °C/W.

Electrical Characteristics (TA=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF Characteristics						
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0V, I_D=250μA	100	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=100V, V_{GS}=0V	-	-	1	μA
Gate-body leakage	I_{GSS}	V_{DS}=0V, V_{GS}=±20V	-	-	±100	nA
ON Characteristics						
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250μA	1.2	1.9	2.5	V
Drain-source on-state resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=2A	-	200	220	mΩ
		V_{GS}=4.5V, I_D=2A		220	260	
Forward transconductance	gfs	V_{DS}=5V, I_D=1A	1	-	-	S
Dynamic Characteristics						
Input capacitance	C_{ISS}	V_{DS}=50V ,V_{GS}=0V f=1.0MHz	-	423	-	pF
Output capacitance	C_{OSS}		-	17	-	
Reverse transfer capacitance	C_{RSS}		-	14	-	
Switching Characteristics						
Turn-on delay time	$t_{D(ON)}$	V_{DD}=50V R_L=39 ohm V_{GS}=10V R_G=1ohm	-	6	-	ns
Rise time	tr		-	10	-	
Turn-off delay time	$t_{D(OFF)}$		-	10	-	
Fall time	tf		-	6	-	
Total gate charge	Qg	V_{DS}=50V I_D=2A V_{GS}=10V	-	6.73	-	nC
Gate-source charge	Qgs		-	2.06	-	
Gate-drain charge	Qgd		-	2.69	-	
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode forward voltage	V_{SD}	V_{GS}=0V,Is=2A	-	0.76	1.16	V

Typical Performance Characteristics

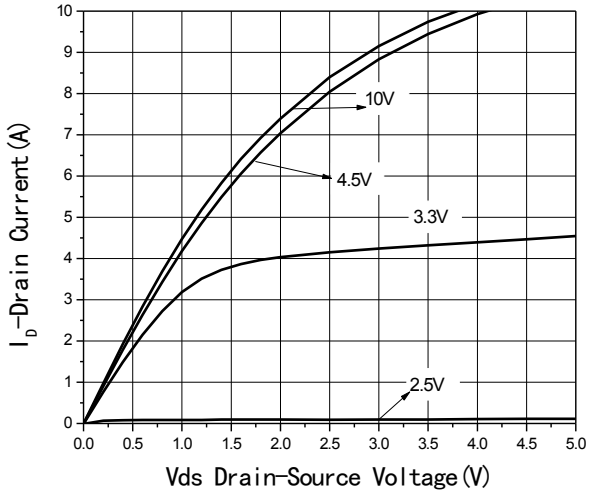


Fig1 Output Characteristics

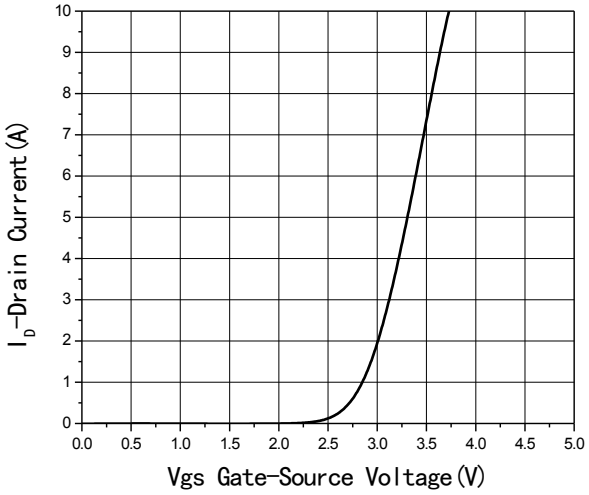


Fig2 Transfer Characteristics

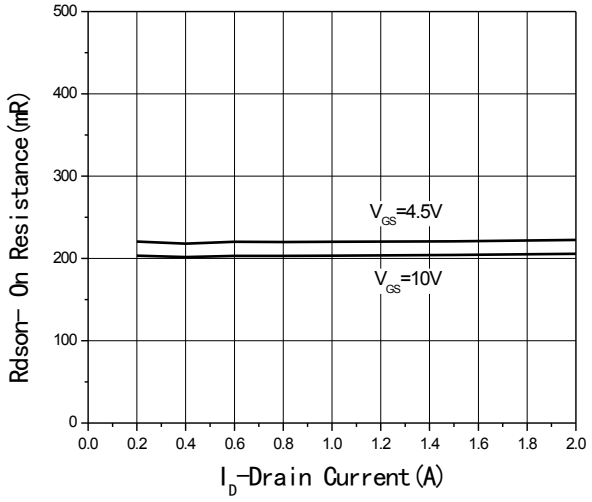


Fig3 Rdson-Drain current

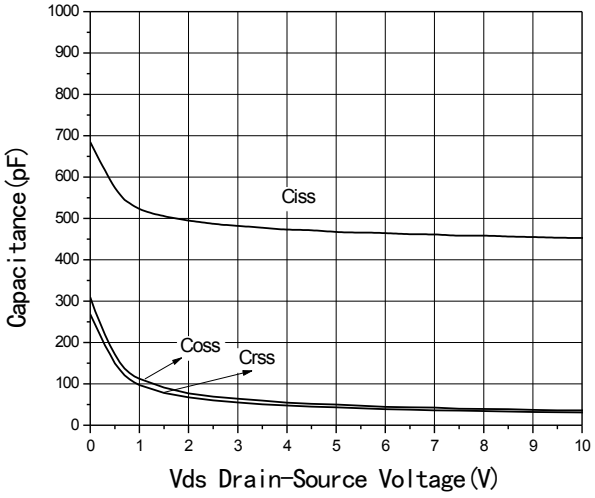


Fig4 Capacitance vs Vds

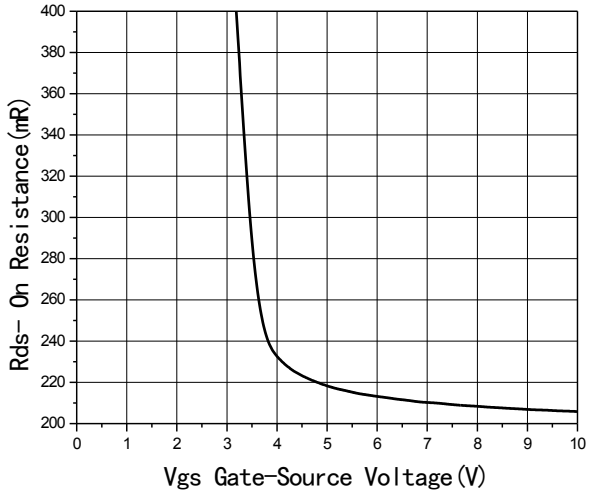


Fig5 Rdson-Gate Drain voltage

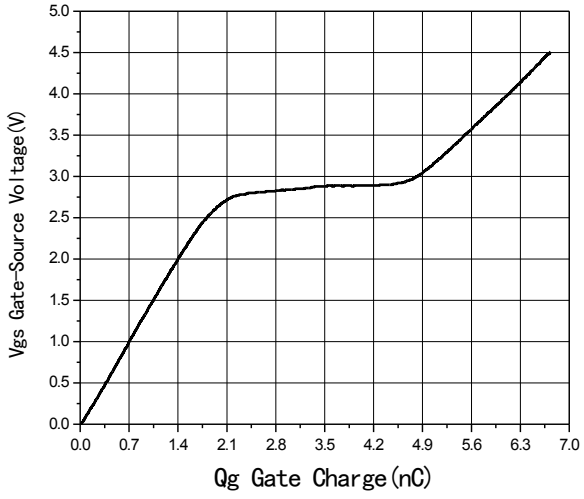


Fig6 Gate Charge

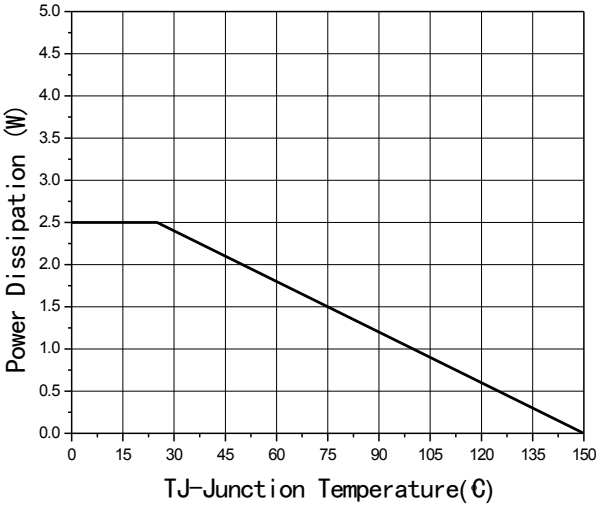


Fig7 Power De-rating

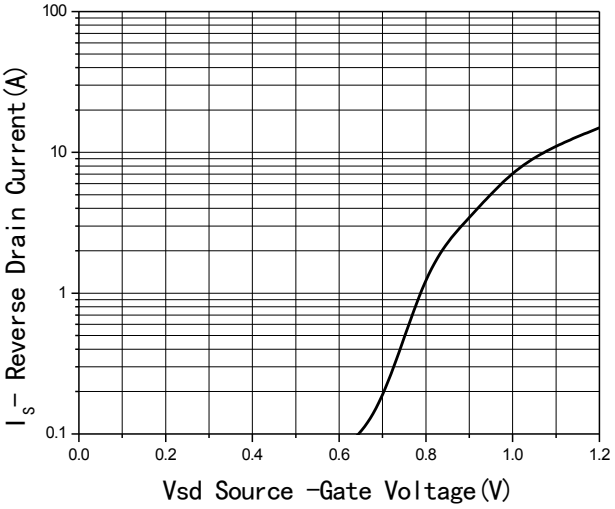
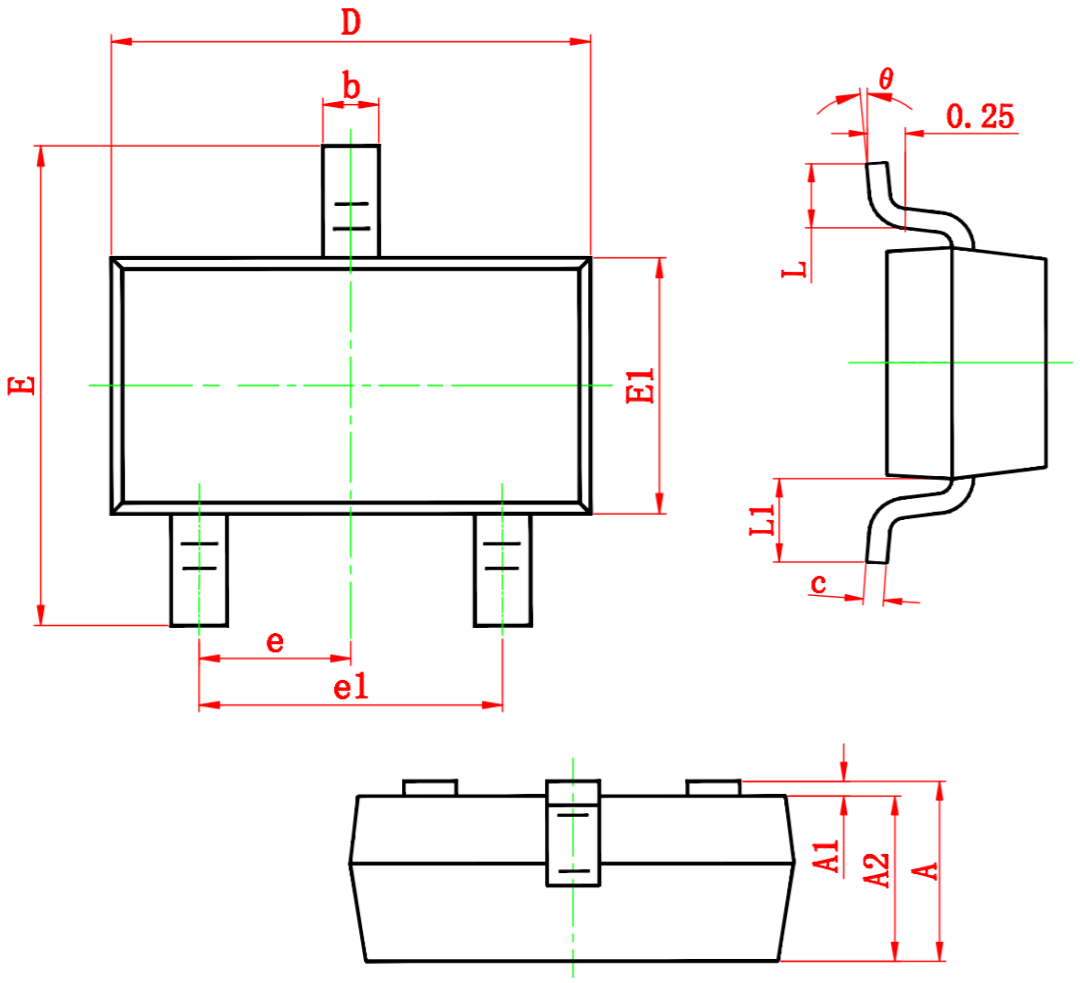


Fig8 Source-Drain Diode Forward

Package Information

- **SOT-23**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	2.250	2.550	0.089	0.100
E1	1.200	1.400	0.047	0.055
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.300	0.500	0.012	0.020
L1	0.550 REF.		0.022 REF.	
θ	0°	8°	0°	8°

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