

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE0202M uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

V_{DS} = 200V,I_D =2A

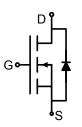
 $R_{DS(ON)} < 580 m\Omega \text{ @ } V_{GS} = 10 V \quad \text{(Typ:520m}\Omega\text{)}$

 $R_{DS(ON)} < 600 \text{m}\Omega @ V_{GS} = 10V (Typ:540 \text{m}\Omega)$

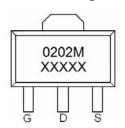
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



SOT-89 -3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
0202M	NCE0202M	SOT-89-3L	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	200	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	2	Α
Drain Current-Pulsed (Note 1)	I _{DM}	8	Α
Maximum Power Dissipation	P _D	1.5	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	83.3	°C/W

Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	200	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =200V,V _{GS} =0V	-	-	1	μA



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NCE0202M

Gate-Body Leakage Current	I_{GSS}	V_{GS} =±20 V , V_{DS} =0 V	-	-	±100	nA
On Characteristics (Note 3)				1		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	1.2	1.8	2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =2A	-	520	580	mΩ
Dialii-Source Oil-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =2A	-	540	600	mΩ
Forward Transconductance	G FS	g _{FS} V _{DS} =15V,I _D =2A		8	-	S
Dynamic Characteristics (Note4)	<u>.</u>					
Input Capacitance	C _{lss}	\/ -25\/\/ -0\/	-	580	-	PF
Output Capacitance	V_{DS} =25V, V_{GS} =0V,		-	90	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	3	-	PF
Switching Characteristics (Note 4)	<u> </u>		•	•		
Turn-on Delay Time	t _{d(on)}		-	10	-	nS
Turn-on Rise Time	t _r	V_{DD} =100V, R_L =15 Ω	-	12	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =2.5 Ω	-	15	-	nS
Turn-Off Fall Time	t _f		-	15	-	nS
Total Gate Charge	Qg	\/ 400\/ L 0A	-	12		nC
Gate-Source Charge	Q_{gs}	V _{DS} =100V,I _D =2A,	-	2.5	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	3.8	-	nC
Drain-Source Diode Characteristics			•	•		
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =2A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	2	Α

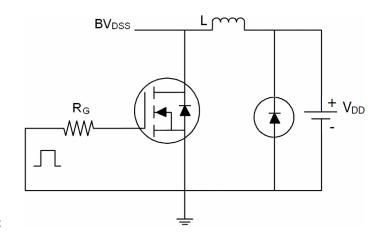
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production

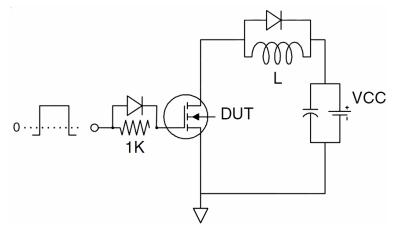


Test Circuit

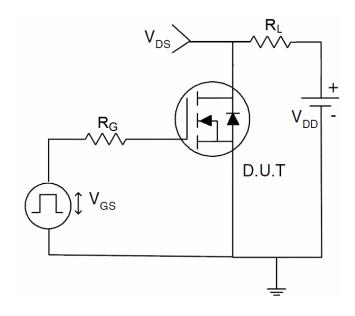
1) E_{AS} test circuit



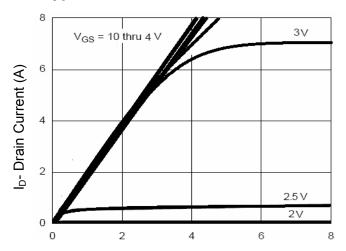
2) Gate charge test circuit



3) Switch Time Test Circuit

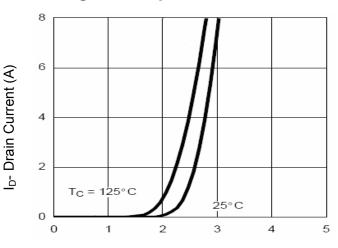


Typical Electrical and Thermal Characteristics (Curves)



Vds Drain-Source Voltage (V)





Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics

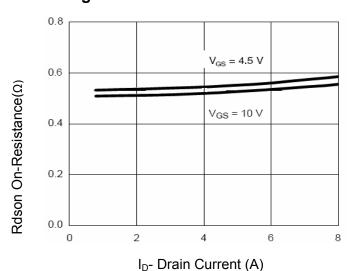
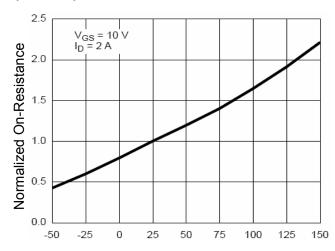
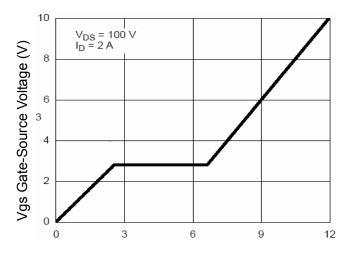


Figure 3 Rdson- Drain Current



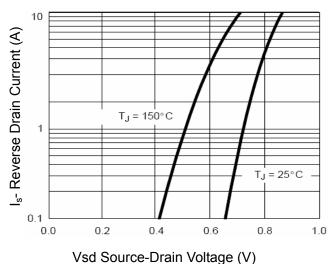
T_J-Junction Temperature(°C)

Figure 4 Rdson-JunctionTemperature



Qg Gate Charge (nC)

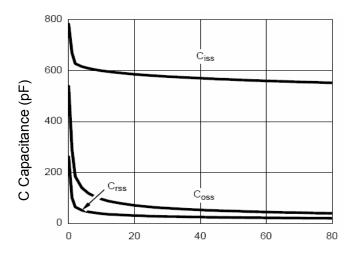
Figure 5 Gate Charge



vsu Source-Drain voltage (v)

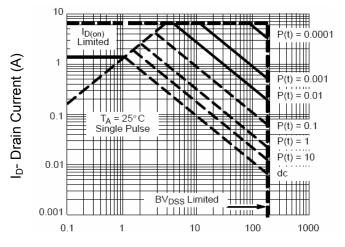
Figure 6 Source- Drain Diode Forward





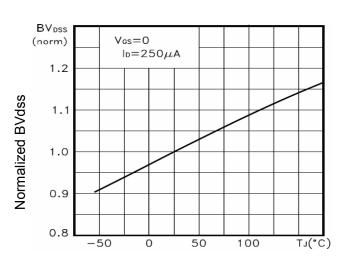
Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds



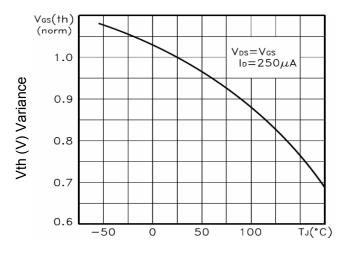
Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area



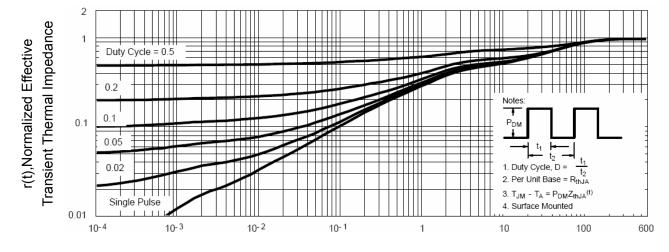
T_J-Junction Temperature(°C)

Figure 9 BV_{DSS} vs Junction Temperature



T_J-Junction Temperature(°C)

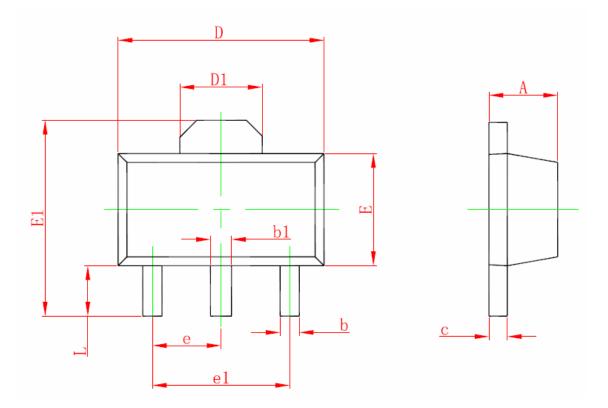
Figure 10 V_{GS(th)} vs Junction Temperature



Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

SOT-89-3L Package Information



Cymbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.020	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550	1.550 REF.		REF.	
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
е	1.500 TYP.		0.060 TYP.		
e1	3.000 TYP.		0.118 TYP.		
L	0.900	1.200	0.035	0.047	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ± 0.10 mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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DMN1017UCP3-7 EFC2J004NUZTDG P85W28HP2F-7071 DMN1053UCP4-7 NTE2384 DMC2700UDMQ-7 DMN2080UCB4-7
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STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 IPS60R360PFD7SAKMA1
DMN2990UFB-7B SSM3K35CT,L3F IPLK60R1K0PFD7ATMA1 2N7002W-G MCAC30N06Y-TP IPWS65R035CFD7AXKSA1
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