NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE2030 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

V_{DS} =20V,I_D =30A

 $R_{DS(ON)}$ <12m Ω @ V_{GS} =10V (Typ:10.5m Ω)

 $R_{DS(ON)}$ <13m Ω @ V_{GS} =4.5V (Typ:11m Ω)

 $R_{DS(ON)}$ <18m Ω @ V_{GS} =2.5V (Typ:14m Ω)

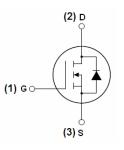
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Power switching application
- Load switching
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram



Marking and pin assignment



TO-220-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE2030	NCE2030	TO-220-3L	-	-	-

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _G s	±12	V
Drain Current-Continuous	I _D	30	А
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	21	Α
Pulsed Drain Current	I _{DM}	100	Α
Maximum Power Dissipation	P _D	40	W
Single pulse avalanche energy (Note 5)	E _{AS}	150	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance,Junction-to-Case ^(Note 2)	$R_{ heta JC}$	3.8	°C/W
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Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<u>.</u>					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						•
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.5	0.7	1.2	V
		V _{GS} =10V, I _D =20A	-	10.5	12	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A	-	11	13	mΩ
		V _{GS} =2.5V, I _D =20A	-	14	18	mΩ
Forward Transconductance	g Fs	V _{DS} =5V,I _D =20A	10	-	-	S
Dynamic Characteristics (Note4)						•
Input Capacitance	C _{lss}	\/ -40\/\/ -0\/		1544		PF
Output Capacitance	Coss	V_{DS} =10V, V_{GS} =0V, F=1.0MHz		210.1		PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIΠZ		201.4		PF
Switching Characteristics (Note 4)	<u>.</u>					
Turn-on Delay Time	$t_{d(on)}$		-	4.5	-	nS
Turn-on Rise Time	t _r	V _{GS} =10V,V _{DS} =10V	-	9.2	-	nS
Turn-Off Delay Time	$t_{\sf d(off)}$	$R_L=0.5\Omega$, $R_{GEN}=3\Omega$	-	18.7	-	nS
Turn-Off Fall Time	t _f		-	3.3	-	nS
Total Gate Charge	Qg			23.5		nC
Gate-Source Charge	Q _{gs}	V _{GS} =4.5V,V _{DS} =10V,I _D =20A		2.8		nC
Gate-Drain Charge	Q_{gd}			5.75		nC
Drain-Source Diode Characteristics	<u>.</u>					
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is	-	-	-	30	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = 20A	-	18	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/μs ^(Note3)	-	9.5	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+				y LS+LD)

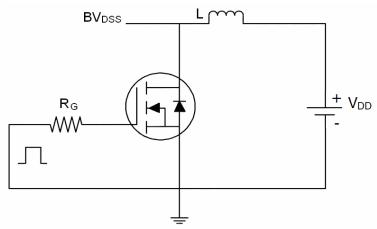
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}$ C,V_{DD}=10V,V_G=10V,L=0.5mH,Rg=25 Ω

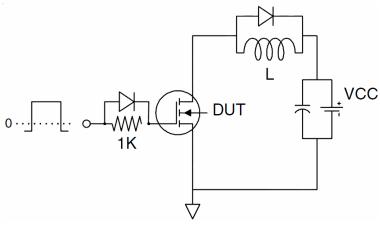


Test circuit

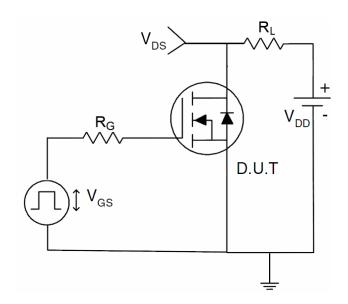
1) E_{AS} test Circuits



2) Gate charge test Circuit:



3) Switch Time Test Circuit:





Typical Electrical and Thermal Characteristics (Curves)

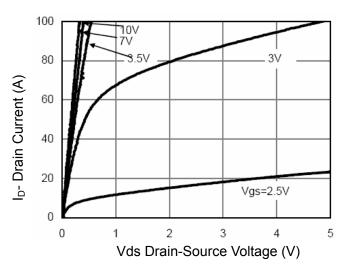


Figure 1 Output Characteristics

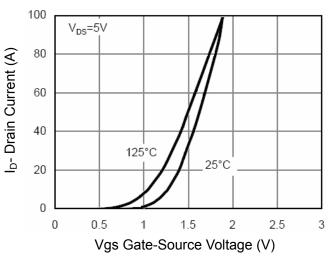


Figure 2 Transfer Characteristics

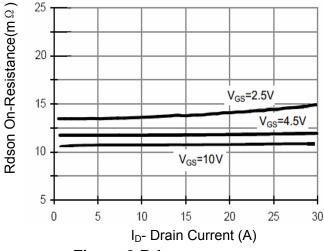


Figure 3 Rdson- Drain Current

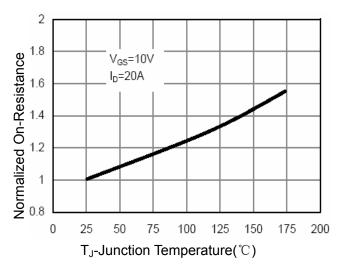


Figure 4 Rdson-Junction Temperature

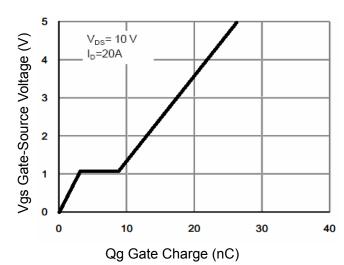


Figure 5 Gate Charge

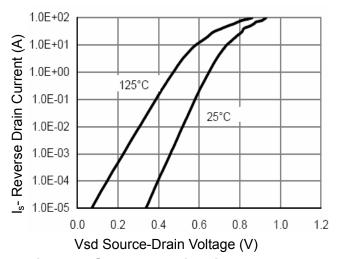


Figure 6 Source- Drain Diode Forward



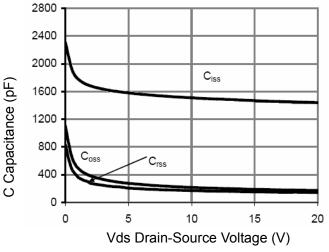


Figure 7 Capacitance vs Vds

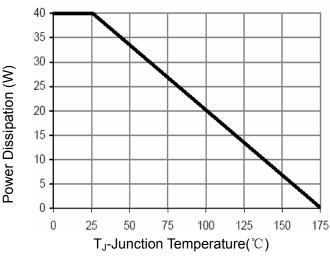


Figure 9 Power De-rating

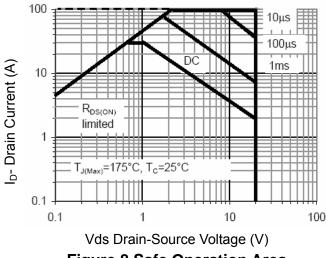


Figure 8 Safe Operation Area

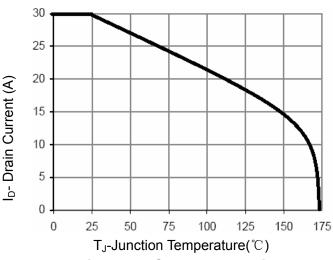
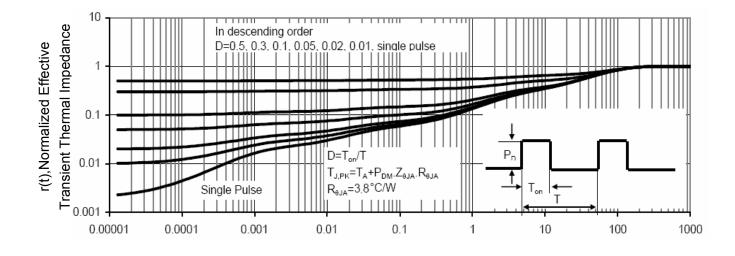


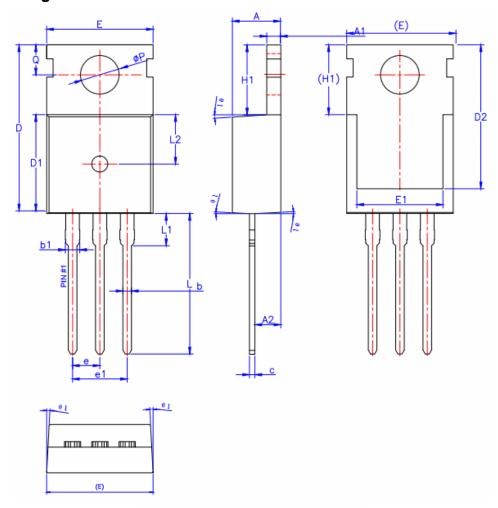
Figure 10 Current De-rating



Square Wave Pluse Duration(sec) **Figure 11 Normalized Maximum Transient Thermal Impedance**



TO-220-3L Package Information



SYMBOL	MIN	NOM	MAX	
Α	4.40	4.50	4.60	
A1	1.27	1.30	1.33	
A2	2.30	2.40	2.50	
b	0.70	_	0.90	
ь1	_	_	1.40	
С	0.45	0.50	0.60	
D	15.30	15.70	16.10	
D1	9.10	9.20	9.30	
D2	13.10	_	13.70	
E	9.70	9.90	10.20	
E1	7.80	8.00	8.20	
е	2.54BSC			
e1	5.08BSC			
H1	6.30	6.50	6.70	
L	12.78	13.08	13.38	
L1	_	_	3.50	
L2	4.60REF			
ØΡ	3.55	3.60	3.65	
Q	2.73	_	2.87	
θ 1	1*	3.	5*	

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