NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE3035Q uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

V_{DS} =30V,I_D =35A

 $R_{DS(ON)} < 7.0 m\Omega$ @ $V_{GS} = 10 V$

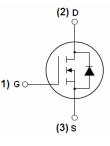
 $R_{DS(ON)}$ < 11m Ω @ V_{GS} =4.5V

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Secondary side synchronous rectifier
- High side switch in POL DC/DC converter

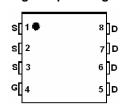
100% UIS TESTED!



Schematic diagram



Marking and pin assignment



DFN 3x3 EP top view

Package Marking and Ordering Information

	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
Ī	NCE3035Q	NCE3035Q	DFN 3x3 EP	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	35	А
Pulsed Drain Current	I _{DM}	120	Α
Maximum Power Dissipation	P _D	35	W
Derating factor		0.28	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	150	mJ
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 150	°C



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NCE3035Q

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	R _{eJC}	3.6	°C/W	Ì
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Electrical Characteristics (TC=25°C unless otherwise noted)

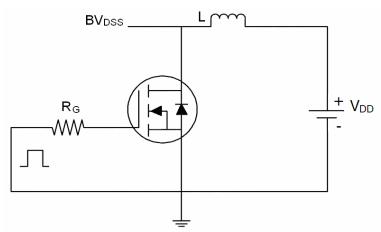
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	30	33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.6	3	V
Drain Source On State Decistores	В	V _{GS} =10V, I _D =12A	-	6.5	7.0	m0
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =10A	-	9	11	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =12A	30	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ -45\/\/ -0\/	-	2330	-	PF
Output Capacitance	C _{oss}	V_{DS} =15V, V_{GS} =0V, F=1.0MHz	-	460	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIHZ	-	230	-	PF
Switching Characteristics (Note 4)	-		•			•
Turn-on Delay Time	$t_{d(on)}$		-	18	-	nS
Turn-on Rise Time	t _r	V _{DD} =15V,I _D =12A	-	10	-	nS
Turn-Off Delay Time	$t_{d(off)}$	V_{GS} =10 V , R_{GEN} =6 Ω	-	34	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg	\/ -45\/1 -40A	-	45	-	nC
Gate-Source Charge	Q_{gs}	V _{DS} =15V,I _D =12A,	-	9.4	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	7.7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =12A	-	0.85	1.2	V
Diode Forward Current (Note 2)	Is		-	-	35	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 12A	-	-	47	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	-	25	nC
			-			•

Notes:

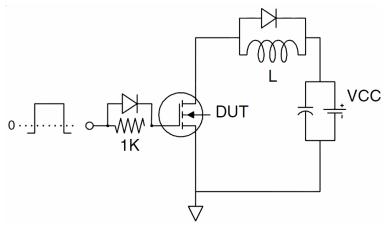
- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature}.$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}$ C,V_{DD}=15V,V_G=10V,L=0.5mH,Rg=25 Ω

Test Circuit

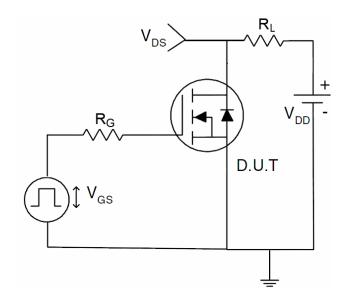
1) E_{AS} Test Circuits



2) Gate Charge Test Circuit



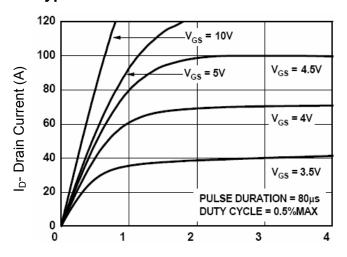
3) Switch Time Test Circuit



Pb Free Product



Typical Electrical and Thermal Characteristics (Curves)



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics

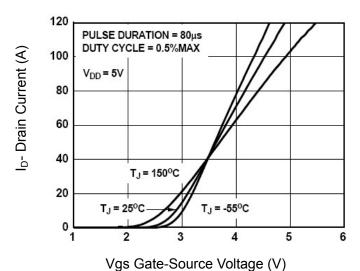


Figure 2 Transfer Characteristics

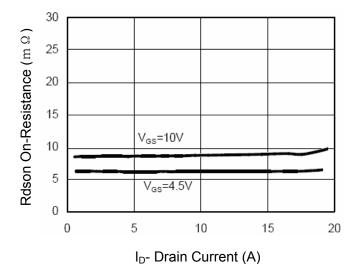


Figure 3 Rdson- Drain Current

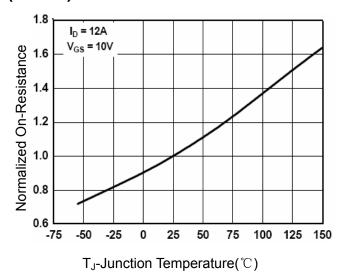


Figure 4 Rdson-Junction Temperature

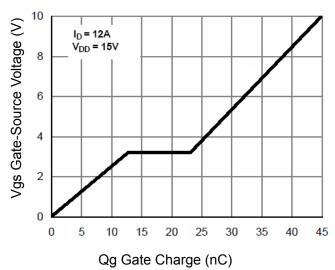


Figure 5 Gate Charge

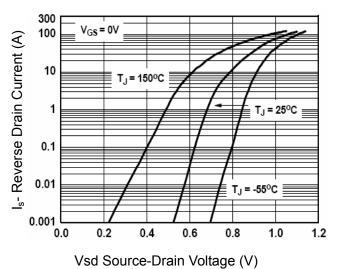


Figure 6 Source- Drain Diode Forward

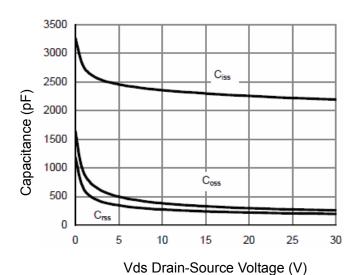


Figure 7 Capacitance vs Vds

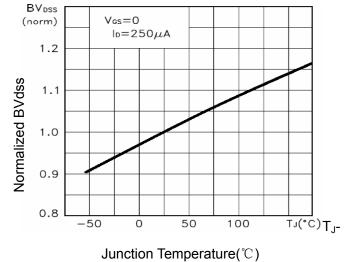


Figure 9 BV_{DSS} vs Junction Temperature

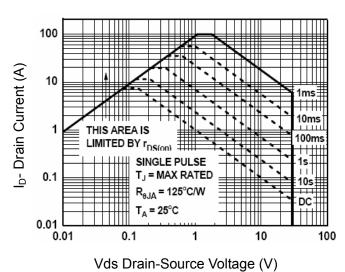


Figure 8 Safe Operation Area

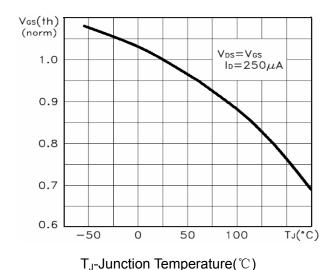


Figure 10 V_{GS(th)} vs Junction Temperature

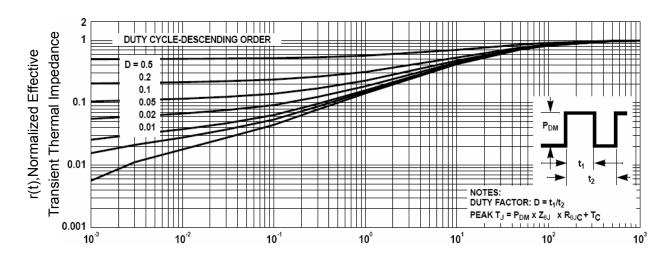
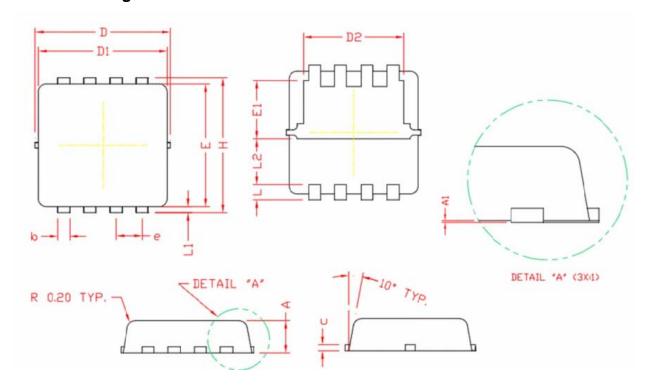


Figure 11 Normalized Maximum Transient Thermal Impedance

Square Wave Pluse Duration(sec)



DFN3X3 EP Package Information



COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.80	0.90
A1	0.00	0.03	0.05
b	0.24	0.30	0.35
С	0.10	0.15	0.20
D	3. 25	3.32	3.40
D1	3.05	3.15	3.25
D2	2.40	2.50	2.60
E	3.00	3.10	3.20
E1	1.35	1.45	1.55
е	0	.65 BSC	
Н	3. 20	3.30	3.40
L	0.30	0.40	0.50
L1	0.10	0.15	0.20
L2	1	. 13 REF	



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NCE3035Q

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C3M0021120D DMN13M9UCA6-7 BSS340NWH6327XTSA1 MCM3400A-TP DMTH10H4M6SPS-13 IPS60R1K0PFD7SAKMA1

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