NCE30ND09S

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE30ND09S uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

V_{DS} =30V,I_D =9A

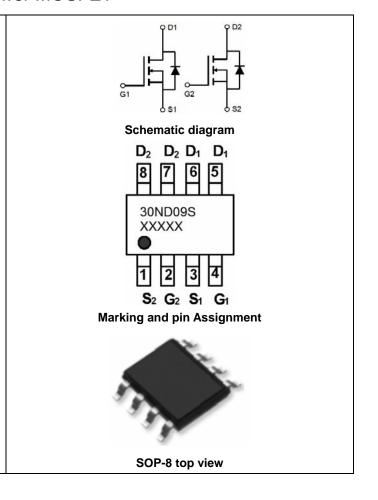
 $R_{DS(ON)}$ < 12m Ω @ V_{GS} =10V

 $R_{DS(ON)}$ < 17m Ω @ V_{GS} =4.5V

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
30ND09S	NCE30ND09S	SOP-8	Ø330mm	12mm	4000 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Abootate maximum ratings (TA=20 camous strict vice noted)							
Symbol	Limit	Unit					
V _{DS}	30	V					
V _G s	±20	V					
I _D	9	А					
I _D (100℃)	6.4	Α					
I _{DM}	40	А					
P _D	2	W					
T_{J} , T_{STG}	-55 To 150	$^{\circ}\!\mathbb{C}$					
	Symbol VDS VGS ID ID(100°C) IDM PD	Symbol Limit VDS 30 VGS ±20 ID 9 ID(100°C) 6.4 IDM 40 PD 2					

Thermal Characteristic

Parameter	Symbol	Тур	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	62.5	85	°C/W

NCE30ND09S

Electrical Characteristics (T_c=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA 30		-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	V _{DS} =30V,V _{GS} =0V -		1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} ,I _D =250μA	1.0	1.6	2.2	V
Drain-Source On-State Resistance	В	V _{GS} =10V, I _D =6A	30 - - - 26	10.3	12.0	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4A	-	13.9	17.0	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =6A	26	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C _{lss}	\/ -15\/\/ -0\/	-	1210	-	PF
Output Capacitance	Coss	V_{DS} =15V, V_{GS} =0V, F=1.0MHz	-	160	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0IVID2	-	105	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	5	-	nS
Turn-on Rise Time	t _r	V_{DD} =15V, R_L =0.75 Ω	-	12	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{G} =3 Ω	-	19	-	nS
Turn-Off Fall Time	t _f		-	6	-	nS
Total Gate Charge	Q_g	\/ 45\/ L CA	-	17.5		nC
Gate-Source Charge	Q_{gs}	V _{DS} =15V,I _D =6A,	-	3		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	4.1		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =6A	-		1.2	V
Diode Forward Current (Note 2)	I _S		-	-	9	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =6A -		19	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	10	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

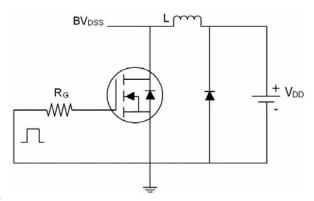
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

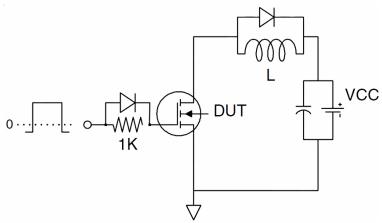
NCE30ND09S

Test circuit

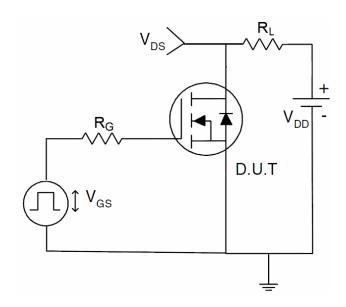
1) E_{AS} test Circuits



2) Gate charge test Circuit:



3) Switch Time Test Circuit:





Typical Electrical and Thermal Characteristics (Curves)

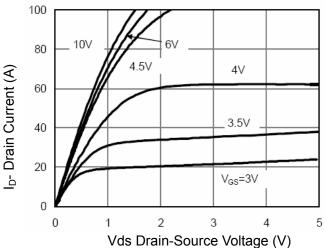


Figure 1 Output Characteristics

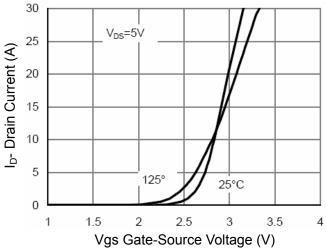


Figure 2 Transfer Characteristics

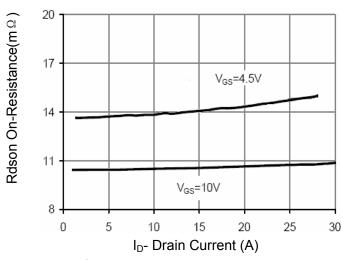


Figure 3 Rdson- Drain Current

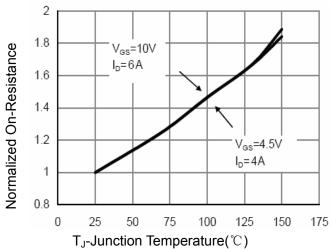


Figure 4 Rdson-Junction Temperature

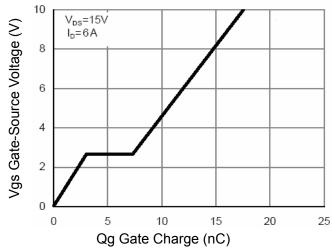


Figure 5 Gate Charge

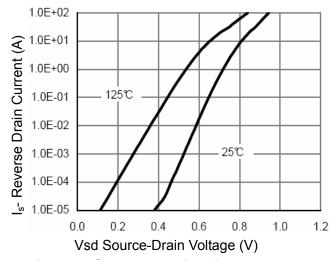


Figure 6 Source- Drain Diode Forward



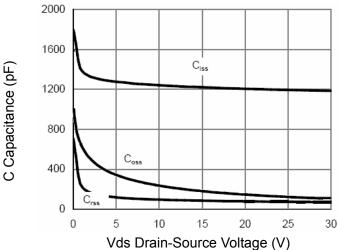


Figure 7 Capacitance vs Vds

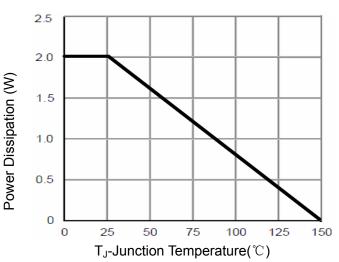


Figure 9 Power De-rating

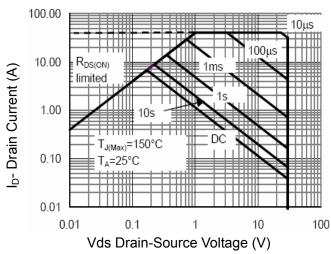


Figure 8 Safe Operation Area

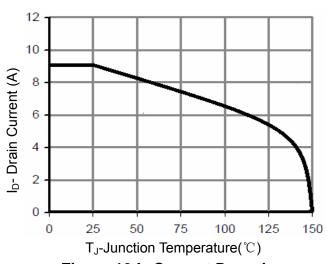
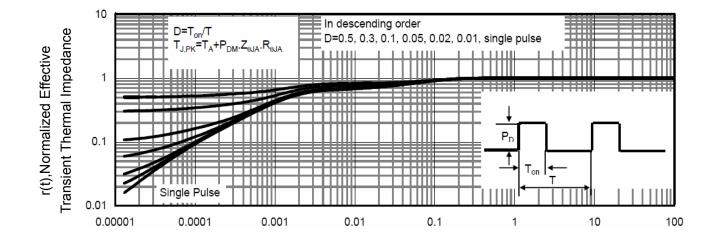


Figure 10 I_D Current De-rating



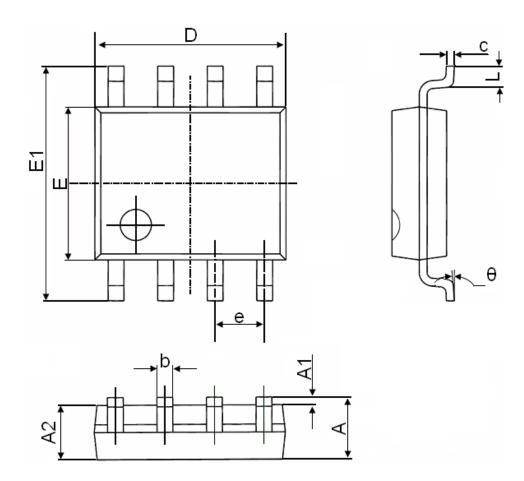
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance





SOP-8 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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