

# NCE30P50G

# NCE P-Channel Enhancement Mode Power MOSFET

### Description

The NCE30P50G uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

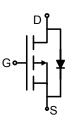
#### **General Features**

- V<sub>DS</sub> =-30V,I<sub>D</sub> =-50A
  R<sub>DS(ON)</sub> < 7mΩ @ V<sub>GS</sub>=-10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

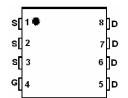
## Application

• Battery and loading switching

#### 100% UIS TESTED!



Schematic diagram



#### Marking and pin assignment



#### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE30P50G	NCE30P50G	DFN 5x6 EP	-	-	-

#### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	Ι <sub>D</sub>	-50	A
Pulsed Drain Current	I <sub>DM</sub>	-200	A
Maximum Power Dissipation	PD	35	W
Derating factor		0. 28	<b>W</b> /°C
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	300	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	°C

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	R <sub>θJC</sub>	3.6	°C/W	]
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#### Electrical Characteristics (TC=25°C unless otherwise noted)

Off Characteristics      Drain-Source Breakdown Voltage    BV <sub>DSS</sub> Zero Gate Voltage Drain Current    I <sub>DSS</sub> Gate-Body Leakage Current    I <sub>GSS</sub> On Characteristics <sup>(Note 3)</sup> Gate Threshold Voltage      Gate Threshold Voltage    V <sub>GS(th)</sub> Drain-Source On-State Resistance    R <sub>DS(ON)</sub> Forward Transconductance    g <sub>FS</sub> Dynamic Characteristics <sup>(Note4)</sup> Input Capacitance      Output Capacitance    C <sub>Iss</sub> Output Capacitance    C <sub>rss</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-30 - -	-33 -	-	V
Zero Gate Voltage Drain Current    IDSS      Gate-Body Leakage Current    IGSS      On Characteristics (Note 3)    IGSS      Gate Threshold Voltage    VGS(th)      Drain-Source On-State Resistance    RDS(ON)      Forward Transconductance    gFS      Dynamic Characteristics (Note4)    Input Capacitance      Output Capacitance    Class	V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-			V
Gate-Body Leakage Current    IGSS      On Characteristics (Note 3)      Gate Threshold Voltage    VGS(th)      Drain-Source On-State Resistance    RDS(ON)      Forward Transconductance    gFS      Dynamic Characteristics (Note4)    Input Capacitance      Output Capacitance    Cliss      Output Capacitance    Coss	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V		-		
On Characteristics (Note 3)      Gate Threshold Voltage    V <sub>GS(th)</sub> Drain-Source On-State Resistance    R <sub>DS(ON)</sub> Forward Transconductance    g <sub>FS</sub> Dynamic Characteristics (Note4)    Input Capacitance      Output Capacitance    C <sub>Iss</sub> Output Capacitance    C <sub>oss</sub>		-	1	1	μA
Gate Threshold VoltageVGS(th)Drain-Source On-State ResistanceRDS(ON)Forward TransconductancegFSDynamic Characteristics (Note4)Input CapacitanceCIssOutput CapacitanceCoss			-	±100	nA
Drain-Source On-State Resistance  RDS(ON)    Forward Transconductance  gFS    Dynamic Characteristics (Note4)  Input Capacitance    Output Capacitance  Cliss    Output Capacitance  Coss	N/ N/ N 050 A		<u> </u>		
Forward Transconductance  gFS    Dynamic Characteristics (Note4)    Input Capacitance  Clss    Output Capacitance  Coss	$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$	-1	-1.5	-2.2	V
Dynamic Characteristics (Note4)    Input Capacitance  Cliss    Output Capacitance  Coss	$V_{GS}$ =-10V, I <sub>D</sub> =-10A	-	4.4	7	mΩ
Input Capacitance  Cliss    Output Capacitance  Coss	V <sub>DS</sub> =-10V,I <sub>D</sub> =-15A	-	20	-	S
Output Capacitance Coss			<u> </u>		
		-	3590	-	PF
Reverse Transfer Canacitance	V <sub>DS</sub> =-15V,V <sub>GS</sub> =0V,	-	695	-	PF
	F=1.0MHz	-	665	-	PF
Switching Characteristics (Note 4)		•	<u> </u>		
Turn-on Delay Time t <sub>d(on)</sub>		-	13	-	nS
Turn-on Rise Time t <sub>r</sub>	V <sub>DD</sub> =-15V,I <sub>D</sub> =-10A	-	12	-	nS
Turn-Off Delay Time t <sub>d(off)</sub>	$V_{GS}$ =-10V,R <sub>GEN</sub> =6 $\Omega$	-	50	-	nS
Turn-Off Fall Time t <sub>f</sub>		-	14	-	nS
Total Gate Charge Qg		-	84	-	nC
Gate-Source Charge Q <sub>gs</sub>	V <sub>DS</sub> =-15V,I <sub>D</sub> =-10A, V <sub>GS</sub> =-10V	-	11.7	-	nC
Gate-Drain Charge Q <sub>gd</sub>	V <sub>GS</sub> =-10V	-	25	-	nC
Drain-Source Diode Characteristics			<u> </u>		
Diode Forward Voltage (Note 3) V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-10A	-	-0.85	-1.2	V
Diode Forward Current (Note 2)		-	-	-50	А
Reverse Recovery Time t <sub>rr</sub>	TJ = 25°C, IF = -10A	-	-	45	nS
Reverse Recovery Charge Qrr	di/dt = 100A/µs(Note3) 43				
Forward Turn-On Time ton I	$u/u = 100A/\mu s(NOIe3)$	-	-	43	nC

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, t  $\leq$  10 sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production 5. EAS condition: Tj=25 $^\circ\!\!\!C,V_{DD}\text{=-}15V,V_{G}\text{=-}10V,L\text{=}0.5\text{mH},Rg\text{=-}25\Omega$

Wuxi NCE Power Co., Ltd

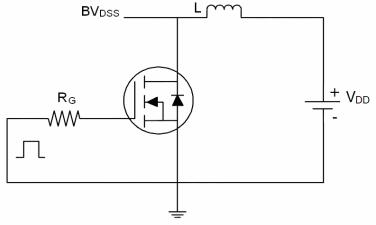




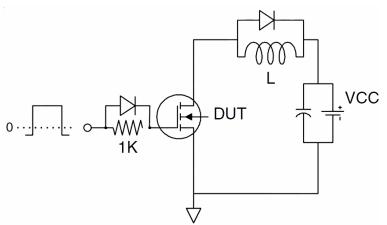


# Test Circuit

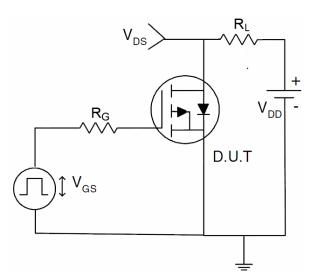
1) E<sub>AS</sub> Test Circuits



2) Gate Charge Test Circuit



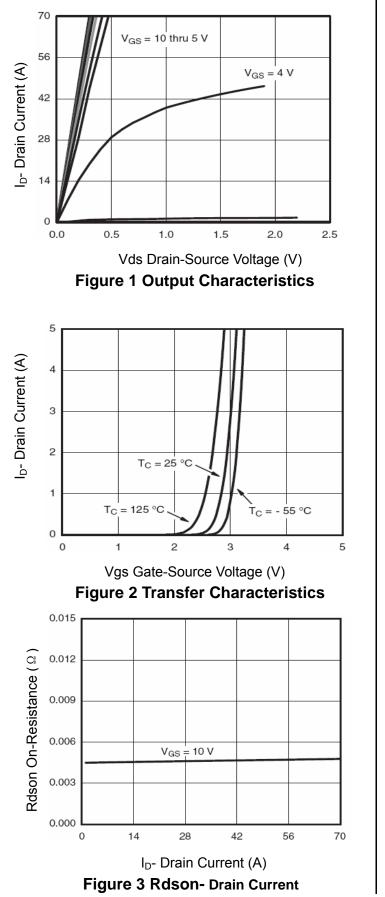
3) Switch Time Test Circuit







## **Typical Electrical and Thermal Characteristics (Curves)**



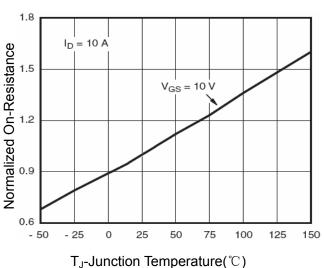
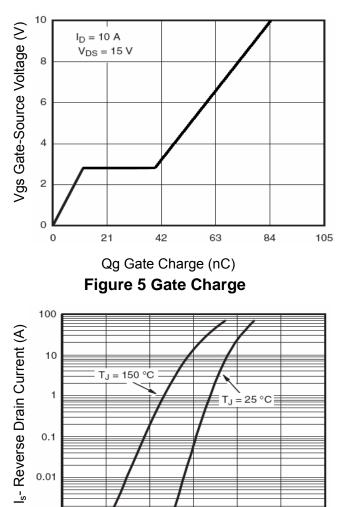


Figure 4 Rdson-Junction Temperature





0.6

0.4

0.001

0.0

0.2

0.8

1.0

1.2





NCE30P50G

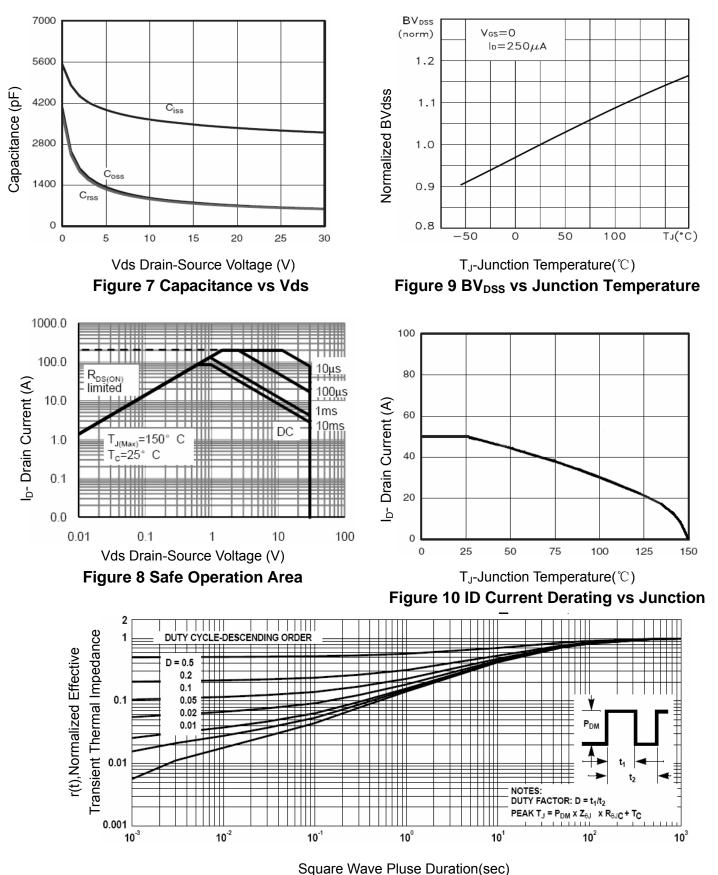


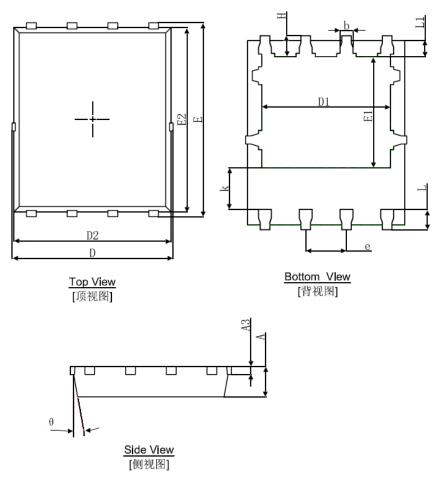
Figure 11 Normalized Maximum Transient Thermal Impedance





# NCE30P50G

## DFN5X6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A	0.900	1.000	0.035	0.039	
A3	0.254	4REF.	0.010	REF.	
D	4.944	5.096	0.195	0.201	
E	5.974	6.126	0.235	0.241	
D1	3.910	4.110	0.154	0.162	
E1	3.375	3.575	0.133	0.141	
D2	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
К	1.190	1.390	0.047	0.055	
b	0.035	0.450	0.014	0.018	
е	1.270	270(TYP.) 0.050(T		YP.)	
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
θ	8°	12°	8°	12°	







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