

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE3404Y uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch and PWM applications.

Genera Features

• $V_{DS} = 30V, I_D = 5.8A$

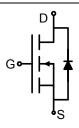
 $R_{DS(ON)}$ < 28m Ω @ V_{GS} =10V

 $R_{DS(ON)}$ < 40m Ω @ V_{GS} =4.5V

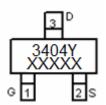
- High Power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Load switch
- PWM application



Schematic diagram



Marking and pin assignment



SOT-23-3L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3404Y	NCE3404Y	SOT-23-3L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	5.8	Α
Drain Current-Pulsed (Note 1)	I _{DM}	20	Α
Maximum Power Dissipation	P _D	1.4	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2) R _{0,JA} 89 °C/W
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	30	33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1	μΑ

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			.	1		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.2	1.6	2.4	V
nin Course On Ctata Registeres	Б	V _{GS} =10V, I _D =5A	-	23	28	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4A	-	27.5	40	mΩ
Forward Transconductance	g Fs	V _{DS} =5V,I _D =5A	-	15	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ -45\/\/ -0\/	-	485.8	-	PF
Output Capacitance	Coss	V_{DS} =15V, V_{GS} =0V, F=1.0MHz	-	65.2	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0IVIH2	-	54	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	5	-	nS
Turn-on Rise Time	t _r	V_{DD} =15V, R_L =3 Ω	-	3	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =3 Ω	-	15	-	nS
Turn-Off Fall Time	t _f		-	3.5	-	nS
Total Gate Charge	Qg	\/ -45\/ -50	-	12.6	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =15V,I _D =5A,	-	1.9	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	2.6	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V_{GS} =0 V , I_{S} =5 A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	5.8	Α

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

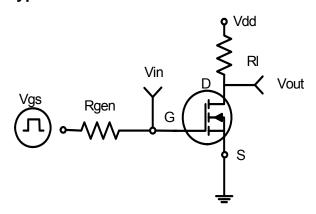


Figure 1:Switching Test Circuit

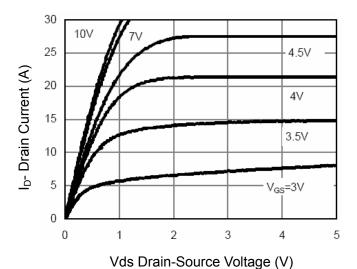


Figure 3 Output Characteristics

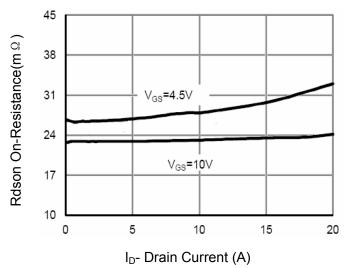


Figure 5 Drain-Source On-Resistance

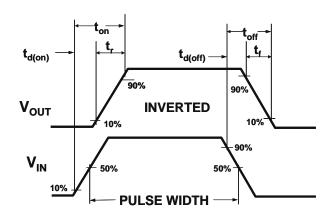


Figure 2:Switching Waveforms

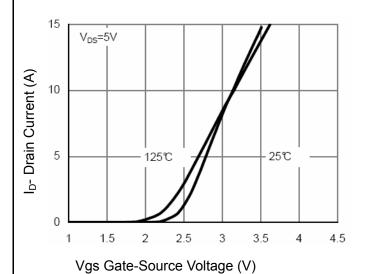


Figure 4 Transfer Characteristics

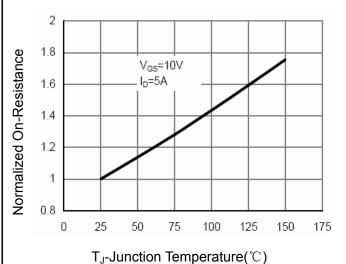
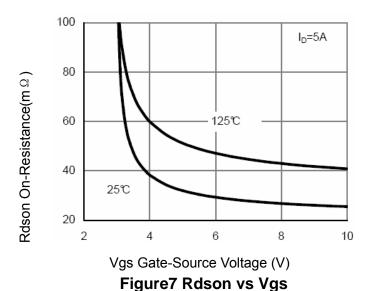
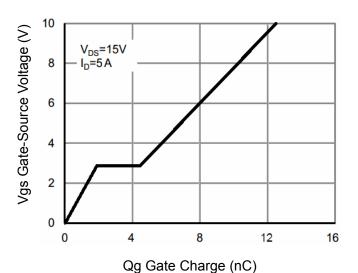


Figure 6 Drain-Source On-Resistance







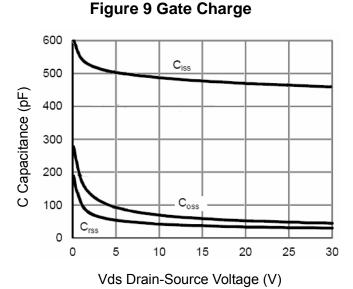


Figure 11 Capacitance vs Vds

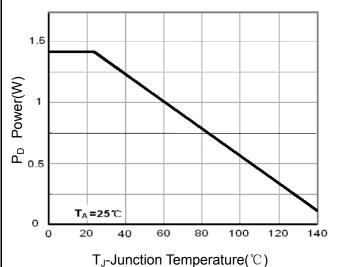


Figure 8 Power Dissipation

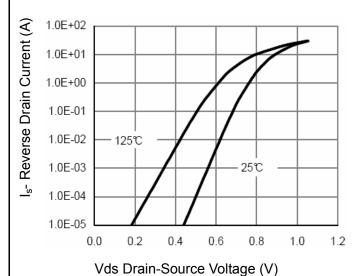
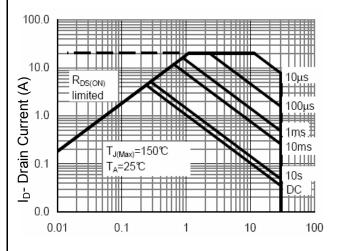


Figure 10 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)
Figure 12 Safe Operation Area



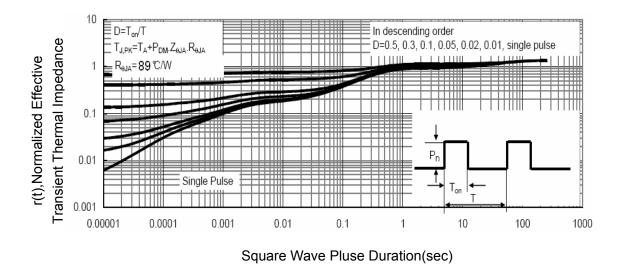
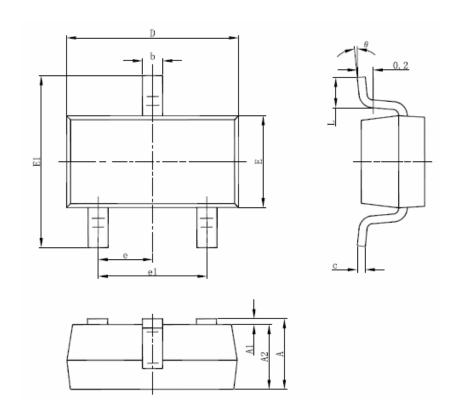


Figure 13 Normalized Maximum Transient Thermal Impedance



SOT-23-3L Package Information



Symbol	Dimensions Ir	Millimeters	Dimensions	In Inches	
Symbol	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
e	0.950	(BSC)	0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- $3. \ Package \ body \ sizes \ exclude \ mold \ flash \ and \ gate \ burrs. \ Mold \ flash \ at \ the \ non-lead \ sides \ should \ be \ less \ than \ 5 \ mils.$
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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