

NCE P-Channel Enhancement Mode Power MOSFET

Description

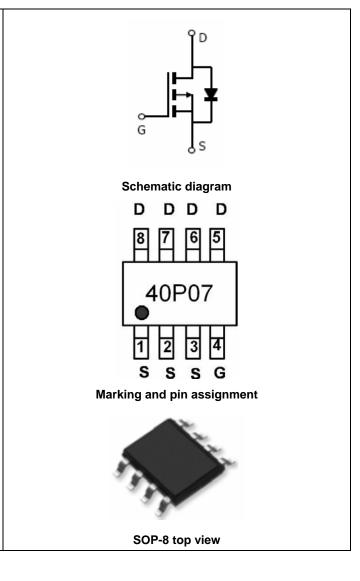
The NCE40P07S uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- $V_{DS} = -40V, I_D = -6.2A$ $R_{DS(ON)} < 25m\Omega @ V_{GS} = -10V$ $R_{DS(ON)} < 30m\Omega @ V_{GS} = -4.5V$
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- DC-DC converter



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
40P07	NCE40P07S	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-40	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	Ι _D	-6.2	А
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	-4	A
Pulsed Drain Current	I _{DM}	40	A
Maximum Power Dissipation	PD	2.5	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C





Thermal Characteristic

Thermal Resistance ,Junction-to-Ambient ^(Note 2)	R _{0JA}	50	°C/W
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Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-40V, V_{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	dy Leakage Current I _{GSS} V _{GS} =±20V,V _{DS} =0V		-	-	±100	nA
On Characteristics (Note 3)	·			•		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.1	-1.7	-2.5	V
Drain-Source On-State Resistance	В	V _{GS} =-10V, I _D =-5A	-	16	25	mΩ
Drain-Source On-State Resistance	R _{DS(ON)} –	V_{GS} =-4.5V, I _D =-5A	-	21	30	mΩ
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-5A	20	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	Clss	<u> </u>	-	1750	-	PF
Output Capacitance	C _{oss}	V _{DS} =-20V,V _{GS} =0V, F=1.0MHz	-	215	-	PF
Reverse Transfer Capacitance	C _{rss}		-	180	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	9	-	nS
Turn-on Rise Time	tr	V_{DD} =-20V, ,R _L =2 Ω	-	8	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =3 Ω	-	28	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg	(1 - 20)(1 - 50)	-	24	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-20V,I _D =-5A, V _{GS} =-10V	-	3.5	-	nC
Gate-Drain Charge	Q _{gd}	v _{GS} =-10v	-	6	-	nC
Drain-Source Diode Characteristics						•
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-6A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	-6.2	А

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, t \leq 10 sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production

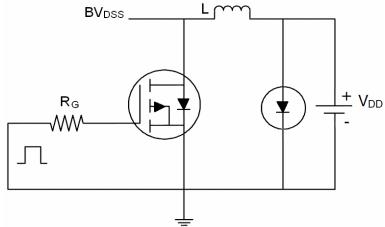


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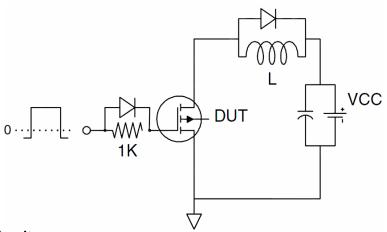




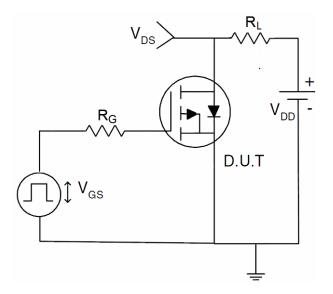
Test Circuit 1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



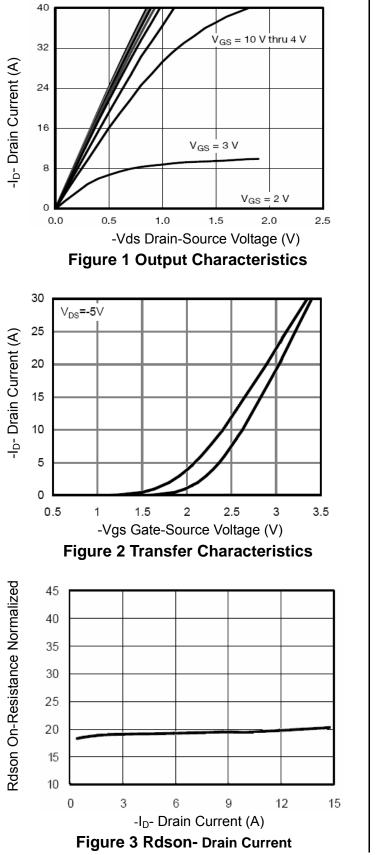
3) Switch Time Test Circuit

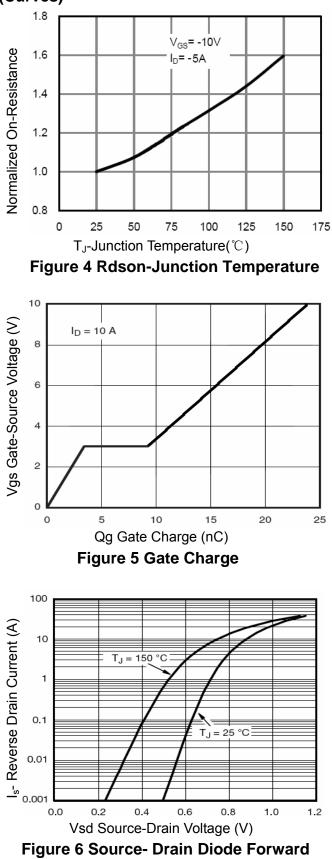














1

0.1

0.01

0.01

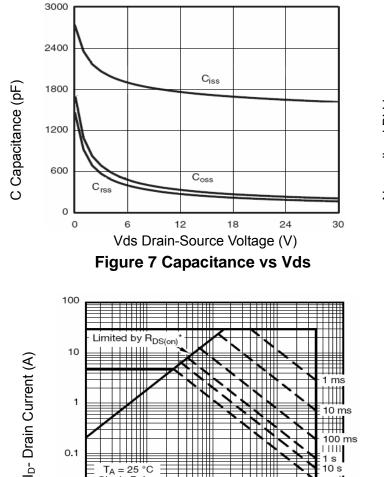
0.1

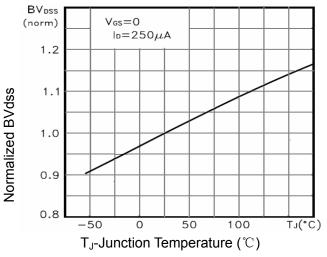
T_A = 25 °C Single Pulse

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NCE40P07S







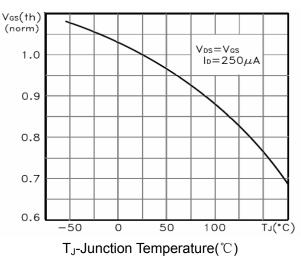
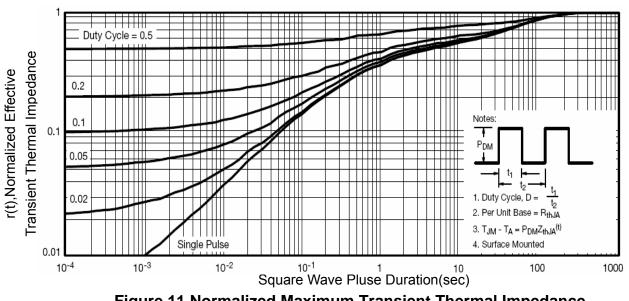


Figure 10 V_{GS(th)} vs Junction Temperature



1 ms

10 ms

100 ms

100

111

1 s

10 s DC

BVDSS Limited

10

1

Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area



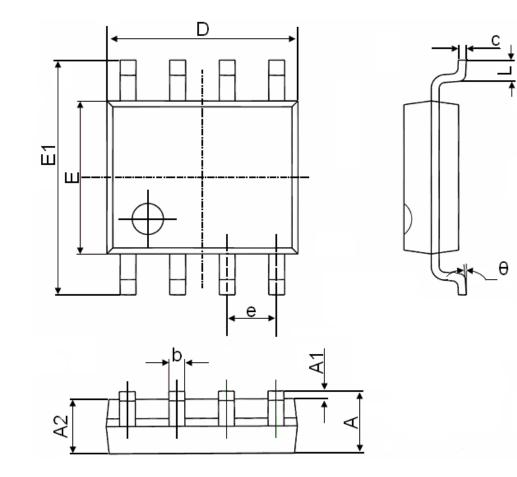


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SOP-8 Package Information



Symbol	Dimensions	n Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
C	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(BSC)		
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	







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