

NCE40P40K

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE40P40K uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge .This device is well suited for high current load applications.

General Features

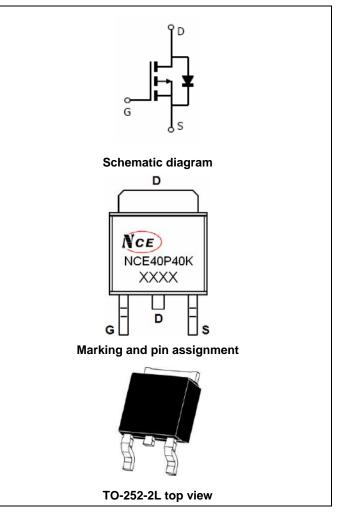
- V_{DS} =-40V, I_{D} =-40A $R_{DS(ON)}$ <14m Ω @ V_{GS} =-10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE40P40K	NCE40P40K	TO-252-2L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-40	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	-40	А
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	-25	А
Pulsed Drain Current	I _{DM}	-50	А
Maximum Power Dissipation	P _D	80	W
Derating factor		0.53	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	544	mJ
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 175	$^{\circ}$



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NCE40P40K

Thermal Characteristic

Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<u> </u>		•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-40V,V _{GS} =0V	-	-	-1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	<u>.</u>		•			
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-1.5	-1.9	-3.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-12A	-	12	14	mΩ
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-12A	34	-	-	S
Dynamic Characteristics (Note4)	<u>.</u>		•			
Input Capacitance	C _{lss}	\/ - 20\/\/ -0\/	-	2960	-	PF
Output Capacitance	Coss	V _{DS} =-20V,V _{GS} =0V,	-	370	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	310	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	10	-	nS
Turn-on Rise Time	t _r	V _{DD} =-20V,I _D =-20A	-	18	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10 V , R_{G} =3 Ω	-	38	-	nS
Turn-Off Fall Time	t _f		-	24	-	nS
Total Gate Charge	Qg	V - 20 I - 42A	-	72		nC
Gate-Source Charge	Q _{gs}	V_{DS} =-20, I_{D} =-12A, V_{GS} =-10V	-	14		nC
Gate-Drain Charge	Q _{gd}	V _{GS} 10V	-	15		nC
Drain-Source Diode Characteristics	<u>.</u>		•			
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =-20A	-		-1.2	V
Diode Forward Current (Note 2)	Is		-	-	-40	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =- 20A	-	40		nS
Reverse Recovery Charge	Qrr	di/dt = -100A/μs ^(Note3)	-	42		nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

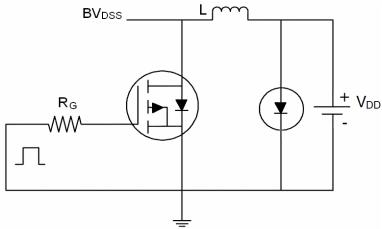
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** E_{AS} condition: Tj=25 $^{\circ}$ C,V_{DD}=-20V,V_G=-10V,L=1mH,Rg=25 Ω ,I_{AS}=33A

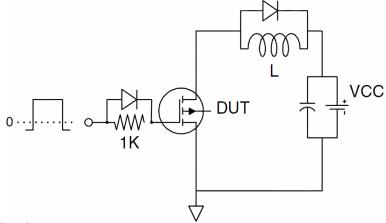


Test Circuit

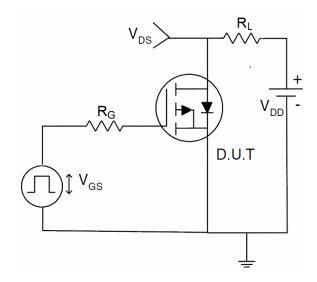
1) E_{AS} Test Circuit



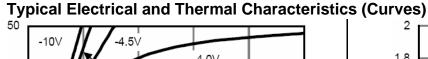
2) Gate Charge Test Circuit

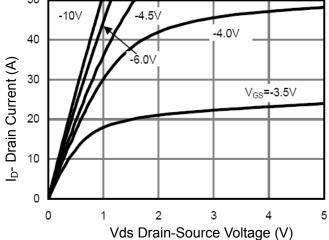


3) Switch Time Test Circuit











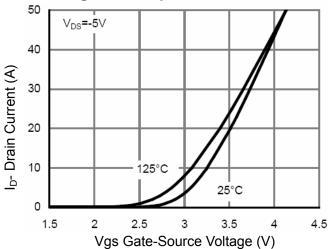


Figure 2 Transfer Characteristics

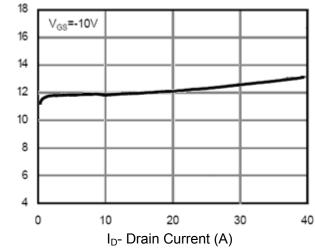


Figure 3 Rdson- Drain Current

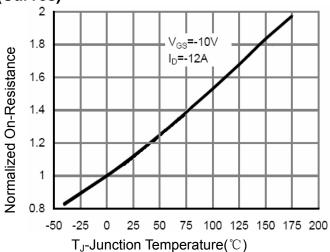
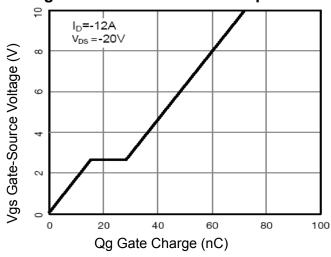


Figure 4 Rdson-Junction Temperature



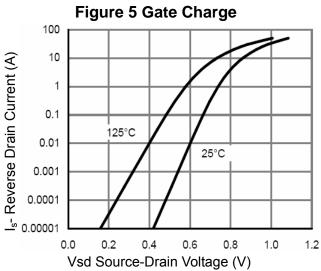
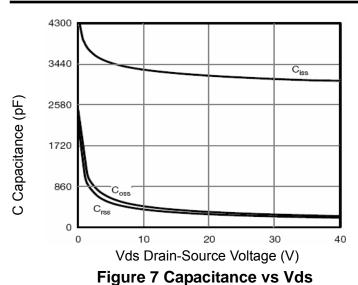


Figure 6 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area

10

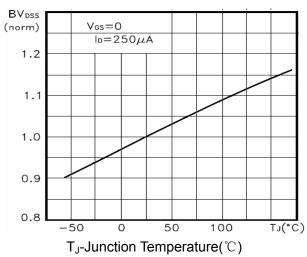


Figure 9 BV_{DSS} vs Junction Temperature

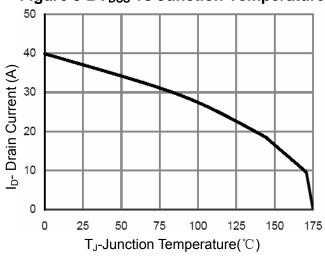
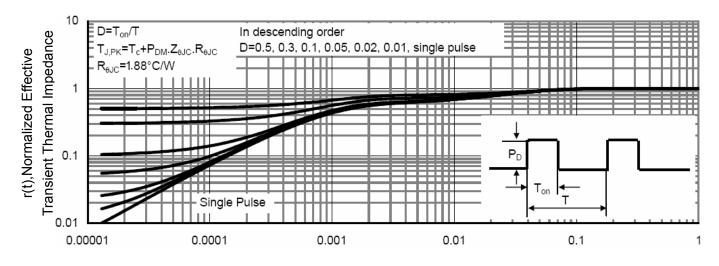


Figure 10 ID Current Derating vs Junction Temperature



100

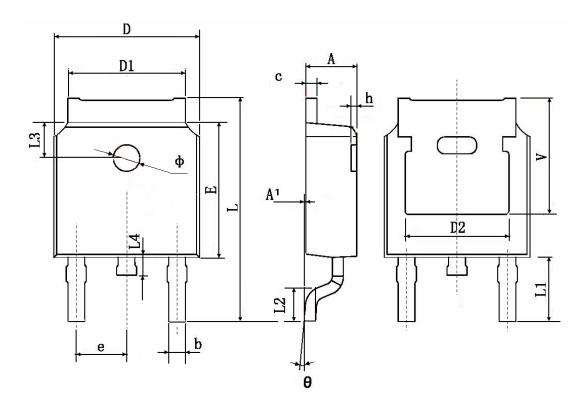
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

Pb Free Product



TO-252 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350 TYP. 0.211 T			TYP.	



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NCE40P40K

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