

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE4953 uses advanced trench technology to provide excellent $R_{\rm DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a load switch or in PWM applications.

General Features

• $V_{DS} = -30V, I_{D} = -5.1A$

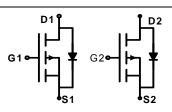
 $R_{DS(ON)}$ < 90m Ω @ V_{GS} =-4.5V

 $R_{DS(ON)}$ < 55m Ω @ V_{GS} =-10V

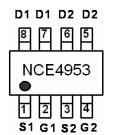
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOP-8 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4953	NCE4953	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	-5.1	Α
Drain Current-Pulsed (Note 1)	I _{DM}	-20	Α
Maximum Power Dissipation	P _D	2.5	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	50	°C/W
	I VOJA	00	0,00

Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-30	-33	-	٧
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V,V _{GS} =0V	-	-	-1	μA



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NCE4953

I _{GSS}	I _{GSS} V _{GS} =±20V,V _{DS} =0V		-	±100	nA
•					
V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-1	-1.6	-3	V
R _{DS(ON)}	V _{GS} =-10V, I _D =-5.1A	-	43	55	mΩ
	V _{GS} =-4.5V, I _D =-4.2A	-	62	90	mΩ
g FS	g _{FS} V _{DS} =-15V,I _D =-4.5A		7	-	S
C _{lss}	\/ - 45\/\/ -0\/	-	520	-	PF
C _{oss}		-	130	-	PF
C _{rss}	F=1.0WIHZ	-	70	-	PF
t _{d(on)}		-	7	-	nS
t _r	V _{DD} =-15V, ID=-1A,	-	13	-	nS
$t_{d(off)}$	V_{GS} =-10 V , R_{GEN} =6 Ω	-	14	-	nS
t _f		-	9	-	nS
Qg		-	11	-	nC
Q _{gs}	V _{DS} =-15V,I _D =-5.1A,V _{GS} =-10V	-	2.2	-	nC
Q_{gd}		-	3	-	nC
V _{SD}	V _{GS} =0V,I _S =-5.1A	-	-	-1.2	V
	$V_{GS(th)}$ $R_{DS(ON)}$ g_{FS} C_{lss} C_{oss} C_{rss} $t_{d(on)}$ t_{r} $t_{d(off)}$ t_{f} Q_{g} Q_{gs} Q_{gd}	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

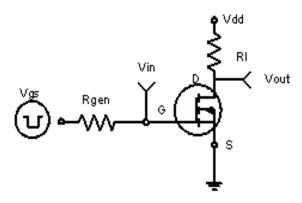


Figure 1:Switching Test Circuit

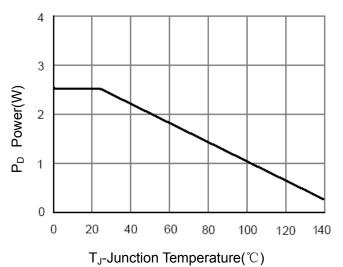


Figure 3 Power Dissipation

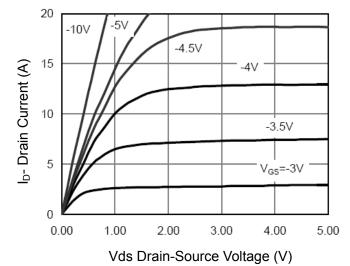


Figure 5 Output Characteristics

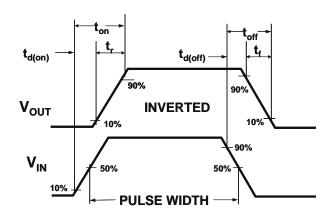


Figure 2:Switching Waveforms

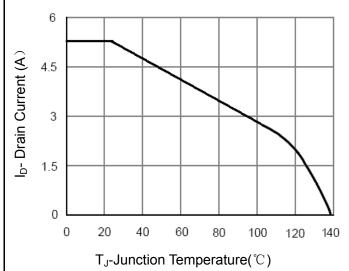


Figure 4 Drain Current

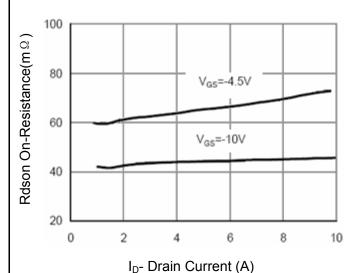


Figure 6 Drain-Source On-Resistance



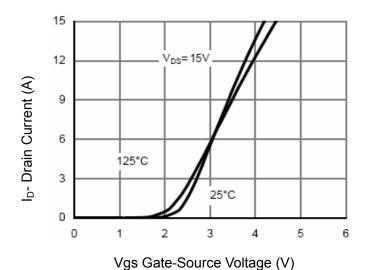
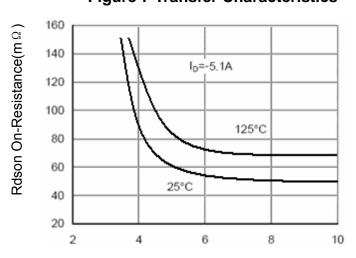


Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)
Figure 9 Rdson vs Vgs

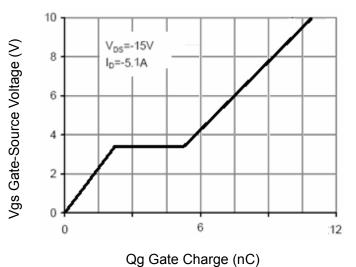


Figure 11 Gate Charge

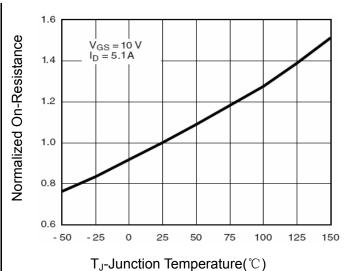


Figure 8 Drain-Source On-Resistance

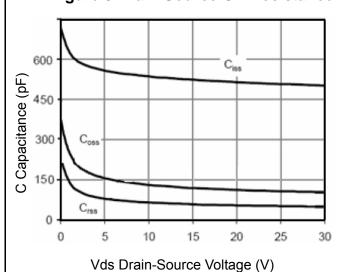


Figure 10 Capacitance vs Vds

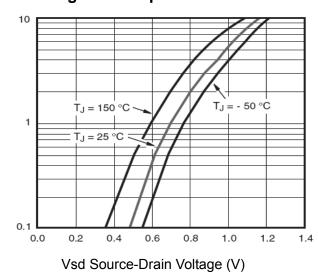
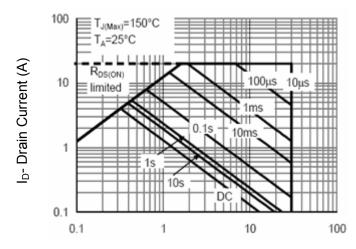


Figure 12 Source- Drain Diode Forward

Is- Reverse Drain Current (A)





Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area

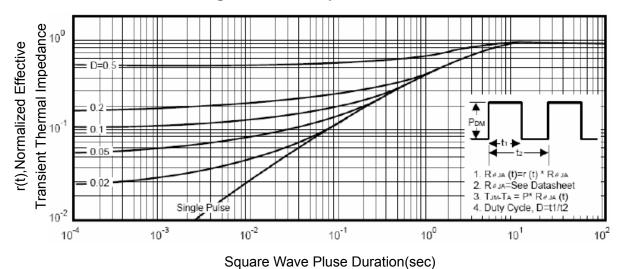
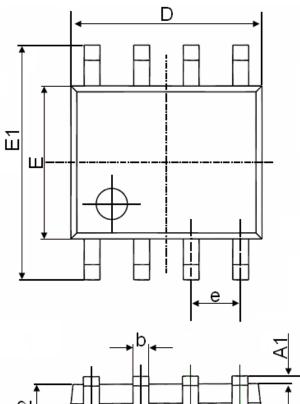


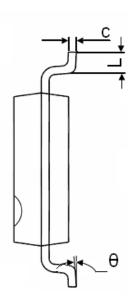
Figure 14 Normalized Maximum Transient Thermal Impedance

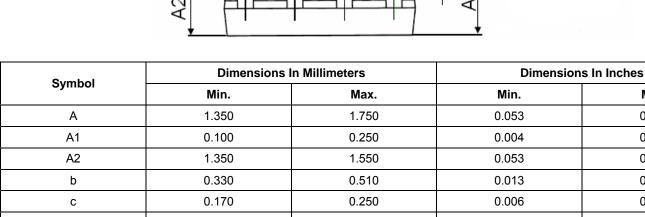
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SOP-8 Package Information







Symbol						
Symbol	Min.	Max.	Min.	Max.		
Α	1.350	1.750	0.053	0.069		
A1	0.100	0.250	0.004	0.010		
A2	1.350	1.550	0.053	0.061		
b	0.330	0.510	0.013	0.020		
С	0.170	0.250	0.006	0.010		
D	4.700	5.100	0.185	0.200		
E	3.800	4.000	0.150	0.157		
E1	5.800	6.200	0.228	0.244		
е	1.270	(BSC)	0.050	(BSC)		
L	0.400	1.270	0.016	0.050		
θ	0°	8°	0°	8°		



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NCE4953

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