

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE60P20K uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

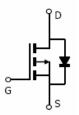
General Features

- V_{DS} =-60V, I_{D} =-20A $R_{DS(ON)}$ <42mΩ @ V_{GS} =-10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!



Schematic diagram



Marking and pin assignment



TO-252-2L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE60P20K	NCE60P20K	TO-252-2L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-60	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	-20	А
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	-14.1	Α
Pulsed Drain Current	I _{DM}	80	А
Maximum Power Dissipation	P _D	60	W
Derating factor		0.4	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	100	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{ heta JC}$	2.5	°C/W	

Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20 V , V_{DS} =0 V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=-250\mu A$	-2	-2.8	-3.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-20A	-	35	42	mΩ
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-20A	-	20	-	S
Dynamic Characteristics (Note4)				Į.		l .
Input Capacitance	C _{lss}	V 20V/V 0V	-	2485	-	PF
Output Capacitance	Coss	V_{DS} =-30V, V_{GS} =0V, F=1.0MHz	-	114	-	PF
Reverse Transfer Capacitance	C _{rss}	r=1.0lvlm2	-	104	-	PF
Switching Characteristics (Note 4)			•	Į.		l .
Turn-on Delay Time	t _{d(on)}		-	12	-	nS
Turn-on Rise Time	t _r	V_{DD} =-30 V , I_D =-20 A	-	14	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10 V , R_{GEN} =3 Ω	-	38	-	nS
Turn-Off Fall Time	t _f		-	15	-	nS
Total Gate Charge	Qg	V - 20VI - 20A	-	46.1	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-30V, I_{D} =-20A,	-	9.9	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =-10V	-	11.7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-20A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	-20	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = -20A	-	-	40	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	-	120	nC

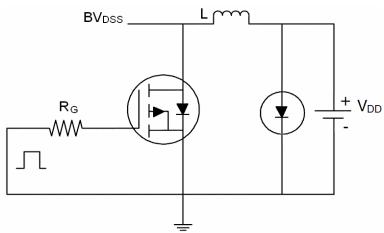
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** E_{AS} condition: Tj=25 $^{\circ}\text{C}$,V_{DD}=-30V,V_G=-10V,L=0.5mH,Rg=25 Ω

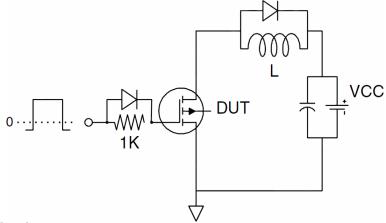


Test Circuit

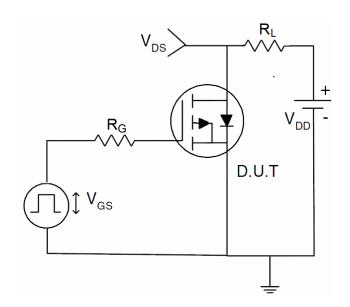
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit

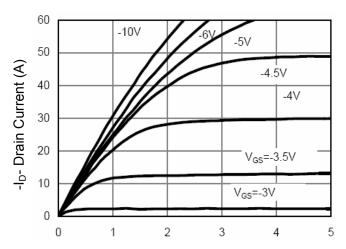


3) Switch Time Test Circuit



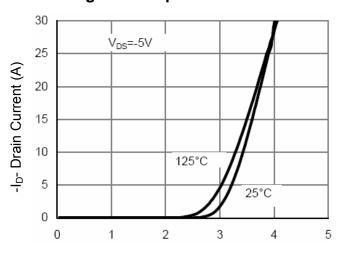


Typical Electrical and Thermal Characteristics (Curves)



-Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



-Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics

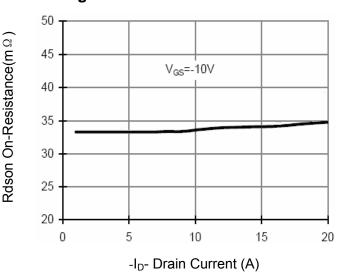


Figure 3 Rdson- Drain Current

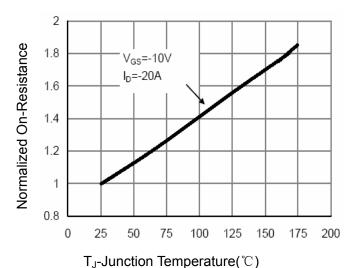


Figure 4 Rdson-JunctionTemperature

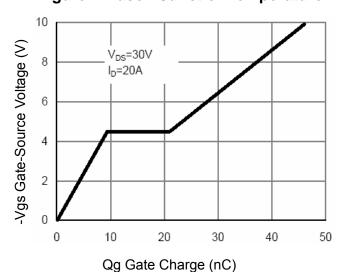


Figure 5 Gate Charge

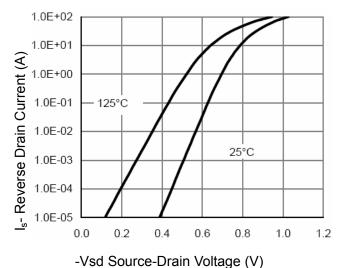


Figure 6 Source- Drain Diode Forward



100.0

10.0

1.0

0.1

0.0

0.01

-I_D- Drain Current (A)

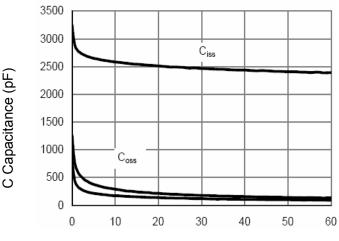
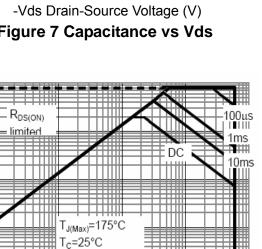


Figure 7 Capacitance vs Vds



-Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area

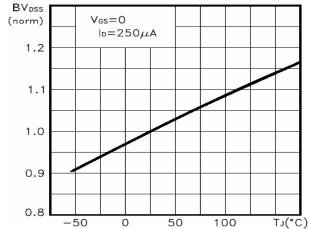
1

10

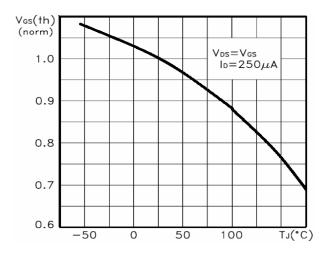
100

0.1

Normalized BVdss



 T_J -Junction Temperature ($^{\circ}$ C) Figure 9 BV_{DSS} vs Junction Temperature



 T_J -Junction Temperature($^{\circ}$ C) Figure 10 V_{GS(th)} vs Junction Temperature

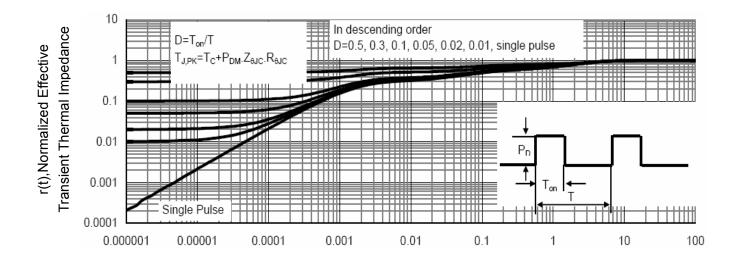
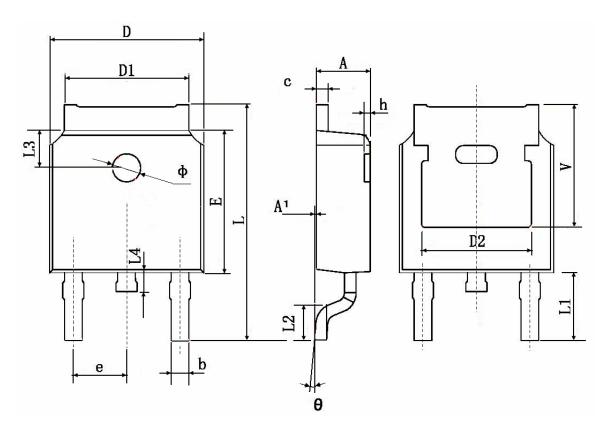


Figure 11 Normalized Maximum Transient Thermal Impedance

Square Wave Pluse Duration(sec)



TO-252 Package Information



Comple of	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.83	4.830 TYP.		TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	O TYP. 0.114 TYF		TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600	TYP.	0.063	TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	5.350 TYP.		I TYP.	

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