## NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE6990K uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

•  $V_{DS}$  =69V, $I_{D}$  =90A  $R_{DS(ON)} < 7.0 mΩ @ V_{GS}$ =10V (Typ:5.7mΩ)

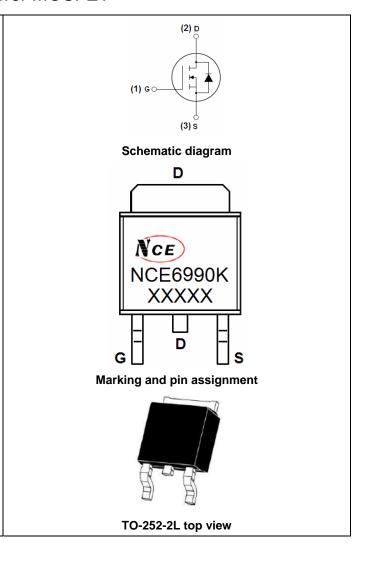
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

#### **Application**

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



**Package Marking and Ordering Information** 

-						
	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
ſ	NCE6990K	NCE6990K	TO-252-2L	-	-	-

## Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	69	V
Gate-Source Voltage	$V_{GS}$	±20	V
Drain Current-Continuous	I <sub>D</sub>	90	Α
Drain Current-Continuous(TC=100℃)	I <sub>D (100℃)</sub>	63.6	Α
Pulsed Drain Current	I <sub>DM</sub>	360	Α
Maximum Power Dissipation	P <sub>D</sub>	160	W
Derating factor		1.1	W/℃
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	450	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C



## **Thermal Characteristic**

Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup>	R <sub>0JC</sub>	0.94	°C/W
Thermal Resistance,Junction-to-Ambient <sup>(Note 2)</sup>	R <sub>θJA</sub>	60	°C/W

## Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

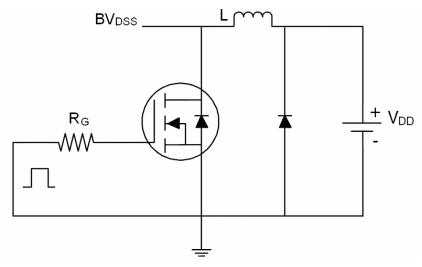
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·		•			
Drain-Source Breakdown Voltage	B <sub>VDSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	69	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =69V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)			•			•
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =VGS,I <sub>D</sub> =250μA	2	2.9	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	-	5.7	7.0	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =10V,I <sub>D</sub> =30A	25	-	-	S
Dynamic Characteristics (Note4)	·		•			
Input Capacitance	C <sub>lss</sub>	\\ 00\\\\ 0\\	-	3831.3	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}=30V, V_{GS}=0V,$	-	311.2	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	244.1	-	PF
Switching Characteristics (Note 4)			•			•
Turn-on Delay Time	t <sub>d(on)</sub>		-	15	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =30 $V$ , $IR_L$ =1 $\Omega$	-	11	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =2.5 $\Omega$	-	52	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	13	-	nS
Total Gate Charge	Qg	\/ -20\/ L -20 A	-	83.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =30V, $I_D$ =30A, $V_{GS}$ =10V	-	26	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	27.4	-	nC
Drain-Source Diode Characteristics			•			•
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =90A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	90	Α
Reverse Recovery Time	t <sub>rr</sub>	$T_J = 25^{\circ}C, I_F = 90A$	-	33		nS
Reverse Recovery Charge	Q <sub>rr</sub>	$di/dt = 100A/\mu s^{(Note3)}$	-	54		nC
Forward Turn-On Time	ton	Intrinsic turn-on time is negligible (turn-on is dominated by LS+L				/ I S+I D)

## Notes:

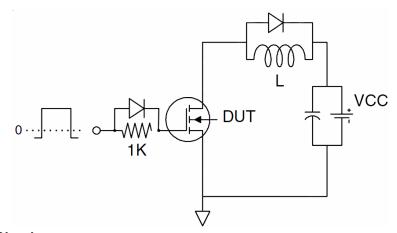
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production 5. EAS condition:Tj=25 $^{\circ}$ C,V<sub>DD</sub>=35V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$

## **Test Circuit**

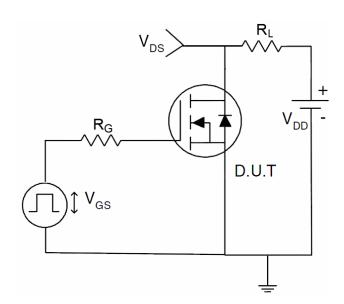
## 1) EAS test Circuit



## 2) Gate charge test Circuit



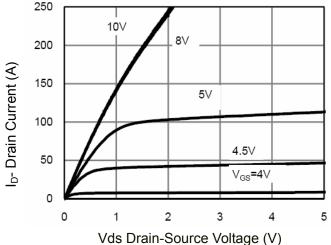
## 3) Switch Time Test Circuit



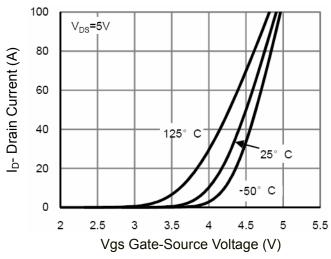
100 125 150 175



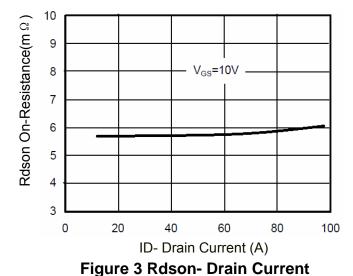




**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 



2 1.8 Normalized On-Resistance 1.6 V<sub>GS</sub>=10V, 30A 1.4 1.2

0.8 0.6

Figure 4 Rdson-JunctionTemperature

50

TJ-Junction Temperature(°C)

75

25

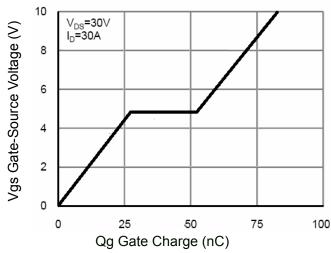


Figure 5 Gate Charge

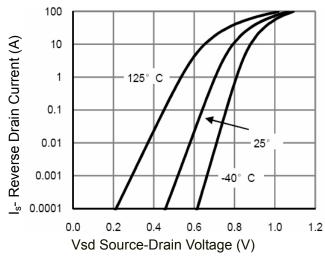


Figure 6 Source- Drain Diode Forward



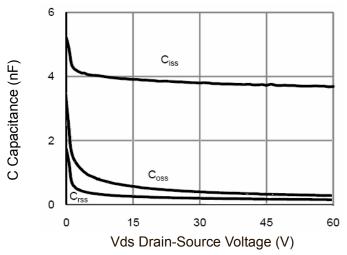
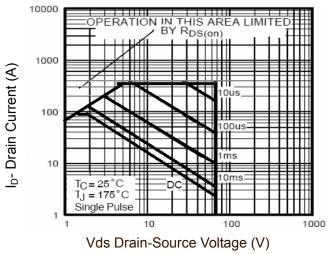


Figure 7 Capacitance vs Vds



**Figure 8 Safe Operation Area** 

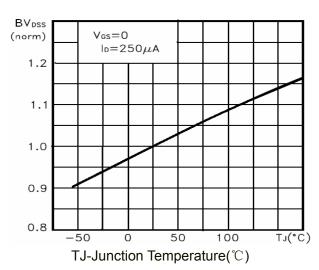


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

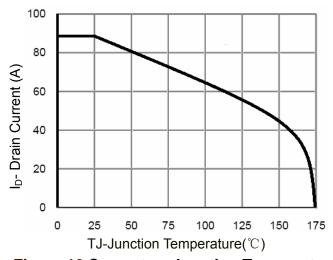
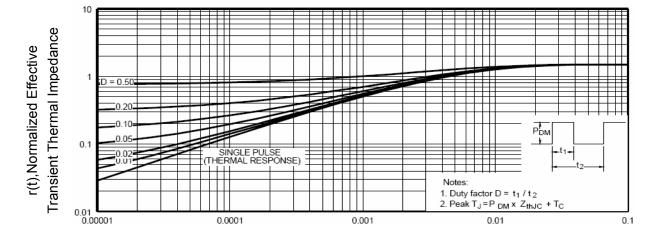


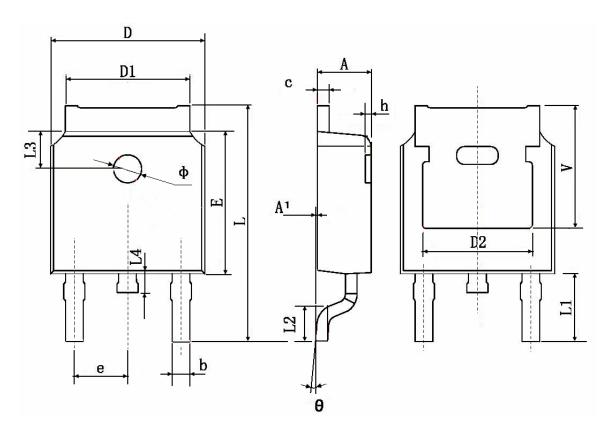
Figure 10 Current vs Junction Temperature



Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

# **TO-252 Package Information**



Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.83	O TYP.	0.190	TYP.	
Е	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	TYP.	0.114	TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063	TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211	I TYP.	

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