



NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE8804 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications .It is ESD protested.

General Features

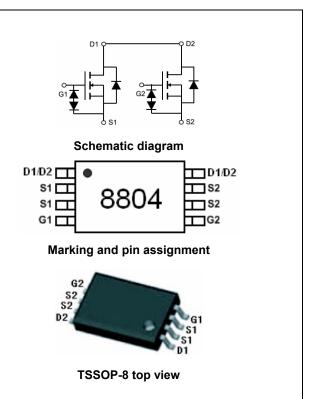
• V_{DS} = 20V,I_D =8A

$$\begin{split} R_{DS(ON)} &< 19 m \Omega @ V_{GS} = 2.5 V \\ R_{DS(ON)} &< 15 m \Omega @ V_{GS} = 4.5 V \\ ESD \ Rating: 2000V \ HBM \end{split}$$

- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Uni-directional load switch
- Bi-directional load switch



Package Marking and Ordering Information

ĺ	Device Marking Device		Device Package	Reel Size	Tape width	Quantity	
ĺ	8804	NCE8804	TSSOP-8	Ø330mm	12mm	3000 units	

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	20	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	I _D	8	A
Drain Current-Pulsed (Note 1)	I _{DM}	30	A
Maximum Power Dissipation	PD	2	W
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	62.5	°C /W
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Electrical Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	20			V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =20V, V_{GS} =0V	-	-	1	μA



Pb Free Product

NCE8804

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±10V, V_{DS} =0V	-	-	±10	μA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.45	0.7	1.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =4.5V, I _D =5A	-	11	15	mΩ
Drain-Source On-State Resistance		V _{GS} =2.5V, I _D =4A	-	15	19	mΩ
Forward Transconductance	g fs	V _{DS} =5V,I _D =5A	-	15	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}		-	1800	-	PF
Output Capacitance	Coss	V_{DS} =10V, V_{GS} =0V,	-	230	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	200	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	2.5		nS
Turn-on Rise Time	tr	V_{DD} =10V,RL=1.2 Ω	-	7.2		nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =3 Ω	-	49		nS
Turn-Off Fall Time	t _f		-	10.8		nS
Total Gate Charge	Qg	V 40V/L 0A	-	17.9		nC
Gate-Source Charge	Q _{gs}	V_{DS} =10V,I _D =8A,	-	1.5	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =4.5V	-	4.7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =8A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	8	А

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production





NCE8804

Typical Electrical and Thermal Characteristics

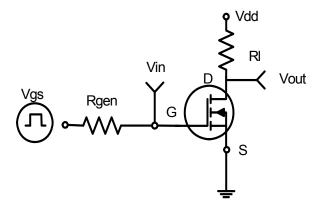
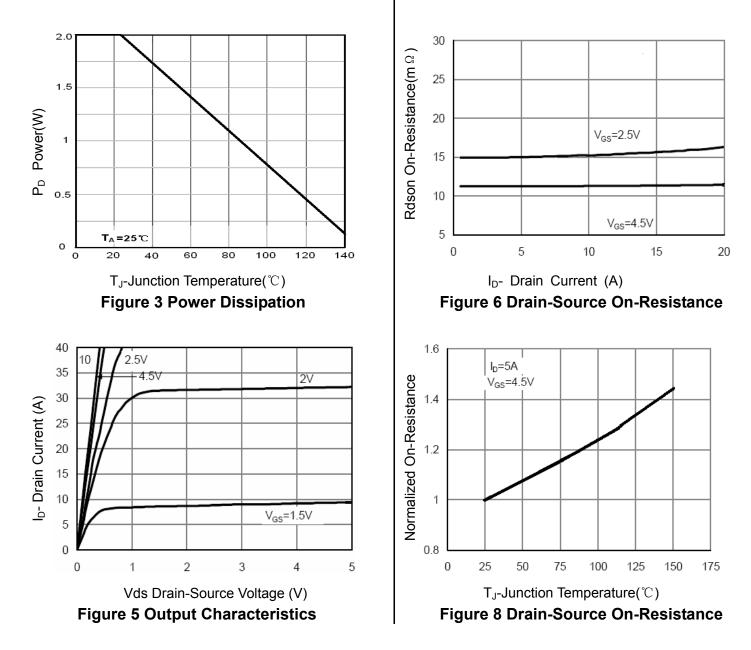


Figure 1:Switching Test Circuit



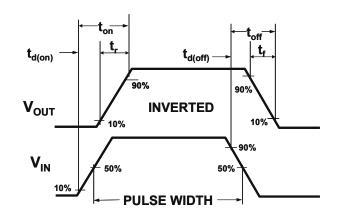
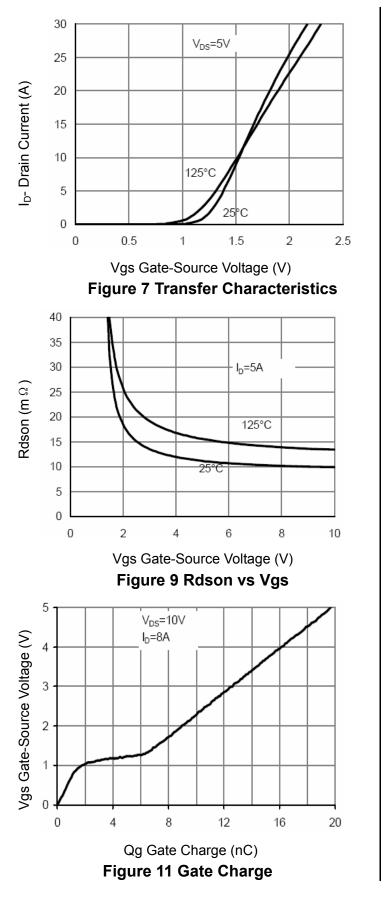


Figure 2:Switching Waveforms



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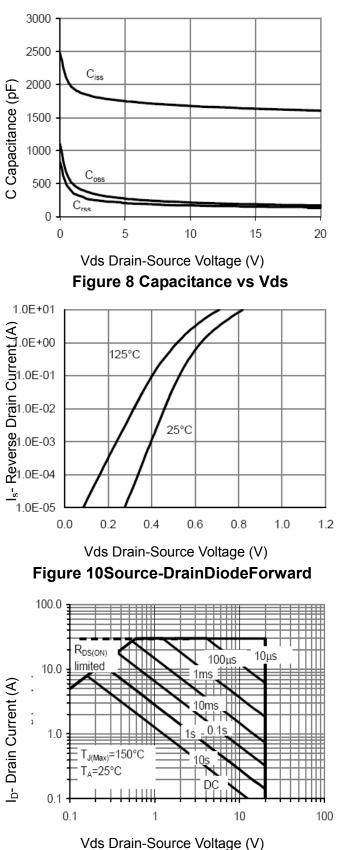


Figure 13 Safe Operation Area



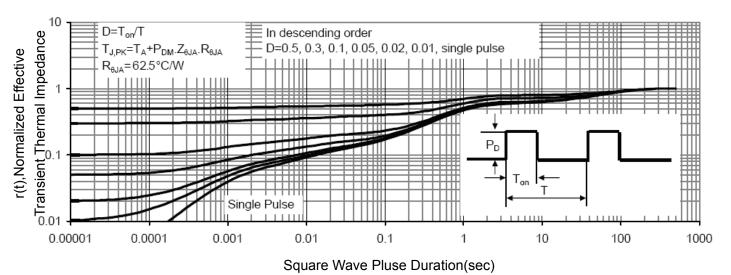


Figure 14 Normalized Maximum Transient Thermal Impedance

Pb Free Product

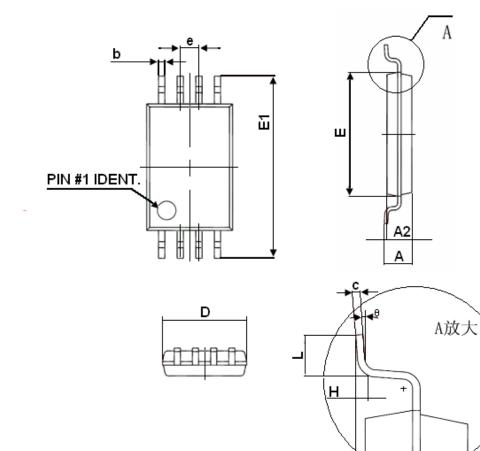
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Tssop-8 Package Information



Symbol	Dimensions In Millimeters				
	Min	Мах			
D	2.900	3.100			
E	4.300	4.500			
b	0.190	0.300			
С	0.090	0.200			
E1	6.250	6.550			
Α		1.100			
A2	0.800	1.000			
A1	0.020	0.150			
е	0.65(BSC)				
L	0.500	0.700			
Н	0.25	(TYP)			
Θ	1 °	7 °			

<u>A1</u>







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