

NCE N-Channel Super Trench II Power MOSFET

Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- •Ideal for high-frequency switching and synchronous rectification

General Features

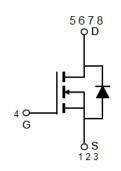
- V_{DS} =100V, I_D =14A $R_{DS(ON)}$ =8.4m Ω , typical@ V_{GS} =10V $R_{DS(ON)}$ =10.0m Ω , typical@ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!

SOP-8



Top View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP092N10AS	NCEP092N10AS	SOP-8	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I _D	14	Α
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	10	Α
Pulsed Drain Current	I _{DM}	300	Α
Maximum Power Dissipation	P _D	3.5	W
Single pulse avalanche energy (Note 4)	E _{AS}	350	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}\mathbb{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	36	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

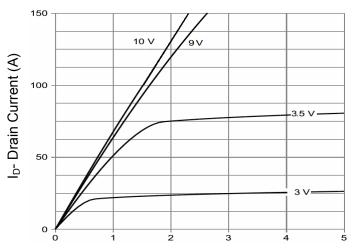
Parameter Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	Source Breakdown Voltage BV _{DSS} V _{GS} =0V I _D =250μA		100		-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μΑ	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_D=250\mu A$	1.2	1.7	2.2	V	
Drain Source On State Registeres	В	V _{GS} =10V, I _D =14A	-	8.4	9.2	mΩ	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =14A	-	10.0	12.0	mΩ	
Forward Transconductance	g FS	V _{DS} =5V,I _D =14A		60	-	S	
Dynamic Characteristics (Note3)							
Input Capacitance	C _{lss}	\/ 50\/\/ 0\/	-	3650	-	pF	
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V, F=1.0MHz	-	315	-	pF	
Reverse Transfer Capacitance	C _{rss}	r=1.0lvlm2	-	22	-	pF	
Switching Characteristics (Note 3)							
Turn-on Delay Time	t _{d(on)}		-	16	-	nS	
Turn-on Rise Time	t _r	V_{DD} =50 V , I_D =14 A	-	11	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =1.6 Ω	-	35	-	nS	
Turn-Off Fall Time	t _f		-	9	-	nS	
Total Gate Charge	Qg	\/ -50\/ -440	-	70	-	nC	
Gate-Source Charge	Q _{gs}	V_{DS} =50V, I_{D} =14A, V_{GS} =10V	-	14.5	-	nC	
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	16.8	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 2)	V _{SD}	V _{GS} =0V,I _S =14A	-	-	1.2	V	
Diode Forward Current	Is		-	-	14	Α	
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =14A	-	60	-	nS	
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	106	-	nC	

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 3. Guaranteed by design, not subject to production
- 4. EAS condition : Tj=25 $^{\circ}\text{C}$,V $_{DD}$ =50 V ,V $_{G}$ =10 V ,L=0.25 mH ,Rg=25 Ω

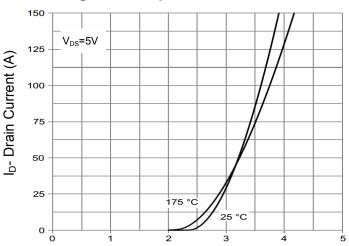


Typical Electrical and Thermal Characteristics



Vds Drain-Source Voltage (V)





Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics

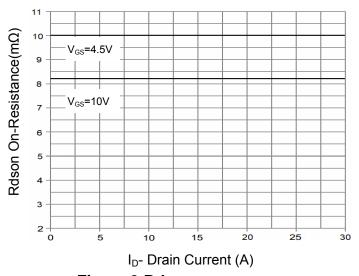


Figure 3 Rdson- Drain Current

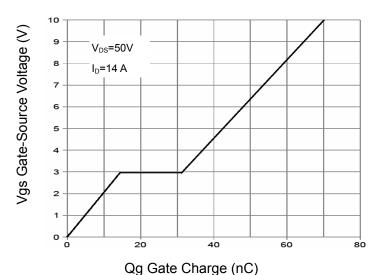


Figure 4 Gate Charge

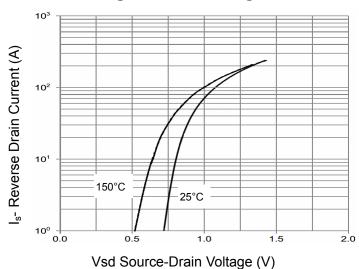


Figure 5 Source- Drain Diode Forward

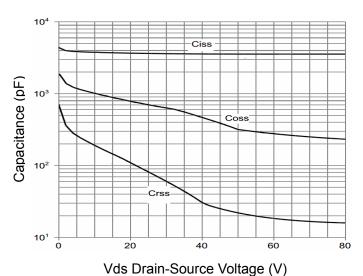


Figure 6 Capacitance vs Vds



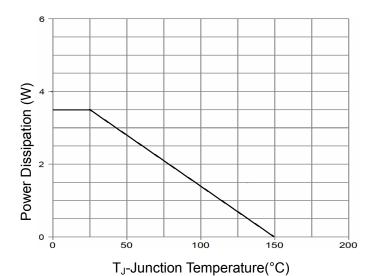
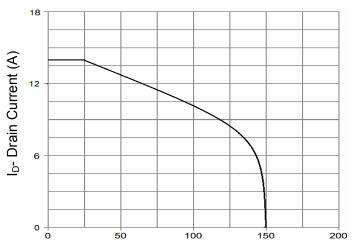


Figure 7 Power De-rating



T_J-Junction Temperature (°C) **Figure 9 Current De-rating**

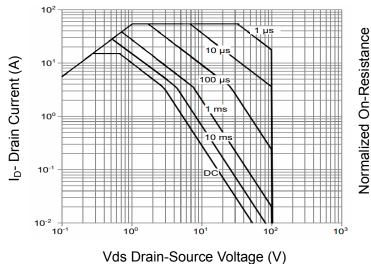


Figure 8 Safe Operation Area

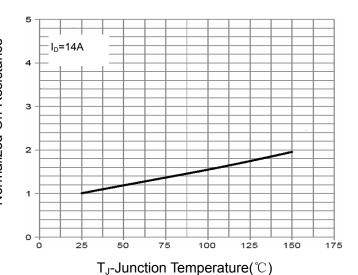
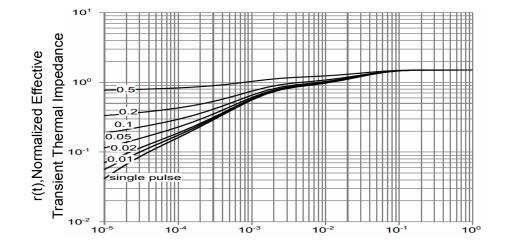


Figure 10 Rdson-Junction Temperature

V2.0

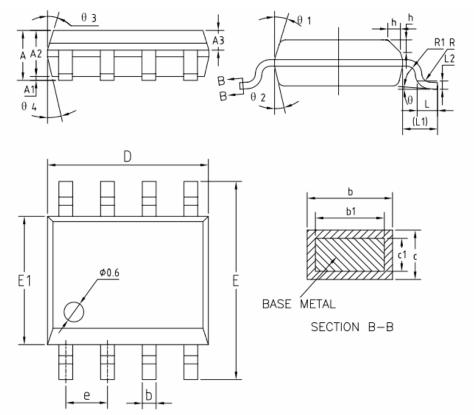


Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance



Sop-8 Package Information



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX		
Α	1.35	1.55	1.75		
A1	0.10	0.15	0.25		
A2	1.25	1.40	1.65		
A3	0.50	0.60	0.70		
b	0.38	_	0.51		
b1	0.37	0.42	0.47		
С	0.18	_	0.25		
c1	0.17 0.20		0.23		
D	4.80	4.90	5.00		
E	5.80	6.00	6.20		
E1	3.80	3.90	4.00		
е	1.17	1.27	1.37		
L	0.45	0.60	0.80		
L1		1.04REF			
L2	0.25BSC				
R	0.07	ı			
R1	0.07	_	_		
h	0.30	0.40	0.50		
θ	0.	_	8*		
θ 1	15 °	17°	19*		
θ 2	2 11'		15*		
θ 3			19°		
θ 4	11*	13°	15 °		



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DMN1017UCP3-7 EFC2J004NUZTDG P85W28HP2F-7071 DMN1053UCP4-7 NTE2384 DMC2700UDMQ-7 DMN2080UCB4-7
DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B IPS60R3K4CEAKMA1 DMN1006UCA6-7 DMN16M9UCA6-7
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DMN2990UFB-7B SSM3K35CT,L3F IPLK60R1K0PFD7ATMA1 2N7002W-G MCAC30N06Y-TP IPWS65R035CFD7AXKSA1
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