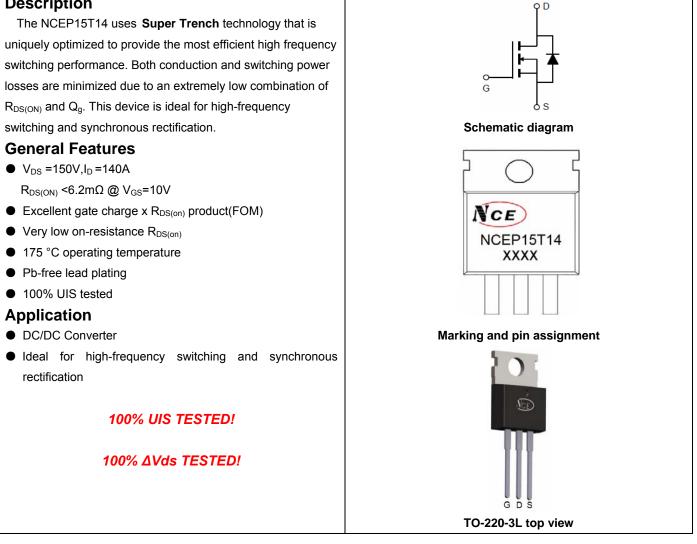


# NCE N-Channel Super Trench Power MOSFET

### Description



### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP15T14	NCEP15T14	TO-220-3L	-	-	-

### Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	150	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι <sub>D</sub>	140	А
Drain Current-Continuous(T <sub>C</sub> =100℃)	I <sub>D</sub> (100℃)	100	A
Pulsed Drain Current	I <sub>DM</sub>	440	А
Maximum Power Dissipation	PD	320	W
Derating factor		2.1	W/°C
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	1296	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C





### **Thermal Characteristic**

	Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{ extsf{ heta}JC}$	0.47	°C/W
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### Electrical Characteristics (T<sub>C</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<b>i</b>					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	150	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =150V, $V_{GS}$ =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	<b>i</b>					
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	2.0	3	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =70A	-	5.6	6.2	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =10V,I <sub>D</sub> =70A	70	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>		-	5900	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =75V, $V_{GS}$ =0V,	-	690	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	7	-	PF
Switching Characteristics (Note 4)	<b>i</b>					
Turn-on Delay Time	t <sub>d(on)</sub>		-	26	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =75V,I <sub>D</sub> =70A	-	36	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{G}$ =4.7 $\Omega$	-	47	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	15	-	nS
Total Gate Charge	Qg		-	80		nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =75V,I <sub>D</sub> =70A,	-	32		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	13		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>F</sub> = I <sub>S</sub>	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	140	А
Reverse Recovery Time	t <sub>rr</sub>	$T_J$ = 25°C, $I_F$ = $I_S$	-	140		nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	498		nC

#### Notes:

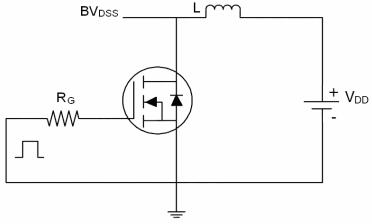
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width ≤ 300 $\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25  $^\circ \!\! \mathbb{C}$  ,V\_{DD}=50V,V\_G=10V,L=0.5mH,Rg=25\Omega



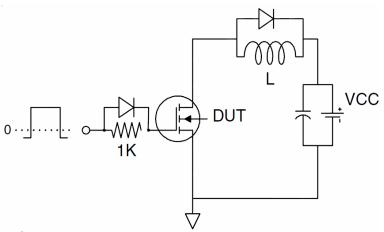
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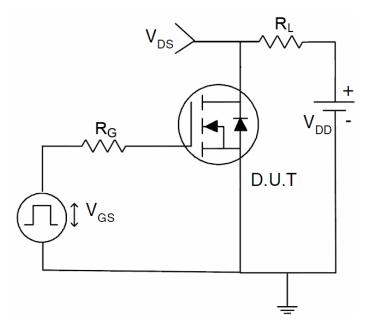
### Test Circuit 1) E<sub>AS</sub> test Circuit



### 2) Gate charge test Circuit

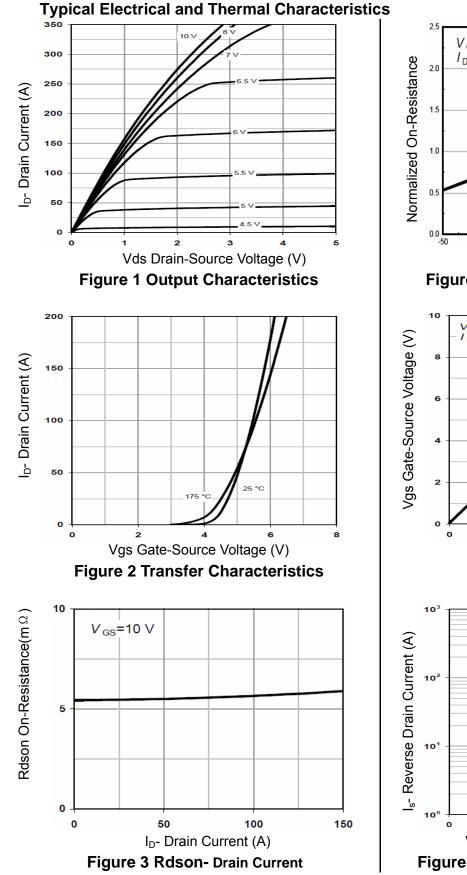


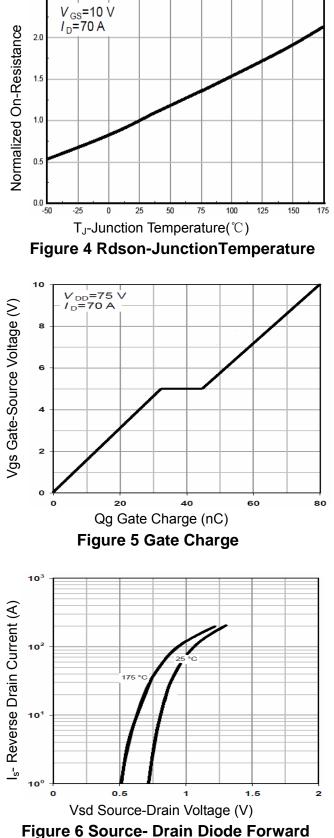
3) Switch Time Test Circuit







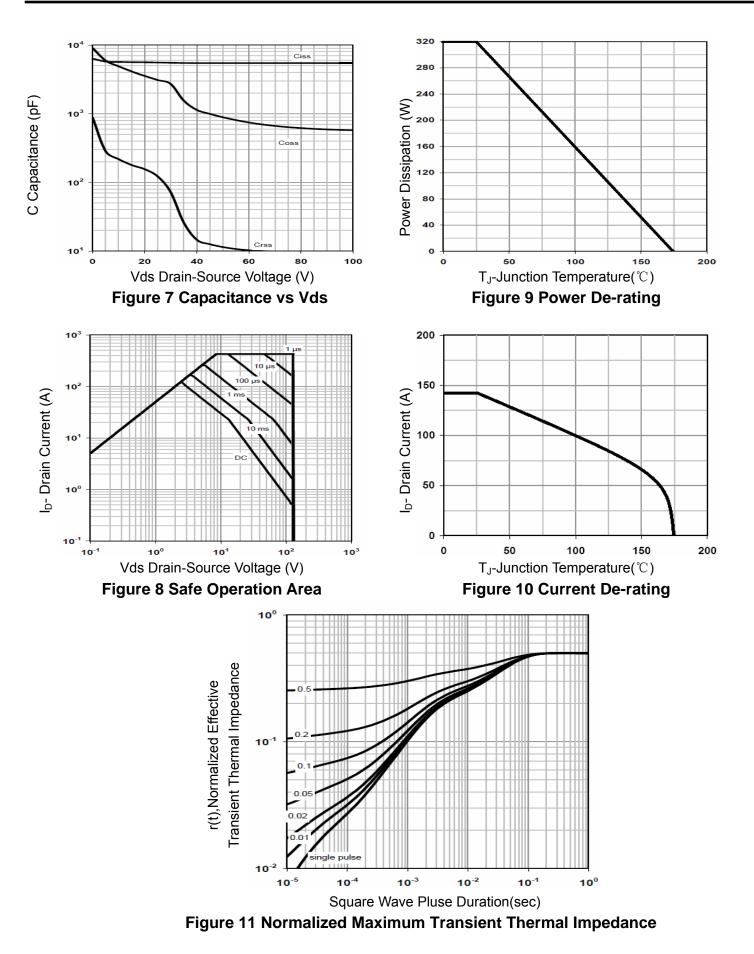






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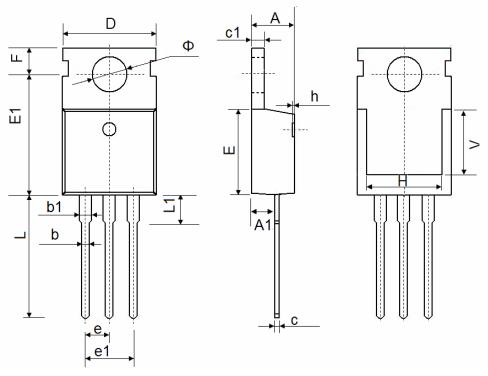




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## TO-220-3L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540 TYP.		0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.500 REF.		0.295 REF.		
Φ	3.400	3.800	0.134	0.150	





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