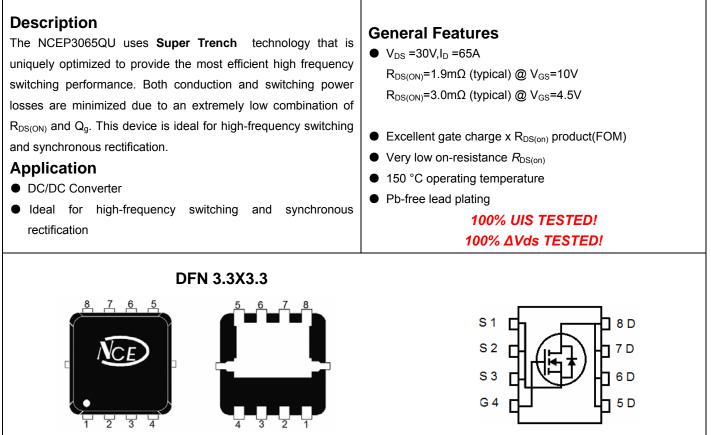


NCE N-Channel Super Trench Power MOSFET



Top View

Bottom View

Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP3065QU	NCEP3065QU	DFN3.3X3.3-8L	Ø180mm	-	5000

Absolute Maximum Ratings (T_c=25[°]C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	Ι _D	65	А
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	45.5	А
Pulsed Drain Current	I _{DM}	260	А
Maximum Power Dissipation	PD	55	W
Derating factor		0.44	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	500	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{θJC}	2.3	°C /W]
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Electrical Characteristics (T_c=25[°]C unless otherwise noted)

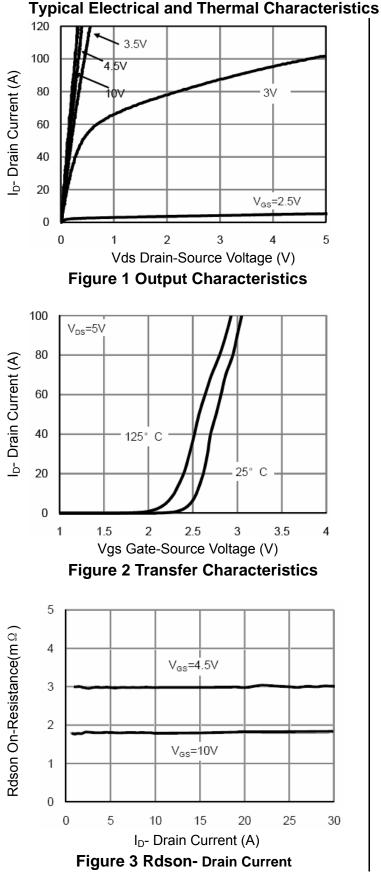
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	30		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	-	1	μA
Cata Rady Laakaga Current		V_{GS} =±5V, V_{DS} =0V	-	-	±80	nA
Gate-Body Leakage Current	I _{GSS} –	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	····					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.0	1.5	2.0	V
Drain-Source On-State Resistance	P	V_{GS} =10V, I _D =20A	1.6	1.9	2.3	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A	2.5	3.0	3.6	mΩ
Gate resistance	R _G	F=1.0MHz	-	2.0	-	Ω
Forward Transconductance	g fs	V _{DS} =5V,I _D =20A		60	-	S
Dynamic Characteristics (Note4)	····					
Input Capacitance	C _{lss}	V _{DS} =15V,V _{GS} =0V,	-	2100	2800	PF
Output Capacitance	C _{oss}		-	773	1400	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	15.5	30	PF
Switching Characteristics (Note 4)	i					
Turn-on Delay Time	t _{d(on)}		-	7.5	-	nS
Turn-on Rise Time	tr	V _{DD} =15V,I _D =20A	-	4.0	-	nS
Turn-Off Delay Time	t _{d(off)}	V _{GS} =10V,R _G =1.6Ω	-	37	-	nS
Turn-Off Fall Time	t _f		-	7.5	-	nS
Total Gate Charge	Qg	V _{DS} =15V,I _D =20A,	-	34.8	52	nC
Gate-Source Charge	Q _{gs}		-	6.2	9.3	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	5.1	7.7	nC
Drain-Source Diode Characteristics	· ·					
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	65	А
Reverse Recovery Time	t _{rr}	T_J = 25°C, I_F = I_S	-	14	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	21	-	nC

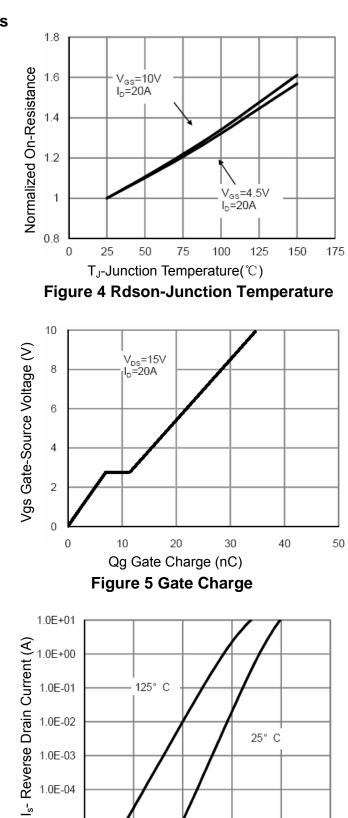
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- Surface Mounted on FR4 Board, t ≤ 10 sec. The value of R_{0JA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25° C. the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^\circ \! \mathrm{C}$,V_DD=15V,V_G=10V,L=0.5mH,Rg=25 Ω



<u>NCEP3065QU</u>





1.0E-05

0.0

0.2

0.4

Vsd Source-Drain Voltage (V) Figure 6 Source- Drain Diode Forward

0.6

0.8

1.0



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NCEP3065QU

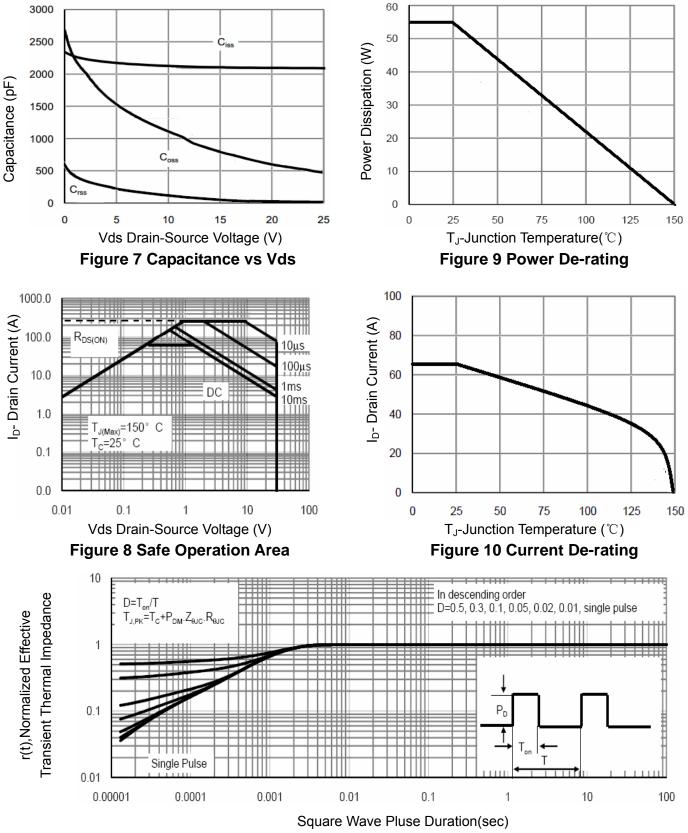
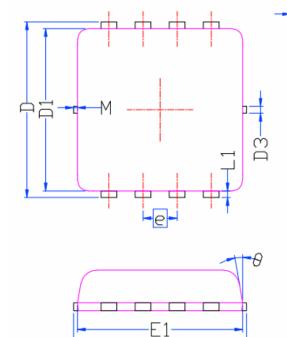


Figure 11 Normalized Maximum Transient Thermal Impedance

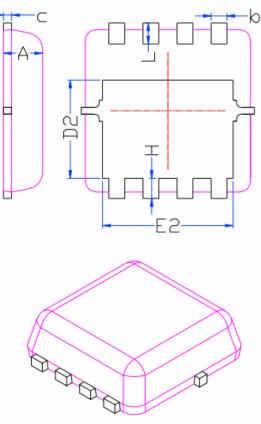


<u>NCEP3065QU</u>

DFN3.3X3.3-8L Package Information



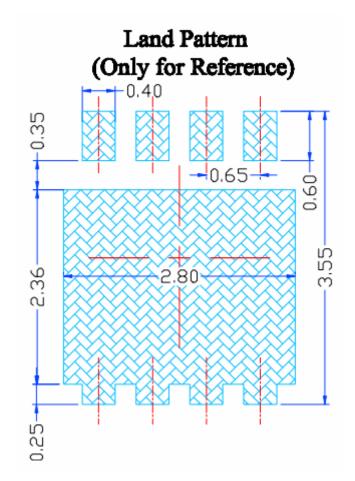
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2 mm h a l	Dimensions In Millimeters				
Symbol	Min.	Nom.	Max.		
A	0.70	0.75	0.80		
b	0.25	0.30	0.35		
с	0.10	0.15	0.25		
D	3.25	3.35	3.45		
D1	3.00	3.10	3.20		
D2	1.78	1.88	1.98		
D3	-	0.13	-		
E	3.10	3.20	3.30		
E1	3.00	3.15	3.20		
E2	2.39	2.49	2.59		
e		0.65BSC			
Н	0.30	0.39	0.50		
L	0.30	0.40	0.50		
L1	-	0.13	-		
M	*	*	0.15		
θ		10 [°]	12 [°]		

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