1. General description

The 74ABT00 is a quad 2-input NAND gate. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. Features and benefits

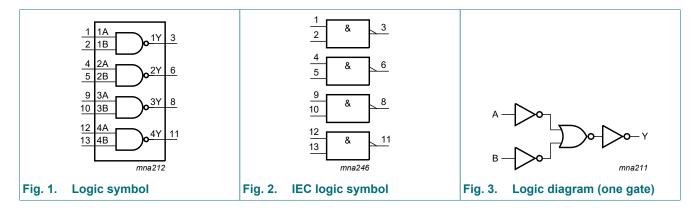
- Supply voltage range from 4.5 V to 5.5 V
- BiCMOS high speed and output drive
- Direct interface with TTL levels
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up protection exceeds 500 mA per JESD78B class II level A
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C

3. Ordering information

Table 1. Ordering information

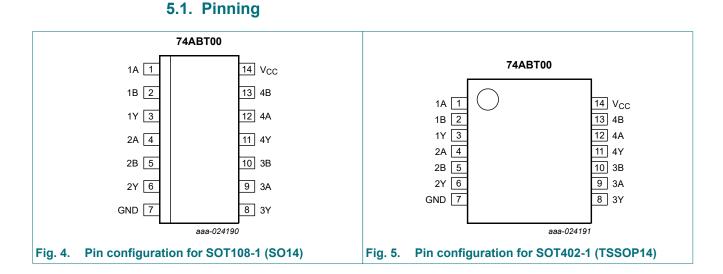
Type number	Package					
	Temperature range	Name	Description	Version		
74ABT00D	-40 °C to +85 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	SOT108-1		
74ABT00PW	-40 °C to +85 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1		

4. Functional diagram



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5. Pinning information



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A	1, 4, 9, 12	data input
1B, 2B, 3B, 4B	2, 5, 10, 13	data input
1Y, 2Y, 3Y, 4Y	3, 6, 8, 11	data output
GND	7	ground (0 V)
V _{CC}	14	supply voltage

6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care.

Input	Output	
nA	nB	nY
L	X	Н
Х	L	Н
Н	Н	L

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
VI	input voltage		[1]	-1.2	+7.0	V
Vo	output voltage	output HIGH or LOW	[1]	-0.5	+5.5	V
I _{IK}	input clamping current	V ₁ < 0 V		-18	-	mA
I _{OK}	output clamping current	V _O < 0 V		-50	-	mA
lo	output current	output in LOW-state		-	40	mA
Tj	junction temperature			-	150	°C
T _{stg}	storage temperature			-65	+150	°C

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

8. Recommended operating conditions

Table 5. Operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		4.5	-	5.5	V
VI	input voltage		0	-	V _{CC}	V
VIH	HIGH-level input voltage		2.0	-	-	V
V _{IL}	LOW-level input voltage		-	-	0.8	V
I _{OH}	HIGH-level output current		-15	-	-	mA
I _{OL}	LOW-level output current		-	-	20	mA
Δt/ΔV	input transition rise and fall rate		0	-	5	ns/V
T _{amb}	ambient temperature	in free air	-40	-	+85	°C

9. Static characteristics

Symbol	Parameter	Conditions		25 °C			-40 °C to +85 °C	
			Min	Тур	Max	Min	Max	
V _{IK}	input clamping voltage	V _{CC} = 4.5 V; I _{IK} = -18 mA	-1.2	-0.9	-	-1.2	-	V
V _{OH}	HIGH-level output voltage	V _{CC} = 4.5 V; I _{OH} = -15 mA; V _I = _{IL} or V _{IH}	2.5	2.9	-	2.5	-	V
V _{OL}	LOW-level output voltage	V_{CC} = 4.5 V; I _{OL} = 20 mA; V _I = V _{IL} or V _{IH}	-	0.35	0.5	-	0.5	V
lı	input leakage current	V _{CC} = 5.5 V; V _I = GND or 5.5 V	-	±0.01	±1.0	-	±1.0	μA
I _{OFF}	power-off leakage current	$V_{CC} = 0 \text{ V}; \text{ V}_1 \text{ or } \text{ V}_0 \le 4.5 \text{ V}$	-	±5.0	±100	-	±100	μA
I _{CEX}	output high leakage current	HIGH-state; V_0 = 5.5 V; V_{CC} = 5.5 V; V _I = GND or V _{CC}	-	5.0	50	-	50	μA
lo	output current	V _{CC} = 5.5 V; V _O = 2.5 V [7] -50	-75	-180	-50	-180	mA
I _{CC}	supply current	V_{CC} = 5.5 V; V_{I} = GND or V_{CC}	-	2	50	-	50	μA
ΔI _{CC}	additional supply current	per input pin; V _{CC} = 5.5 V; [2 one input at 3.4 V; other inputs at V _{CC} or GND	2] -	0.25	500	-	500	μA
CI	input capacitance	$V_{I} = 0 V \text{ or } V_{CC}$	-	3	-	-	-	pF

[1] Not more than one output should be tested at a time, and the duration of the test should not exceed one second.

[2] This is the increase in supply current for each input at 3.4 V.

10. Dynamic characteristics

Table 7. Dynamic characteristics

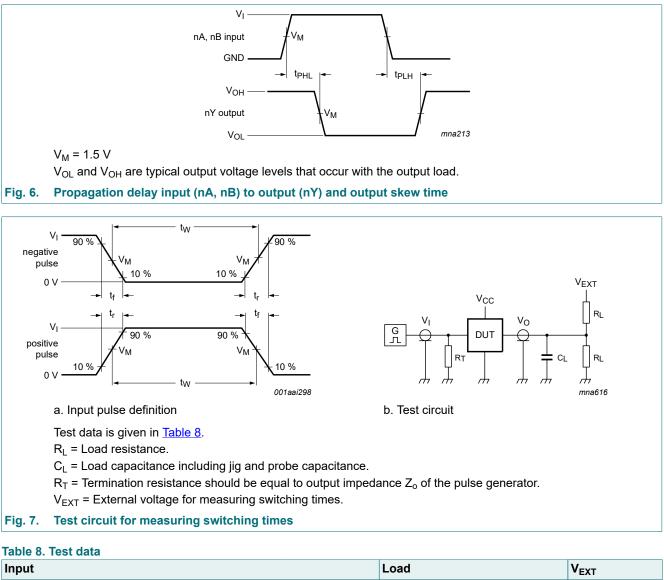
GND = 0 V; for test circuit, see Fig. 7.

Symbol	Parameter	Conditions	Conditions 25 °C; $V_{CC} = 5.0 V$ -40 °C to +85 °C $V_{CC} = 5.0 V \pm 0.5$		25 °C; V _{CC} = 5.0 V			Unit
			Min	Тур	Max	Min	Max	
t _{PLH}	LOW to HIGH propagation delay	nA, nB to nY; see <u>Fig. 6</u>	1.0	2.5	3.6	1.0	4.1	ns
t _{PHL}	HIGH to LOW propagation delay	nA, nB to nY; see <u>Fig. 6</u>	1.0	2.0	2.8	1.0	3.4	ns
t _{sk(o)}	output skew time	[1]	-	0.4	0.5	-	0.5	ns

[1] Skew between any two outputs of the same package switching in the same direction. This parameter is guaranteed by design.

Quad 2-input NAND gate

10.1. Waveforms and test circuit



Input			Load	V _{EXT}		
VI	f _i	t _W	t _r , t _f	CL	RL	t _{PHL} , t _{PLH}
3.0 V	1 MHz	500 ns	≤ 2.5 ns	50 pF	500 Ω	open

11. Package outline

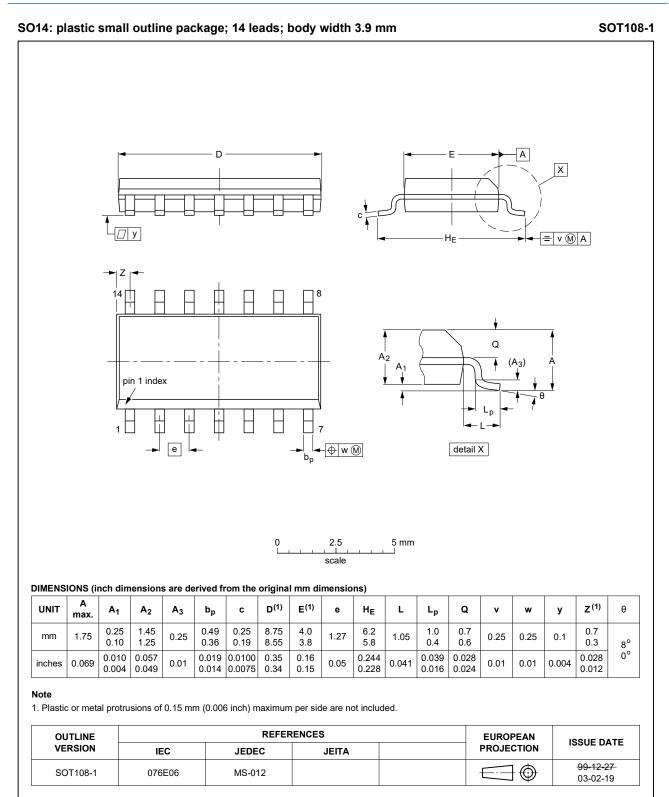


Fig. 8. Package outline SOT108-1 (SO14)

Product data sheet

Quad 2-input NAND gate

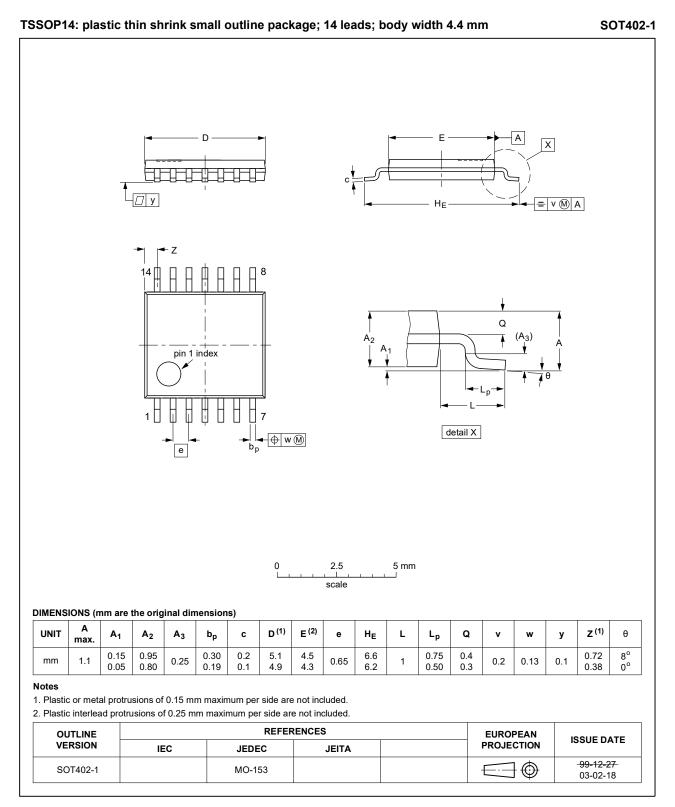


Fig. 9. Package outline SOT402-1 (TSSOP14)

⁷⁴ABT00

12. Abbreviations

Table 9. Abbreviations				
Acronym	Description			
BiCMOS	Bipolar Complementary Metal-Oxide Semiconductor			
DUT	Device Under Test			
ESD	ElectroStatic Discharge			
HBM	Human Body Model			
MM	Machine Model			
TTL	Transistor-Transistor Logic			

13. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
74ABT00 v.4	20201005	Product data sheet	-	74ABT00 v.3			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. <u>Section 1</u> and <u>Section 2</u> updated. Type number 74ABT00DB (SOT337-1 / SSOP14) removed. 						
74ABT00 v.3	20160811	Product data sheet	-	74ABT00 v.2			
Modifications:	guidelines o	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. 					
74ABT00 v.2	19950918	Product specification	-	-			

Quad 2-input NAND gate

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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