Dual retriggerable monostable multivibrator with resetRev. 5 — 17 June 2020Product data sheet

### 1. General description

The 74AHC123A; 74AHCT123A is a dual retriggerable monostable multivibrator with reset. The basic output pulse width is programmed by selection of external components ( $R_{EXT}$  and  $C_{EXT}$ ). Once triggered this basic pulse width may be extended by retriggering either of the edge triggered inputs ( $n\overline{A}$  or (nB). By repeating this process, the output pulse period (nQ = HIGH,  $n\overline{Q} = LOW$ ) can be made as long as desired. Alternatively, an output delay can be terminated at any time by a LOW-going edge on input  $n\overline{R}D$ . Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

### 2. Features and benefits

- Wide supply voltage range from 2.0 V to 5.5 V
- · DC triggered from active HIGH or active LOW inputs
- Retriggerable for very long pulses up to 100 % duty factor
- Direct reset terminates output pulse
- Overvoltage tolerant inputs to 5.5 V
- All inputs have a Schmitt-trigger action
- High noise immunity
- Input levels:
  - For 74AHC123A: CMOS level
  - For 74AHCT123A: TTL level
- ESD protection:
  - HBM JESD22-A114E exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101C exceeds 1000 V
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level A
- Multiple package options
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

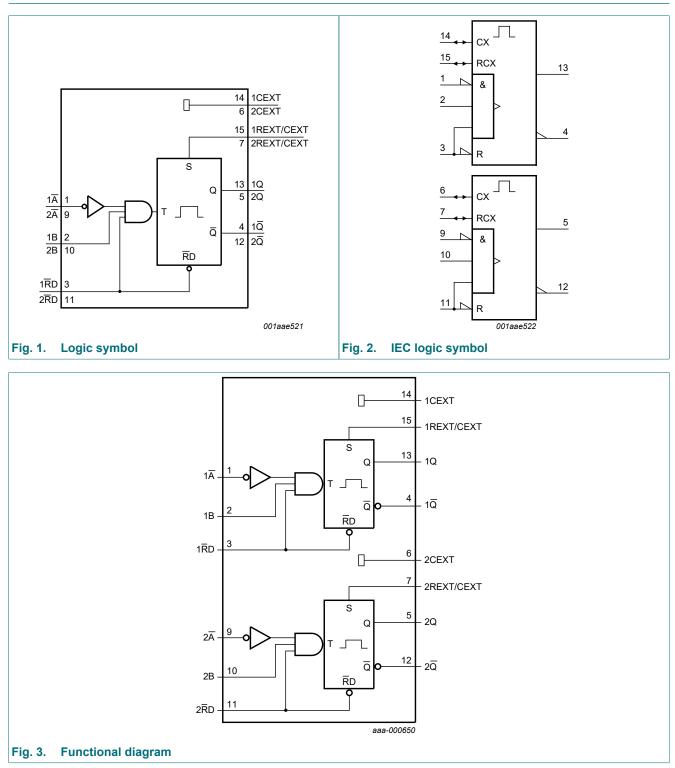
### 3. Ordering information

#### Table 1. Ordering information

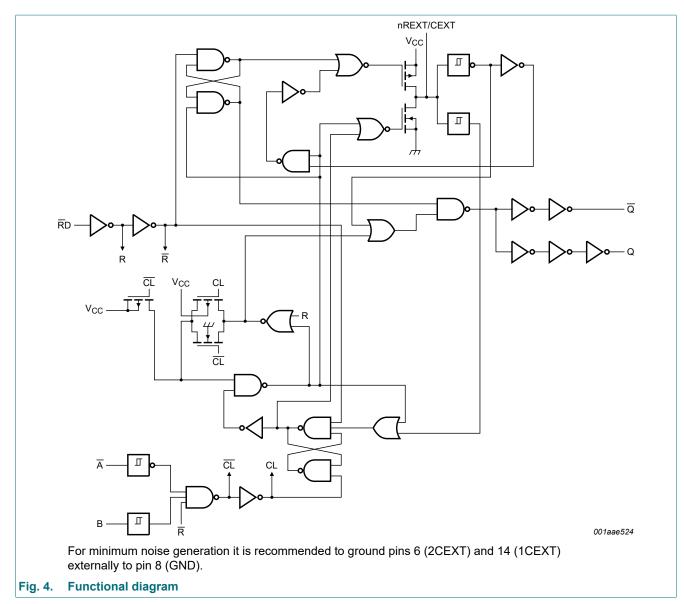
Type number	Package			
	Temperature range	Name	Description	Version
74AHC123AD	-40 °C to +125 °C	SO16	plastic small outline package; 16 leads;	SOT109-1
74AHCT123AD	-		body width 3.9 mm	
74AHC123APW	-40 °C to +125 °C	TSSOP16	plastic thin shrink small outline package; 16 leads;	SOT403-1
74AHCT123APW	-		body width 4.4 mm	
74AHC123ABQ	-40 °C to +125 °C	DHVQFN16	plastic dual in-line compatible thermal enhanced	SOT763-1
74AHCT123ABQ			very thin quad flat package; no leads; 16 terminals; body 2.5 × 3.5 × 0.85 mm	

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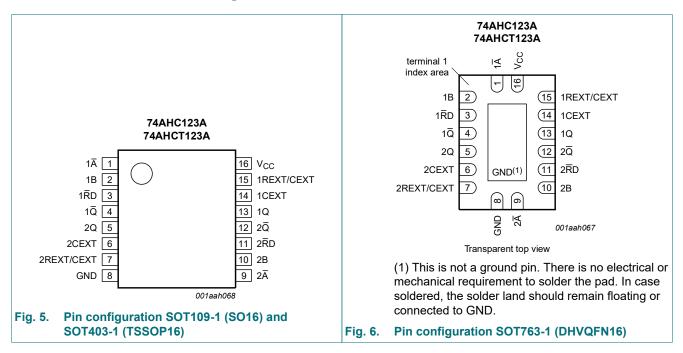
### 4. Functional diagram



### Dual retriggerable monostable multivibrator with reset



### 5. Pinning information



### 5.1. Pinning

### 5.2. Pin description

Symbol	Pin	Description
1Ā	1	negative-edge triggered input 1
1B	2	positive-edge triggered input 1
1RD	3	direct reset LOW and positive-edge triggered input 1
1 <del>Q</del>	4	active LOW output 1
2Q	5	active HIGH output 2
2CEXT	6	external capacitor connection 2
2REXT/CEXT	7	external resistor and capacitor connection 2
GND	8	ground (0 V)
2⊼	9	negative-edge triggered input 2
2B	10	positive-edge triggered input 2
2RD	11	direct reset LOW and positive-edge triggered input 2
2 <del>Q</del>	12	active LOW output 2
1Q	13	active HIGH output 1
1CEXT	14	external capacitor connection 1
1REXT/CEXT	15	external resistor and capacitor connection 1
V <sub>CC</sub>	16	supply voltage

### Table 2 Pin description

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### 6. Functional description

#### Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care;

 $\uparrow$  = LOW-to-HIGH transition;

↓ = HIGH-to-LOW transition;

 $\Pi$  = one HIGH level output pulse;

└ = one LOW level output pulse.

Input			Output					
nRD	nĀ	nB	nQ	nQ				
L	Х	Х	L	Н				
Х	Н	Х	L [1]	H [1]				
Х	Х	L	L [1]	H [1]				
Н	L	↑	Л	U				
Н	Ļ	Н	Л	U				
1	L	Н	Л	U				

[1] If the monostable multivibrator was triggered before this condition was established, the pulse will continue as programmed.

## 7. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < -0.5 V [1]	-20	-	mA
I <sub>OK</sub>	output clamping current	$V_{\rm O} < -0.5 \text{ V or } V_{\rm O} > V_{\rm CC} + 0.5 \text{ V}$ [1]	-	±20	mA
I <sub>O</sub>	output current	$V_{\rm O}$ = -0.5 V to (V <sub>CC</sub> + 0.5 V)	-	±25	mA
I <sub>CC</sub>	supply current		-	75	mA
I <sub>GND</sub>	ground current		-75	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C [2]	-	500	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT109-1 (SO16) package: P<sub>tot</sub> derates linearly with 12.4 mW/K above 110 °C.

For SOT403-1 (TSSOP16) package: Ptot derates linearly with 8.5 mW/K above 91 °C.

For SOT763-1 (DHVQFN16) package: Ptot derates linearly with 11.2 mW/K above 106 °C.

### 8. Recommended operating conditions

#### Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	7	4AHC123	BA	74	Unit		
			Min	Тур	Мах	Min	Тур	Max	
V <sub>CC</sub>	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	0	-	5.5	V
Vo	output voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and	V <sub>CC</sub> = 3.3 V ± 0.3 V	-	-	100	-	-	-	ns/V
	fall rate	V <sub>CC</sub> = 5.0 V ± 0.5 V	-	-	20	-	-	20	ns/V

### 9. Static characteristics

#### **Table 6. Static characteristics**

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C			°C to 5 °C	-	°C to 5 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
74AHC1	23A	J		<b></b>						_
V <sub>IH</sub>	HIGH-level	V <sub>CC</sub> = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
	input voltage	V <sub>CC</sub> = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V <sub>CC</sub> = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V <sub>IL</sub>	LOW-level	V <sub>CC</sub> = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V <sub>CC</sub> = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V <sub>CC</sub> = 5.5 V	-	-	1.65	-	1.65	-	1.65	V
V <sub>OH</sub>	HIGH-level	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>								
	output voltage	I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I <sub>O</sub> = -8.0 mA; V <sub>CC</sub> = 4.5 V	3.94	-	-	3.8	-	3.70	-	V
V <sub>OL</sub>	LOW-level	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>								_
	output voltage	I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I <sub>O</sub> = 8.0 mA; V <sub>CC</sub> = 4.5 V	-	-	0.36	-	0.44	-	0.55	V

### Dual retriggerable monostable multivibrator with reset

Symbol	Parameter	Conditions			25 °C			°C to 5 °C		°C to 25 °C	Unit
			F	Min	Тур	Max	Min	Max	Min	Мах	1
lı	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V									
		nREXT/CEXT	[1]	-	-	±0.25	-	±2.5	-	±10.0	μA
		pins n $\overline{A}$ , nB, n $\overline{R}D$		-	-	±0.1	-	±1.0	-	±2.0	μA
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V		-	-	4.0	-	40	-	80	μA
		active state (per circuit); V <sub>I</sub> = V <sub>CC</sub> or GND	[1]								
		V <sub>CC</sub> = 3.0 V		-	160	250	-	280	-	280	μA
		V <sub>CC</sub> = 4.5 V		-	380	500	-	650	-	650	μA
		V <sub>CC</sub> = 5.5 V		-	560	750	-	975	-	975	μA
CI	input capacitance			-	5.0	10	-	10	-	10	pF
Co	output capacitance			-	4.0	-	-	-	-	-	pF
74AHCT	123A										
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 4.5 V to 5.5 V		2.0	-	-	2.0	-	2.0	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 4.5 V to 5.5 V		-	-	0.8	-	0.8	-	0.8	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$									
	output voltage	I <sub>O</sub> = -50 μA		4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -8.0 mA		3.94	-	-	3.8	-	3.70	-	V
V <sub>OL</sub>	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$									
	output voltage	I <sub>O</sub> = 50 μA		-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 8.0 mA		-	-	0.36	-	0.44	-	0.55	V
lı	input leakage current	nREXT/CEXT; V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	[1]	-	-	±0.25	-	±2.5	-	±10.0	μA
		pins n $\overline{A}$ , nB, n $\overline{R}$ D; V <sub>1</sub> = V <sub>CC</sub> or GND; V <sub>CC</sub> = 5.5 V		-	-	±0.1	-	±1.0	-	±2.0	μA
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V		-	-	4.0	-	40	-	80	μA
		active state (per circuit); $V_I = V_{CC}$ or GND	[1]								
		V <sub>CC</sub> = 4.5 V		-	380	500	-	650	-	650	μA
		V <sub>CC</sub> = 5.5 V		-	560	750	-	975	-	975	μA
CI	input capacitance			-	3	10	-	10	-	10	pF
Co	output capacitance			-	4.0	-	-	-	-	-	pF

[1] Voltage on nREXT/CEXT =  $0.5 \times V_{CC}$  and pin nREXT/CEXT in OFF-state during test.

# **10.** Dynamic characteristics

#### Table 7. Dynamic characteristics

GND = 0 V; For test circuit see Fig. 12.

Symbol	Parameter	Conditions		25 °C			°C to 5 °C		°C to 5 °C	Unit
			Min	Typ[1]	Мах	Min	Max	Min	Max	
74AHC1	23A									
t <sub>pd</sub>	propagation	$n\overline{A}$ and $nB$ to $nQ$ and $n\overline{Q}$ ; see <u>Fig. 7</u> [2]								
	delay	$V_{CC}$ = 3.0 V to 3.6 V; C <sub>L</sub> = 15 pF	-	7.4	20.6	1.0	24.0	1.0	26.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V; C <sub>L</sub> = 50 pF	-	10.5	24.1	1.0	27.5	1.0	30.0	ns
		$V_{CC}$ = 4.5 V to 5.5 V; C <sub>L</sub> = 15 pF	-	5.1	12.0	1.0	14.0	1.0	15.5	ns
		$V_{CC}$ = 4.5 V to 5.5 V; C <sub>L</sub> = 50 pF	-	7.3	14.0	1.0	16.0	1.0	17.5	ns
		$\overline{nRD}$ to $nQ$ and $\overline{nQ}$ ; see <u>Fig. 7</u> [2]								
		V <sub>CC</sub> = 3.0 V to 3.6 V; C <sub>L</sub> = 15 pF	-	8.2	22.4	1.0	26.0	1.0	28.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V; C <sub>L</sub> = 50 pF	-	11.7	25.9	1.0	29.5	1.0	32.0	ns
		$V_{CC}$ = 4.5 V to 5.5 V; C <sub>L</sub> = 15 pF	-	5.6	12.9	1.0	15.0	1.0	16.5	ns
		$V_{CC}$ = 4.5 V to 5.5 V; C <sub>L</sub> = 50 pF	-	8.1	14.9	1.0	17.0	1.0	19.0	ns
		$n\overline{R}D$ to $nQ$ and $n\overline{Q}$ (reset); see <u>Fig. 7</u> [2]								
		V <sub>CC</sub> = 3.0 V to 3.6 V; C <sub>L</sub> = 15 pF	-	6.4	15.8	1.0	18.5	1.0	20.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V; C <sub>L</sub> = 50 pF	-	9.2	19.3	1.0	22.0	1.0	24.5	ns
		$V_{CC}$ = 4.5 V to 5.5 V; C <sub>L</sub> = 15 pF	-	4.4	9.4	1.0	11.0	1.0	12.0	ns
		$V_{CC}$ = 4.5 V to 5.5 V; C <sub>L</sub> = 50 pF	-	6.3	11.4	1.0	13.0	1.0	14.5	ns
t <sub>W</sub>	pulse width	inputs; $n\overline{A} = LOW$ ; see <u>Fig. 7</u>								
		V <sub>CC</sub> = 3.0 V to 3.6 V	5.0	-	-	5.0	-	5.0	-	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	5.0	-	-	5.0	-	5.0	-	ns
		inputs; nB = HIGH; see <u>Fig. 7</u>								
		V <sub>CC</sub> = 3.0 V to 3.6 V	5.0	-	-	5.0	-	5.0	-	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	5.0	-	-	5.0	-	5.0	-	ns
		inputs; $n\overline{R}D = LOW$ ; see Fig. 7								
		V <sub>CC</sub> = 3.0 V to 3.6 V	5.0	-	-	5.0	-	5.0	-	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	5.0	-	-	5.0	-	5.0	-	ns
		outputs; $n\overline{Q}$ = LOW and [3] $nQ$ = HIGH; $C_L$ = 50 pF; see Fig. 7, Fig. 8, Fig. 9 and Fig. 10								
		C <sub>EXT</sub> = 28 pF; R <sub>EXT</sub> = 2 kΩ								
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	115	240	-	300	-	300	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	100	200	-	240	-	240	ns
		C <sub>EXT</sub> = 0.01 μF; R <sub>EXT</sub> = 10 kΩ								
		V <sub>CC</sub> = 3.0 V to 3.6 V	90	100	110	90	110	85	115	μs
		V <sub>CC</sub> = 4.5 V to 5.5 V	90	100	110	90	110	85	115	µs
		C <sub>EXT</sub> = 0.1 μF; R <sub>EXT</sub> = 10 kΩ;								
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	0.9	1	1.1	0.9	1.1	0.85	1.15	ms
		V <sub>CC</sub> = 4.5 V to 5.5 V	0.9	1	1.1	0.9	1.1	0.85	1.15	ms

### Dual retriggerable monostable multivibrator with reset

Symbol	Parameter	Conditions		25 °C			°C to 5 °C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Мах	-
t <sub>rtrig</sub>	retrigger time	$n\overline{A}$ to nB; C <sub>EXT</sub> = 100 pF; R <sub>EXT</sub> = 1 k $\Omega$ ; C <sub>L</sub> = 50 pF; see Fig. 8 and Fig. 10								
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	60	-	-	-	-	-	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	39	-	-	-	-	-	ns
		$n\overline{A}$ to nB; C <sub>EXT</sub> = 0.01 µF; R <sub>EXT</sub> = 1 k $\Omega$ ; C <sub>L</sub> = 50 pF; see Fig. 8 and Fig. 10								_
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	1.5	-	-	-	-	-	μs
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	1.2	-	-	-	-	-	μs
C <sub>PD</sub>	power dissipation capacitance	$C_{L} = 50 \text{ pF}; f_{i} = 1 \text{ MHz}; \qquad [4]$ V <sub>I</sub> = GND to V <sub>CC</sub>	-	57	-	-	-	-	-	pF
74AHCT	123A	1	-							
t <sub>pd</sub>	propagation	$n\overline{A}$ and $nB$ to $nQ$ and $n\overline{Q}$ ; see Fig. 7 [2]								
	delay	V <sub>CC</sub> = 4.5 V to 5.5 V; C <sub>L</sub> = 15 pF	-	5.0	12.0	1.0	14.0	1.0	15.5	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V; C <sub>L</sub> = 50 pF	-	7.1	14.0	1.0	16.0	1.0	17.5	ns
		nRD to nQ and nQ; see Fig. 7 [2]								
		V <sub>CC</sub> = 4.5 V to 5.5 V; C <sub>L</sub> = 15 pF	-	5.2	12.9	1.0	15.0	1.0	16.5	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V; C <sub>L</sub> = 50 pF	-	7.5	14.9	1.0	17.0	1.0	18.5	ns
		$\overline{nRD}$ to $nQ$ and $\overline{nQ}$ (reset); see <u>Fig. 7</u> [2]								
		V <sub>CC</sub> = 4.5 V to 5.5 V; C <sub>L</sub> = 15 pF	-	4.7	9.4	1.0	11.0	1.0	12.0	ns
		$V_{CC}$ = 4.5 V to 5.5 V; C <sub>L</sub> = 50 pF	-	6.7	11.4	1.0	13.0	1.0	14.5	ns
t <sub>W</sub>	pulse width	inputs; $n\overline{A}$ = LOW; C <sub>L</sub> = 50 pF; see <u>Fig. 7</u>								
		V <sub>CC</sub> = 4.5 V to 5.5 V	5.0	-	-	5.0	-	5.0	-	ns
		inputs; nB = HIGH; C <sub>L</sub> = 50 pF; see <u>Fig. 7</u>								
		V <sub>CC</sub> = 4.5 V to 5.5 V	5.0	-	-	5.0	-	5.0	-	ns
		inputs; nRD = LOW; C <sub>L</sub> = 50 pF; see <u>Fig. 7</u>								
		V <sub>CC</sub> = 4.5 V to 5.5 V	5.0	-	-	5.0	-	5.0	-	ns
		outputs; $n\overline{Q}$ = LOW and[3] $nQ$ = HIGH; $C_L$ = 50 pF;[3] $C_{EXT}$ = 28 pF; $R_{EXT}$ = 2 k $\Omega$ ;see Fig. 7, Fig. 8, Fig. 9 and Fig. 10								
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	100	200	-	240	-	240	ns
		C <sub>EXT</sub> = 0.01 μF; R <sub>EXT</sub> = 10 kΩ								
		V <sub>CC</sub> = 4.5 V to 5.5 V	90	100	110	90	110	85	115	μs
		C <sub>EXT</sub> = 0.1 μF; R <sub>EXT</sub> = 10 kΩ								
		V <sub>CC</sub> = 4.5 V to 5.5 V	0.9	1	1.1	0.9	1.1	0.85	1.15	ms

#### Dual retriggerable monostable multivibrator with reset

Symbol	Parameter	Conditions		25 °C			°C to 5 °C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	1
t <sub>rtrig</sub>	retrigger time	$n\overline{A}$ to nB; C <sub>EXT</sub> = 100 pF; R <sub>EXT</sub> = 1 k $\Omega$ ; C <sub>L</sub> = 50 pF; see <u>Fig. 8</u> and <u>Fig. 10</u>								
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	60	-	-	-	-	-	ns
		$n\overline{A}$ to nB; C <sub>EXT</sub> = 0.01 µF; R <sub>EXT</sub> = 1 k $\Omega$ ; C <sub>L</sub> = 50 pF; see <u>Fig. 8</u> and <u>Fig. 10</u>								
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	1.5	-	-	-	-	-	μs
C <sub>PD</sub>	power dissipation capacitance	$\label{eq:CL} \begin{array}{l} C_L = 50 \text{ pF};  \text{f}_\text{i} = 1 \text{ MHz}; \\ \text{V}_\text{I} = \text{GND to } \text{V}_\text{CC} \end{array} \tag{4}$	-	58	-	-	-	-	-	pF
Externa	l components									
R <sub>EXT</sub>	external	V <sub>CC</sub> = 2.0 V	5	-	-	-	-	-	-	kΩ
	resistance	V <sub>CC</sub> > 3.0 V	1	-	-	-	-	-	-	kΩ
C <sub>EXT</sub>	external	V <sub>CC</sub> = 2.0 V [5	-	-	-	-	-	-	-	pF
	capacitance	V <sub>CC</sub> > 3.0 V [5]	-	-	-	-	-	-	-	pF

Typical values are measured at nominal supply voltage ( $V_{CC}$  = 3.3 V and  $V_{CC}$  = 5.0 V). [1]

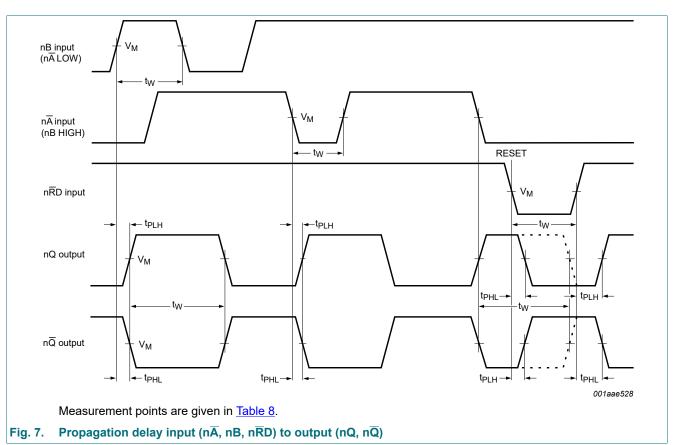
[1] Typical values are inclusived at nonlinear suppry voltage (v<sub>CC</sub> = 0.0 v and v<sub>CC</sub> = 0.0 v)
[2] t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>; C<sub>EXT</sub> = 0 pF; R<sub>EXT</sub> = 5 kΩ.
[3] For C<sub>EXT</sub> ≥ 10 nF the typical value of the pulse width t<sub>W</sub> (µs) = C<sub>EXT</sub> (nF) × R<sub>EXT</sub> (kΩ).
[4] C<sub>PD</sub> is used to determine the dynamic power dissipation P<sub>D</sub> (µW). P<sub>D</sub> = C<sub>PD</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>i</sub> + Σ(C<sub>L</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>o</sub>) where:
f = insut fermion Multiple

- - $f_i$  = input frequency in MHz;
  - f<sub>o</sub> = output frequency in MHz;
  - $C_L$  = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V.

[5] C<sub>EXT</sub> has no limits.

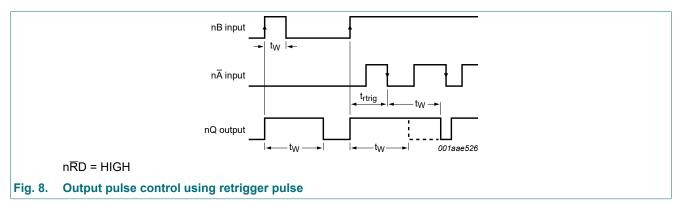
74AHC\_AHCT123A



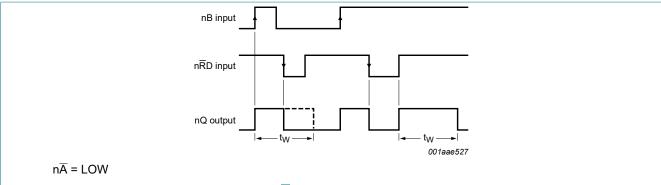
### 10.1. Waveforms

#### **Table 8. Measurement points**

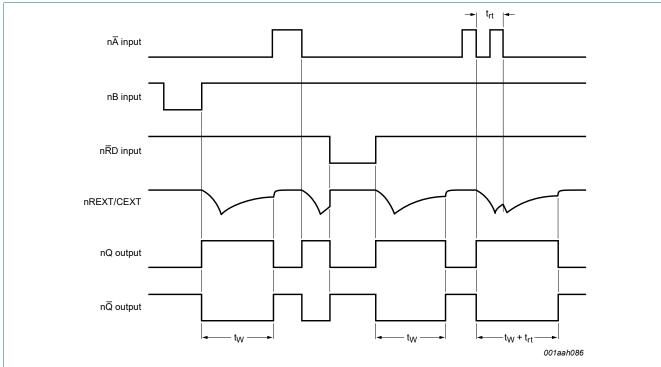
Туре	Input	Output
	V <sub>M</sub>	V <sub>M</sub>
74AHC123A	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>
74AHCT123A	1.5 V	0.5V <sub>CC</sub>



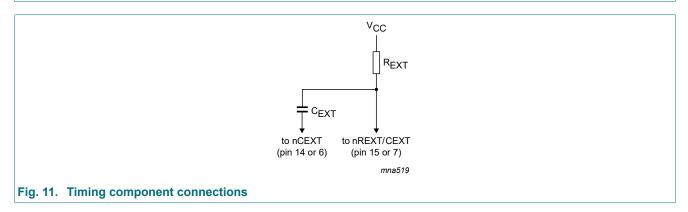
### Dual retriggerable monostable multivibrator with reset



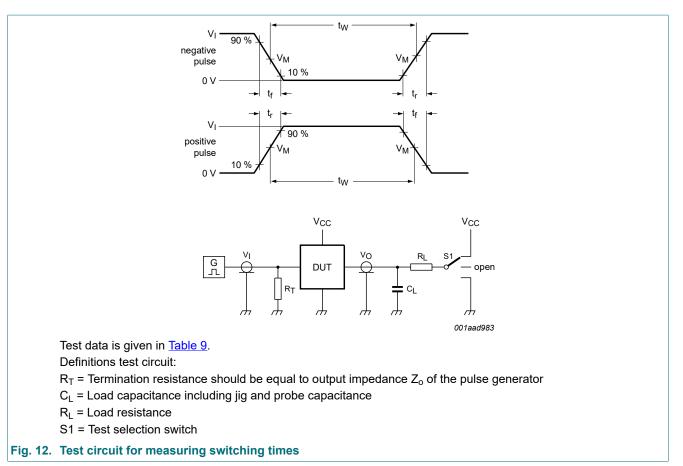








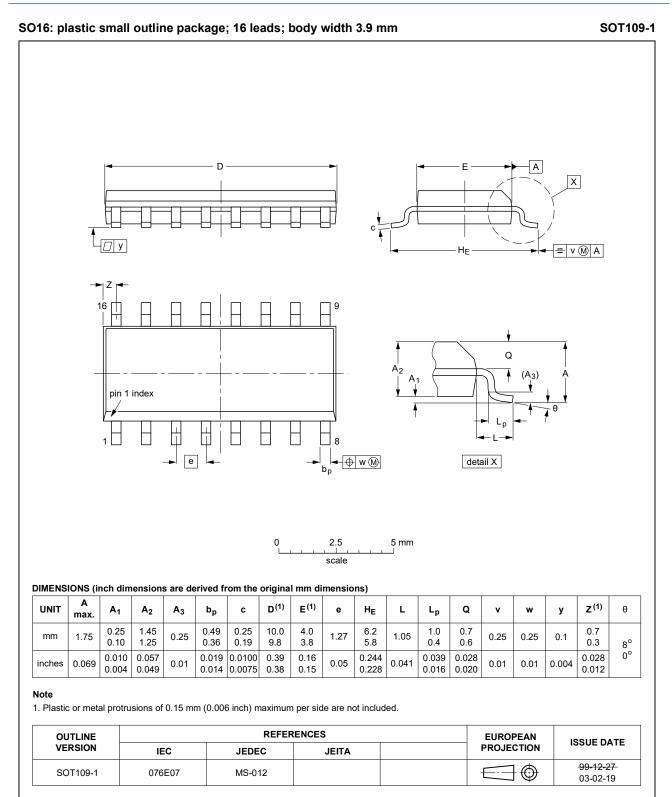
### Dual retriggerable monostable multivibrator with reset



#### Table 9. Test data

Туре	Input		Load		S1 position			
	VI	t <sub>r</sub> , t <sub>f</sub>	CL	RL	t <sub>PHL</sub> , t <sub>PLH</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>	
74AHC123A	V <sub>CC</sub>	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V <sub>CC</sub>	
74AHCT123A	3.0 V	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V <sub>CC</sub>	

### **11. Package outline**



#### Fig. 13. Package outline SOT109-1 (SO16)

74AHC\_AHCT123A

### Dual retriggerable monostable multivibrator with reset

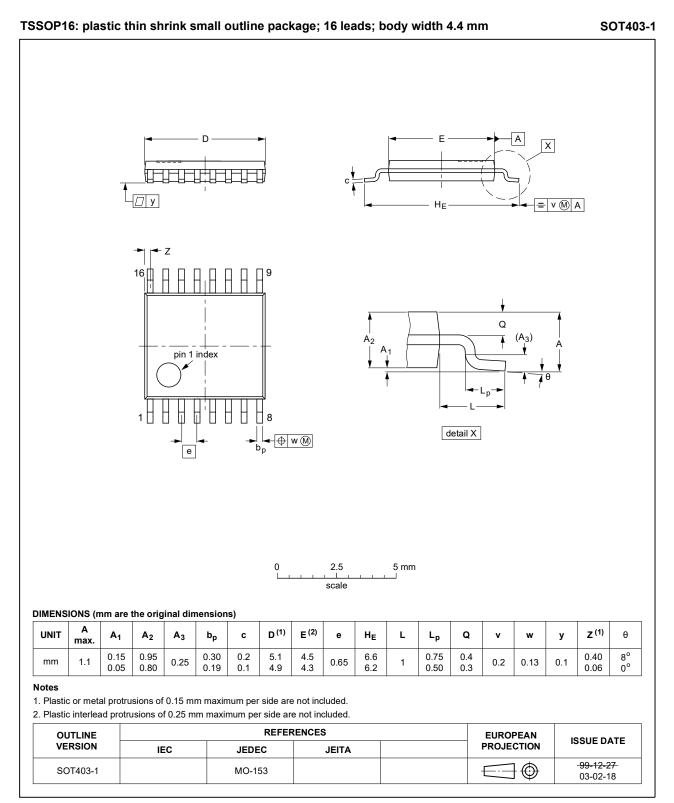


Fig. 14. Package outline SOT403-1 (TSSOP16)

### Dual retriggerable monostable multivibrator with reset

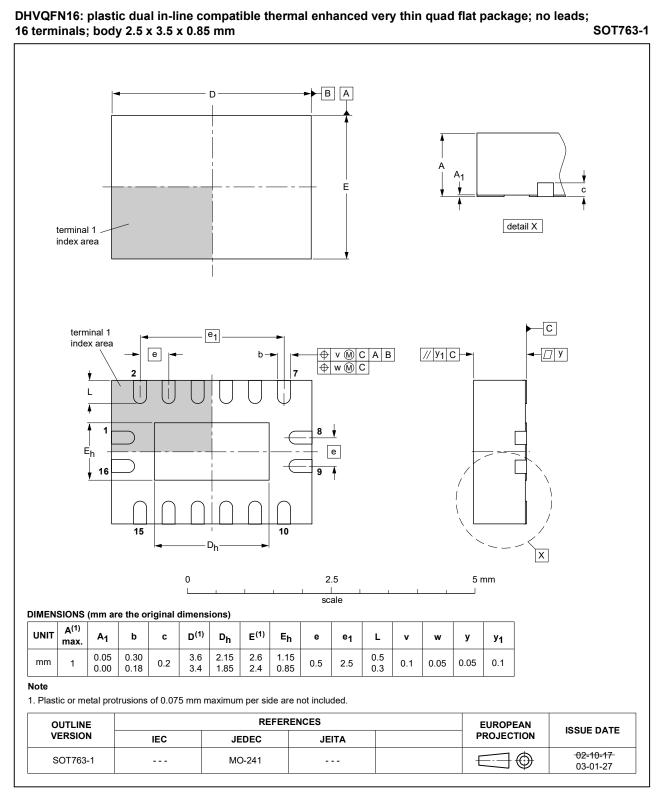


Fig. 15. Package outline SOT763-1 (DHVQFN16)

# 12. Abbreviations

Acronym	Description
CDM	Charged-Device Model
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

### 13. Revision history

### Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AHC_AHCT123A v.5	20200617	Product data sheet	-	74AHC_AHCT123A v.4	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li><u>Section 1</u> and <u>Section 2</u> updated.</li> <li><u>Table 4</u>: Derating values for P<sub>tot</sub> total power dissipation updated.</li> </ul>				
74AHC_AHCT123A v.4	20111108	Product data sheet	-	74AHC_AHCT123A v.3	
Modifications:	Legal pages updated.				
74AHC_AHCT123A v.3	20110908	Product data sheet	-	74AHC_AHCT123A v.2	
74AHC_AHCT123A v.2	20080118	Product data sheet	-	74AHC_AHCT123A v.1	
74AHC_AHCT123A v.1	20000315	Product specification	-	-	

# 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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