

74HC160

Presettable synchronous BCD decade counter;
asynchronous reset

Rev. 4 — 27 March 2019

Product data sheet

1. General description

The 74HC160 is a synchronous presettable decade counter with an internal look-ahead carry. Synchronous operation is provided by having all flip-flops clocked simultaneously on the positive-going edge of the clock (CP). The outputs (Q0 to Q3) of the counters may be preset HIGH or LOW. A LOW at the parallel enable input (PE) disables the counting action and causes the data at the data inputs (D0 to D3) to be loaded into the counter on the positive-going edge of the clock. Preset takes place regardless of the levels at count enable inputs (CEP and CET). A LOW at the master reset input (MR) sets Q0 to Q3 LOW regardless of the levels at input pins CP, PE, CET and CEP (thus providing an asynchronous clear function). The look-ahead carry simplifies serial cascading of the counters. Both CEP and CET must be HIGH to count. The CET input is fed forward to enable the terminal count output (TC). The TC output thus enabled will produce a HIGH output pulse of a duration approximately equal to a HIGH output of Q0. This pulse can be used to enable the next cascaded stage. The maximum clock frequency for the cascaded counters is determined by the CP to TC propagation delay and CEP to CP set-up time, according to the following formula:

$$f_{\max} = \frac{1}{t_{P(\max)}(\text{CP to TC}) + t_{\text{SU}}(\text{CEP to CP})}$$

Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

2. Features and benefits

- Complies with JEDEC standard no. 7A
- CMOS input levels
- Synchronous counting and loading
- 2 count enable inputs for n-bit cascading
- Asynchronous reset
- Positive-edge triggered clock
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

| Type number | Package | | | |
|-------------|-------------------|------|--|----------|
| | Temperature range | Name | Description | Version |
| 74HC160D | -40 °C to +125 °C | SO16 | plastic small outline package; 16 leads; body width 3.9 mm | SOT109-1 |

4. Functional diagram

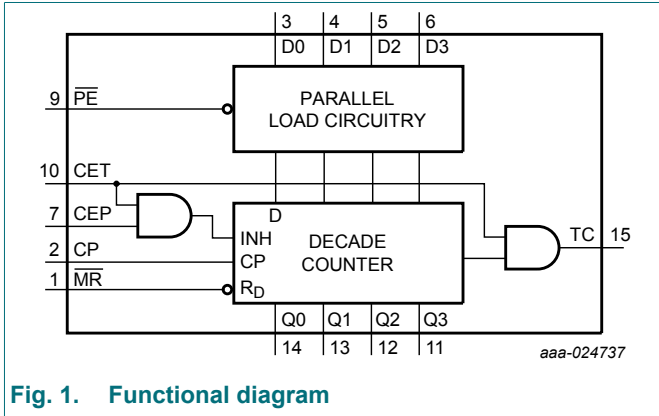


Fig. 1. Functional diagram

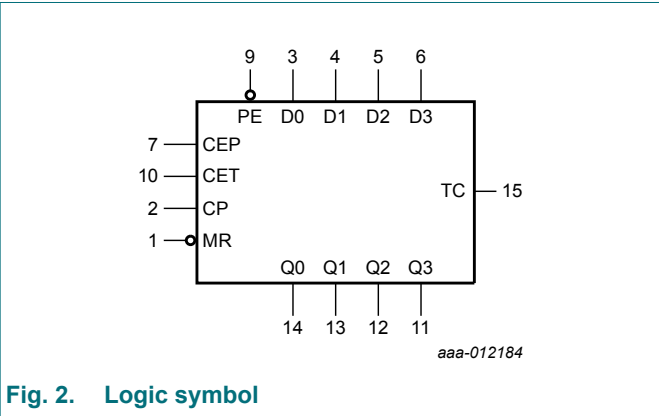


Fig. 2. Logic symbol

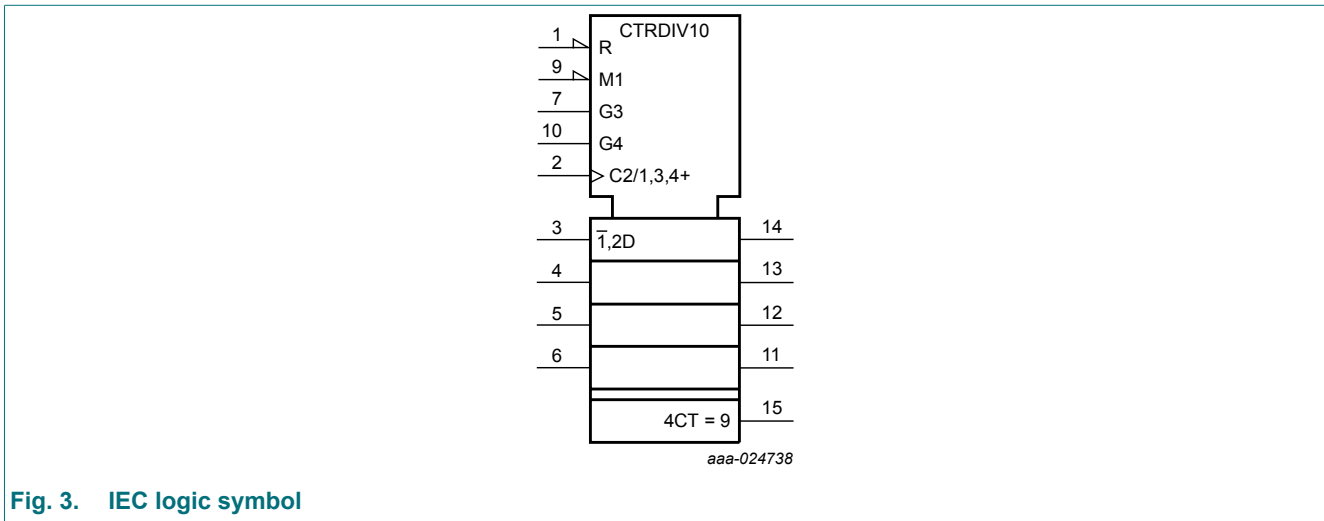


Fig. 3. IEC logic symbol

Pre-settable synchronous BCD decade counter; asynchronous reset

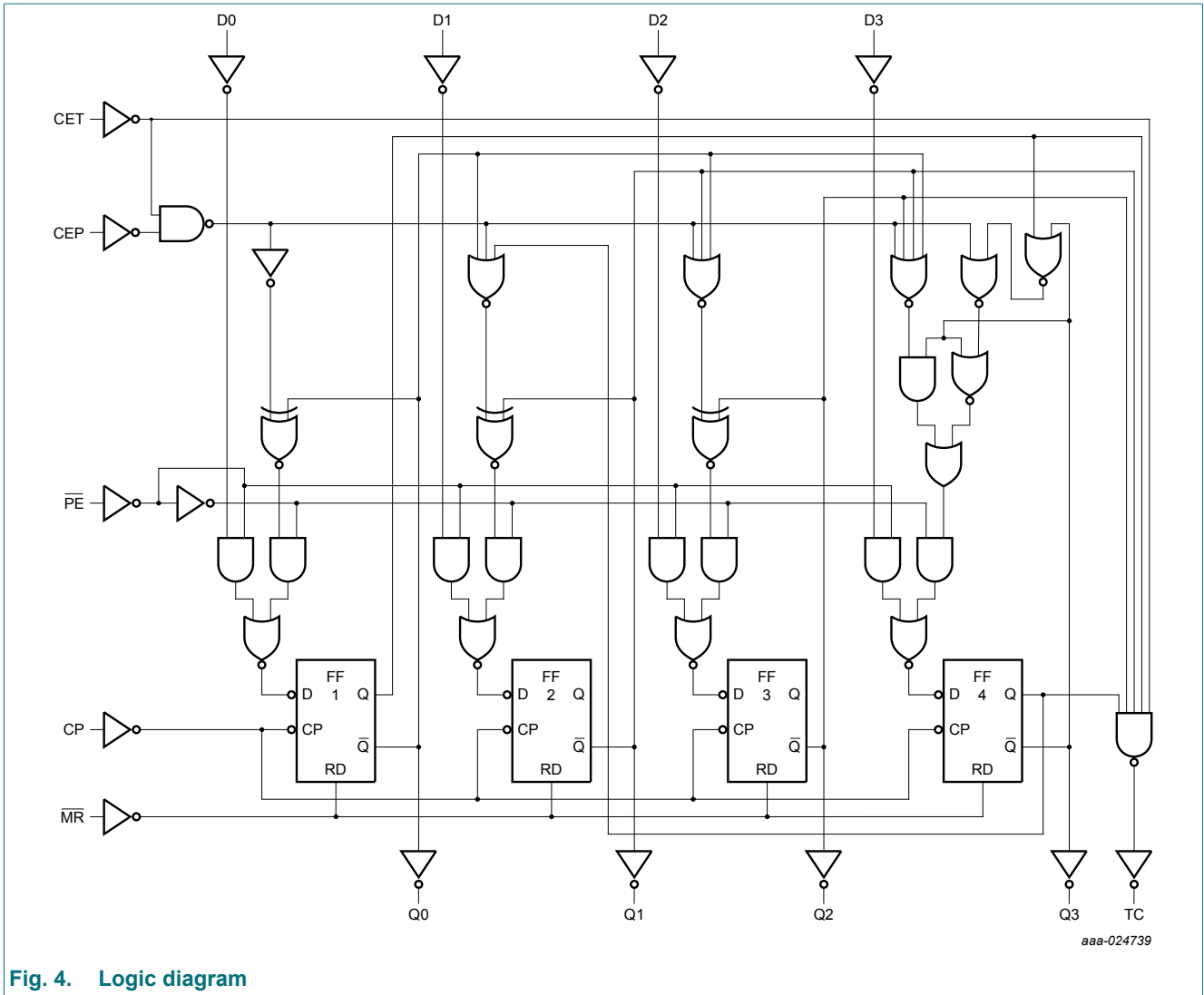


Fig. 4. Logic diagram

5. Pinning information

5.1. Pinning

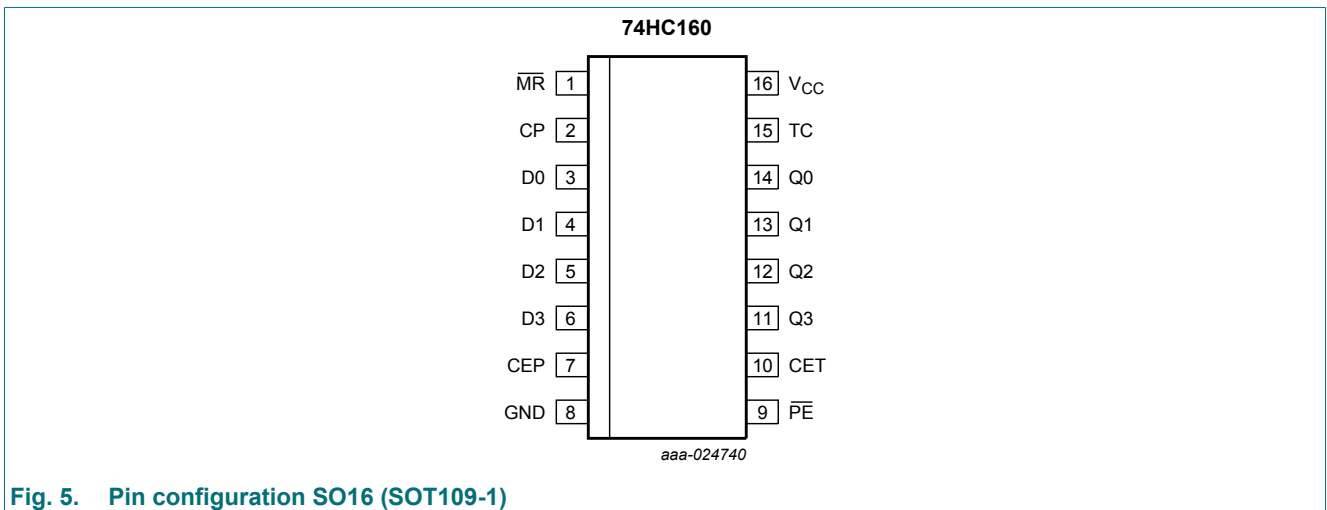


Fig. 5. Pin configuration SO16 (SOT109-1)

5.2. Pin description

Table 2. Pin description

| Symbol | Pin | Description |
|-----------------|----------------|---|
| MR | 1 | asynchronous master reset (active LOW) |
| CP | 2 | clock input (LOW-to-HIGH, edge triggered) |
| D0, D1, D2, D3 | 3, 4, 5, 6 | data input |
| CEP | 7 | count enable input |
| GND | 8 | ground (0 V) |
| PE | 9 | parallel enable input (active LOW) |
| CET | 10 | count enable carry input |
| Q0, Q1, Q2, Q3 | 14, 13, 12, 11 | flip-flop output |
| TC | 15 | terminal count output |
| V _{CC} | 16 | supply voltage |

6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; ↑ = LOW-to-HIGH clock transition;
 h = HIGH voltage level one set-up time prior to the LOW-to-HIGH CP transition;
 l = LOW voltage level one set-up time prior to the LOW-to-HIGH CP transition;
 q_n = lower case letters indicate the state of the referenced output one set-up time prior to the LOW-to-HIGH CP transition.

| Operating mode | Inputs | | | | | | Outputs | |
|-------------------|--------|----|-----|-----|----|----------------|----------------|-----|
| | MR | CP | CEP | CET | PE | D _n | Q _n | TC |
| Reset (clear) | L | X | X | X | X | X | L | L |
| Parallel load | H | ↑ | X | X | l | l | L | L |
| | H | ↑ | X | X | l | h | H | [1] |
| Count | H | ↑ | h | h | h | X | count | [1] |
| Hold (do nothing) | H | X | l | X | h | X | q _n | [1] |
| | H | X | X | l | h | X | q _n | L |

[1] The TC output is HIGH when CET is HIGH and the counter is at terminal count (HLLH);

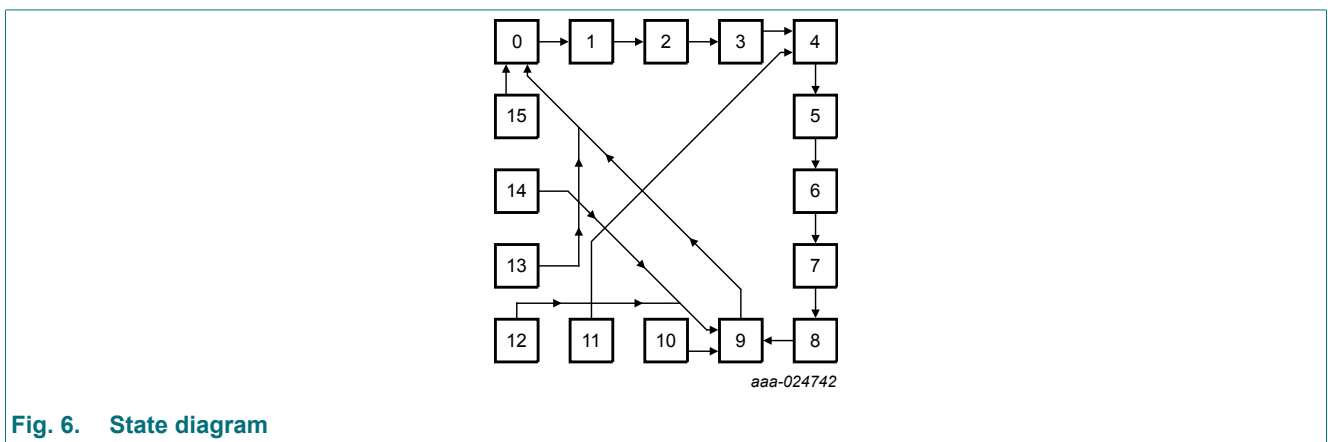
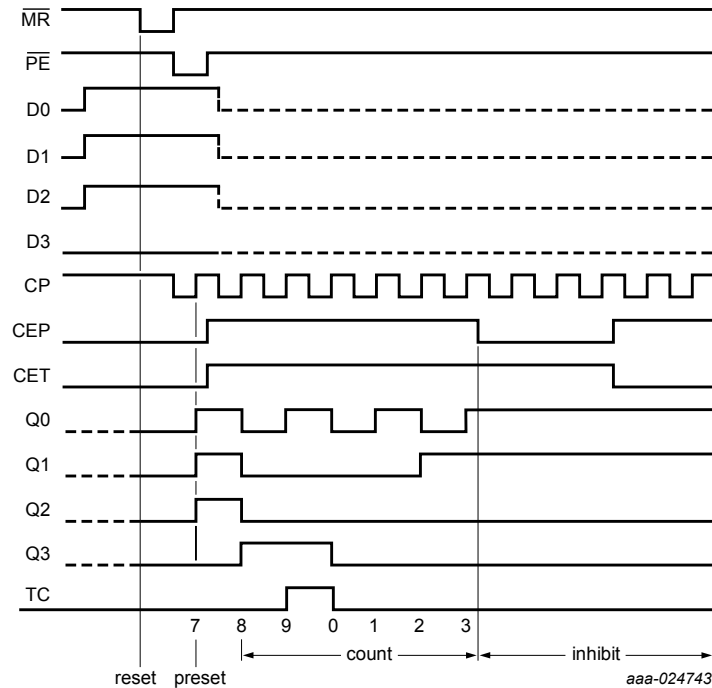


Fig. 6. State diagram



Typical timing sequence:
 reset outputs to zero; preset to BCD seven; count to eight, nine, zero, one, two and three; inhibit.

Fig. 7. Typical timing sequence

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|-----------|-------------------------|--|------|----------|------|
| V_{CC} | supply voltage | | -0.5 | +7.0 | V |
| I_{IK} | input clamping current | $V_I < -0.5\text{ V}$ or $V_I > V_{CC} + 0.5\text{ V}$ | - | ± 20 | mA |
| I_{OK} | output clamping current | $V_O < -0.5\text{ V}$ or $V_O > V_{CC} + 0.5\text{ V}$ | - | ± 20 | mA |
| I_O | output current | $-0.5\text{ V} < V_O < V_{CC} + 0.5\text{ V}$ | - | ± 25 | mA |
| I_{CC} | supply current | | - | +50 | mA |
| I_{GND} | ground current | | -50 | - | mA |
| T_{stg} | storage temperature | | -65 | +150 | °C |
| P_{tot} | total power dissipation | [1] | - | 500 | mW |

[1] For SO16 package: above 70 °C the value of P_{tot} derates linearly at 8 mW/K.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---------------------|-------------------------------------|-------------------------|-----|------|----------|------|
| V_{CC} | supply voltage | | 2.0 | 5.0 | 6.0 | V |
| V_I | input voltage | | 0 | - | V_{CC} | V |
| V_O | output voltage | | 0 | - | V_{CC} | V |
| T_{amb} | ambient temperature | | -40 | +25 | +125 | °C |
| $\Delta t/\Delta V$ | input transition rise and fall rate | $V_{CC} = 2.0\text{ V}$ | - | - | 625 | ns/V |
| | | $V_{CC} = 4.5\text{ V}$ | - | 1.67 | 139 | ns/V |
| | | $V_{CC} = 6.0\text{ V}$ | - | - | 83 | ns/V |

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|----------|--|---|-------|------|-----------|------------------|-----------|-------------------|-----------|---------------|
| | | | Min | Typ | Max | Min | Max | Min | Max | |
| V_{IH} | HIGH-level input voltage | $V_{CC} = 2.0\text{ V}$ | 1.5 | 1.2 | - | 1.5 | - | 1.5 | - | V |
| | | $V_{CC} = 4.5\text{ V}$ | 3.15 | 2.4 | - | 3.15 | - | 3.15 | - | V |
| | | $V_{CC} = 6.0\text{ V}$ | 4.2 | 3.2 | - | 4.2 | - | 4.2 | - | V |
| V_{IL} | LOW-level input voltage | $V_{CC} = 2.0\text{ V}$ | - | 0.8 | 0.5 | - | 0.5 | - | 0.5 | V |
| | | $V_{CC} = 4.5\text{ V}$ | - | 2.1 | 1.35 | - | 1.35 | - | 1.35 | V |
| | | $V_{CC} = 6.0\text{ V}$ | - | 2.8 | 1.8 | - | 1.8 | - | 1.8 | V |
| V_{OH} | HIGH-level output voltage | $V_I = V_{IH}$ or V_{IL} | | | | | | | | |
| | | $I_O = -20\ \mu\text{A}$; $V_{CC} = 2.0\text{ V}$ | 1.9 | 2.0 | - | 1.9 | - | 1.9 | - | V |
| | | $I_O = -20\ \mu\text{A}$; $V_{CC} = 4.5\text{ V}$ | 4.4 | 4.5 | - | 4.4 | - | 4.4 | - | V |
| | | $I_O = -20\ \mu\text{A}$; $V_{CC} = 6.0\text{ V}$ | 5.9 | 6.0 | - | 5.9 | - | 5.9 | - | V |
| | | $I_O = -4.0\text{ mA}$; $V_{CC} = 4.5\text{ V}$ | 3.98 | 4.32 | - | 3.84 | - | 3.7 | - | V |
| | $I_O = -5.2\text{ mA}$; $V_{CC} = 6.0\text{ V}$ | 5.48 | 5.81 | - | 5.34 | - | 5.2 | - | V | |
| V_{OL} | LOW-level output voltage | $V_I = V_{IH}$ or V_{IL} | | | | | | | | |
| | | $I_O = 20\ \mu\text{A}$; $V_{CC} = 2.0\text{ V}$ | - | 0 | 0.1 | - | 0.1 | - | 0.1 | V |
| | | $I_O = 20\ \mu\text{A}$; $V_{CC} = 4.5\text{ V}$ | - | 0 | 0.1 | - | 0.1 | - | 0.1 | V |
| | | $I_O = 20\ \mu\text{A}$; $V_{CC} = 6.0\text{ V}$ | - | 0 | 0.1 | - | 0.1 | - | 0.1 | V |
| | | $I_O = 4.0\text{ mA}$; $V_{CC} = 4.5\text{ V}$ | - | 0.15 | 0.26 | - | 0.33 | - | 0.4 | V |
| | $I_O = 5.2\text{ mA}$; $V_{CC} = 6.0\text{ V}$ | - | 0.16 | 0.26 | - | 0.33 | - | 0.4 | V | |
| I_I | input leakage current | $V_I = V_{CC}$ or GND; $V_{CC} = 6.0\text{ V}$ | - | - | ± 0.1 | - | ± 1.0 | - | ± 1.0 | μA |
| I_{CC} | supply current | $V_I = V_{CC}$ or GND; $I_O = 0\text{ A}$; $V_{CC} = 6.0\text{ V}$ | - | - | 8.0 | - | 80.0 | - | 160.0 | μA |
| C_I | input capacitance | | - | 3.5 | - | - | - | - | - | pF |

10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); $C_L = 50$ pF unless otherwise specified; for test circuit see Fig. 13.

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|------------------|-------------------------------|------------------------------------|-------|-----|-----|------------------|-----|-------------------|-----|------|
| | | | Min | Typ | Max | Min | Max | Min | Max | |
| t_{pd} | propagation delay | CP to Qn; see Fig. 8 [1] | | | | | | | | |
| | | $V_{CC} = 2.0$ V | - | 61 | 185 | - | 230 | - | 280 | ns |
| | | $V_{CC} = 4.5$ V | - | 22 | 37 | - | 46 | - | 56 | ns |
| | | $V_{CC} = 5.0$ V; $C_L = 15$ pF | - | 19 | - | - | - | - | - | ns |
| | | $V_{CC} = 6.0$ V | - | 18 | 31 | - | 39 | - | 48 | ns |
| | | CP to TC; see Fig. 8 | | | | | | | | |
| | | $V_{CC} = 2.0$ V | - | 69 | 215 | - | 270 | - | 325 | ns |
| | | $V_{CC} = 4.5$ V | - | 25 | 43 | - | 54 | - | 65 | ns |
| | | $V_{CC} = 5.0$ V; $C_L = 15$ pF | - | 21 | - | - | - | - | - | ns |
| | | $V_{CC} = 6.0$ V | - | 20 | 31 | - | 46 | - | 55 | ns |
| | | CET to TC; see Fig. 9 | | | | | | | | |
| | | $V_{CC} = 2.0$ V | - | 47 | 150 | - | 190 | - | 225 | ns |
| | | $V_{CC} = 4.5$ V | - | 17 | 30 | - | 38 | - | 45 | ns |
| | | $V_{CC} = 5.0$ V; $C_L = 15$ pF | - | 14 | - | - | - | - | - | ns |
| $V_{CC} = 6.0$ V | - | 14 | 26 | - | 33 | - | 38 | ns | | |
| t_{PHL} | High to LOW propagation delay | \overline{MR} to Qn; see Fig. 10 | | | | | | | | |
| | | $V_{CC} = 2.0$ V | - | 69 | 210 | - | 265 | - | 315 | ns |
| | | $V_{CC} = 4.5$ V | - | 25 | 42 | - | 53 | - | 63 | ns |
| | | $V_{CC} = 5.0$ V; $C_L = 15$ pF | - | 21 | - | - | - | - | - | ns |
| | | $V_{CC} = 6.0$ V | - | 20 | 36 | - | 45 | - | 54 | ns |
| | | \overline{MR} to TC; see Fig. 10 | | | | | | | | |
| | | $V_{CC} = 2.0$ V | - | 69 | 220 | - | 275 | - | 330 | ns |
| | | $V_{CC} = 4.5$ V | - | 25 | 44 | - | 55 | - | 66 | ns |
| | | $V_{CC} = 5.0$ V; $C_L = 15$ pF | - | 21 | - | - | - | - | - | ns |
| | | $V_{CC} = 6.0$ V | - | 20 | 37 | - | 47 | - | 56 | ns |
| t_t | transition time | see Fig. 8 and Fig. 9 [2] | | | | | | | | |
| | | $V_{CC} = 2.0$ V | - | 19 | 75 | - | 95 | - | 110 | ns |
| | | $V_{CC} = 4.5$ V | - | 7 | 15 | - | 19 | - | 22 | ns |
| | | $V_{CC} = 6.0$ V | - | 6 | 13 | - | 16 | - | 19 | ns |
| t_w | pulse width | CP HIGH or LOW; see Fig. 8 | | | | | | | | |
| | | $V_{CC} = 2.0$ V | 80 | 22 | - | 100 | - | 120 | - | ns |
| | | $V_{CC} = 4.5$ V | 16 | 8 | - | 20 | - | 24 | - | ns |
| | | $V_{CC} = 6.0$ V | 14 | 3 | - | 17 | - | 20 | - | ns |
| t_w | pulse width | \overline{MR} LOW; see Fig. 10 | | | | | | | | |
| | | $V_{CC} = 2.0$ V | 80 | 28 | - | 100 | - | 120 | - | ns |
| | | $V_{CC} = 4.5$ V | 16 | 10 | - | 20 | - | 24 | - | ns |
| | | $V_{CC} = 6.0$ V | 14 | 8 | - | 17 | - | 20 | - | ns |

Presettable synchronous BCD decade counter; asynchronous reset

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|------------------|-------------------------------|--|-------|-----|-----|------------------|-----|-------------------|-----|------|
| | | | Min | Typ | Max | Min | Max | Min | Max | |
| t _{rec} | recovery time | \overline{MR} to CP; see Fig. 10 | | | | | | | | |
| | | V _{CC} = 2.0 V | 100 | 30 | - | 125 | - | 150 | - | ns |
| | | V _{CC} = 4.5 V | 20 | 11 | - | 25 | - | 30 | - | ns |
| | | V _{CC} = 6.0 V | 17 | 9 | - | 21 | - | 26 | - | ns |
| t _{su} | set-up time | Dn to CP; see Fig. 11 | | | | | | | | |
| | | V _{CC} = 2.0 V | 80 | 22 | - | 100 | - | 120 | - | ns |
| | | V _{CC} = 4.5 V | 16 | 8 | - | 20 | - | 24 | - | ns |
| | | V _{CC} = 6.0 V | 14 | 6 | - | 17 | - | 20 | - | ns |
| | | \overline{PE} to CP; see Fig. 11 | | | | | | | | |
| | | V _{CC} = 2.0 V | 135 | 41 | - | 170 | - | 205 | - | ns |
| | | V _{CC} = 4.5 V | 27 | 15 | - | 34 | - | 41 | - | ns |
| | | V _{CC} = 6.0 V | 23 | 12 | - | 29 | - | 35 | - | ns |
| | | CEP, CET to CP; see Fig. 12 | | | | | | | | |
| | | V _{CC} = 2.0 V | 200 | 63 | - | 250 | - | 300 | - | ns |
| | | V _{CC} = 4.5 V | 40 | 23 | - | 50 | - | 60 | - | ns |
| | | V _{CC} = 6.0 V | 34 | 18 | - | 43 | - | 51 | - | ns |
| t _h | hold time | Dn to CP; see Fig. 11 | | | | | | | | |
| | | V _{CC} = 2.0 V | 0 | -17 | - | 0 | - | 0 | - | ns |
| | | V _{CC} = 4.5 V | 0 | -6 | - | 0 | - | 0 | - | ns |
| | | V _{CC} = 6.0 V | 0 | -5 | - | 0 | - | 0 | - | ns |
| | | \overline{PE} to CP; see Fig. 11 | | | | | | | | |
| | | V _{CC} = 2.0 V | 0 | -41 | - | 0 | - | 0 | - | ns |
| | | V _{CC} = 4.5 V | 0 | -15 | - | 0 | - | 0 | - | ns |
| | | V _{CC} = 6.0 V | 0 | -12 | - | 0 | - | 0 | - | ns |
| | | CEP, CET to CP; see Fig. 12 | | | | | | | | |
| | | V _{CC} = 2.0 V | 0 | -58 | - | 0 | - | 0 | - | ns |
| | | V _{CC} = 4.5 V | 0 | -21 | - | 0 | - | 0 | - | ns |
| | | V _{CC} = 6.0 V | 0 | -17 | - | 0 | - | 0 | - | ns |
| f _{max} | maximum frequency | CP; see Fig. 8 | | | | | | | | |
| | | V _{CC} = 2.0 V | 6 | 18 | - | 4.8 | - | 4 | - | MHz |
| | | V _{CC} = 4.5 V | 30 | 55 | - | 24 | - | 20 | - | MHz |
| | | V _{CC} = 5.0 V; C _L = 15 pF | - | 61 | - | - | - | - | - | MHz |
| | | V _{CC} = 6.0 V | 35 | 66 | - | 28 | - | 24 | - | MHz |
| C _{PD} | power dissipation capacitance | V _I = GND to V _{CC} ; f _i = 1 MHz [3] | - | 39 | - | - | - | - | - | pF |

[1] t_{pd} is the same as t_{PHL} and t_{PLH}.

[2] t_t is the same as t_{THL} and t_{TLH}.

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW):

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i = input frequency in MHz;

f_o = output frequency in MHz;

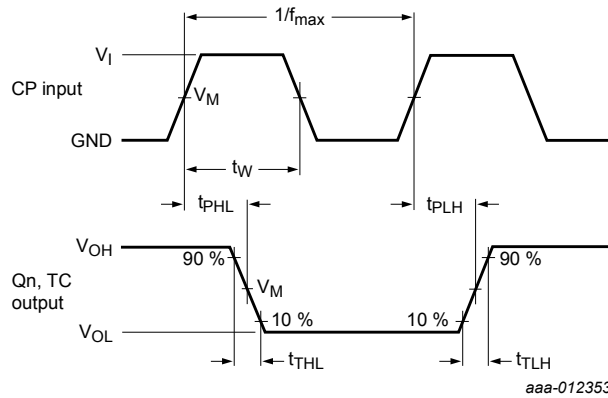
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\sum(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

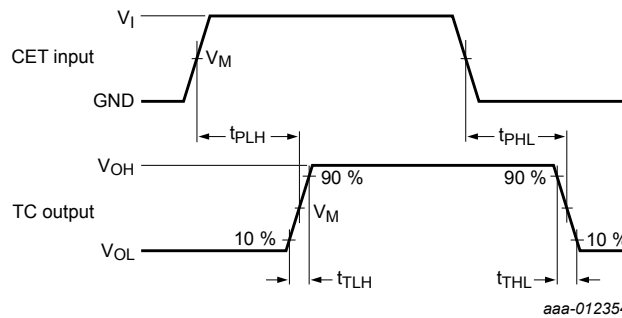
10.1. Waveforms and test circuit



Measurement points are given in [Table 8](#).

Logic levels V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

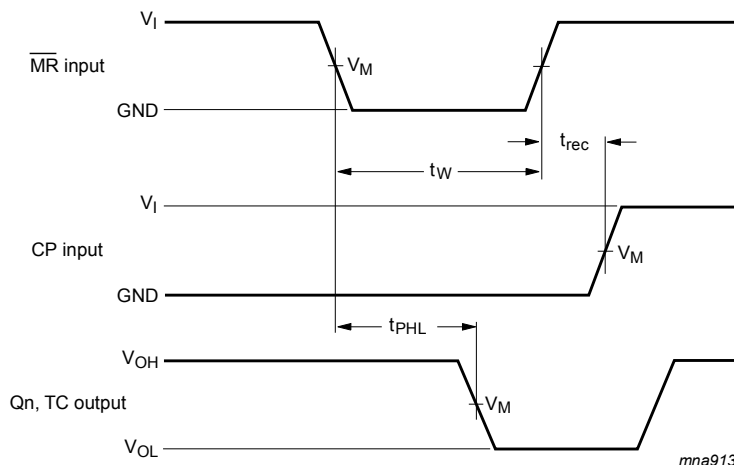
Fig. 8. The clock (CP) to outputs (Qn, TC) propagation delays, pulse width, output transition times and maximum frequency



Measurement points are given in [Table 8](#).

Logic levels V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 9. The count enable carry input (CET) to terminal count output (TC) propagation delays and output transition times



Measurement points are given in [Table 8](#).

Logic levels V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 10. The master reset (\overline{MR}) pulse width, master reset to output (Qn, TC) propagation delays, and the master reset to clock (CP) recovery times

Pre-settable synchronous BCD decade counter; asynchronous reset

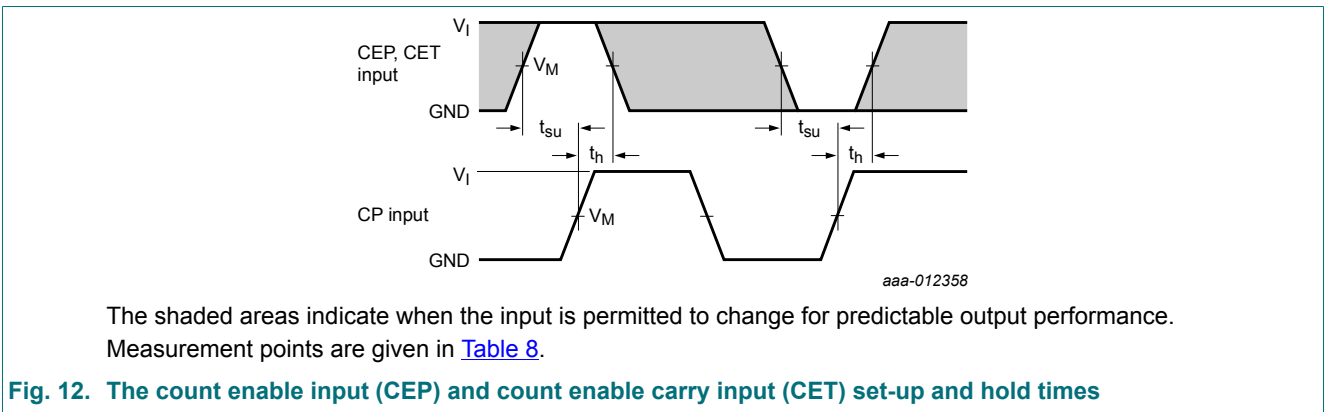
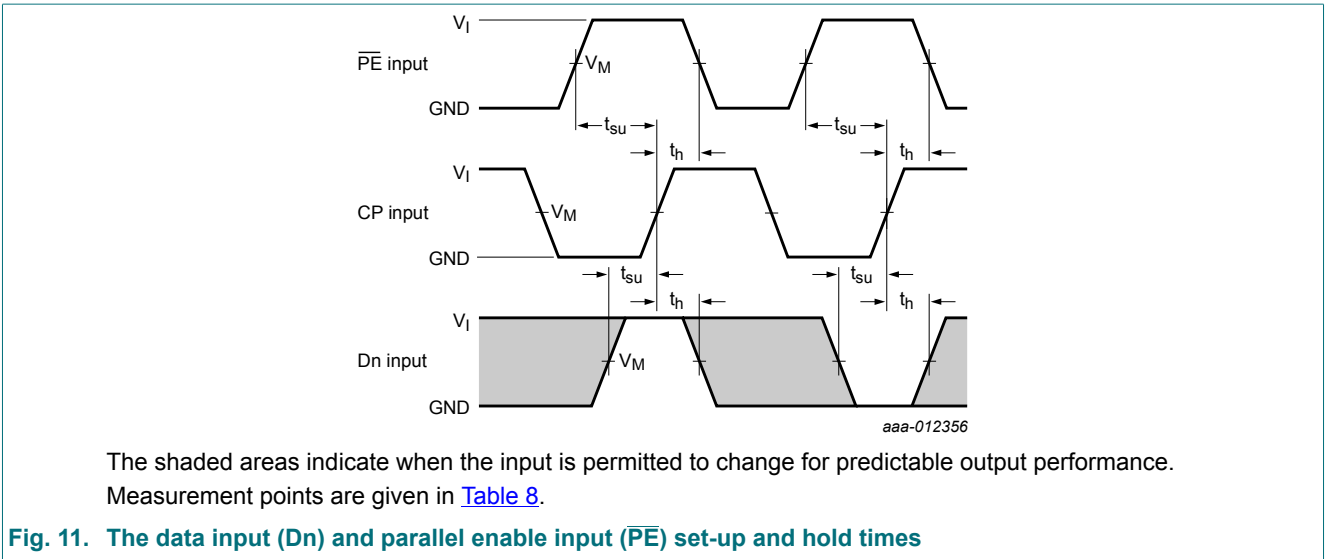


Table 8. Measurement points

| Input | | Output |
|---------------------|-----------------|---------------------|
| V_M | V_I | V_M |
| $0.5 \times V_{CC}$ | GND to V_{CC} | $0.5 \times V_{CC}$ |

Presetable synchronous BCD decade counter; asynchronous reset

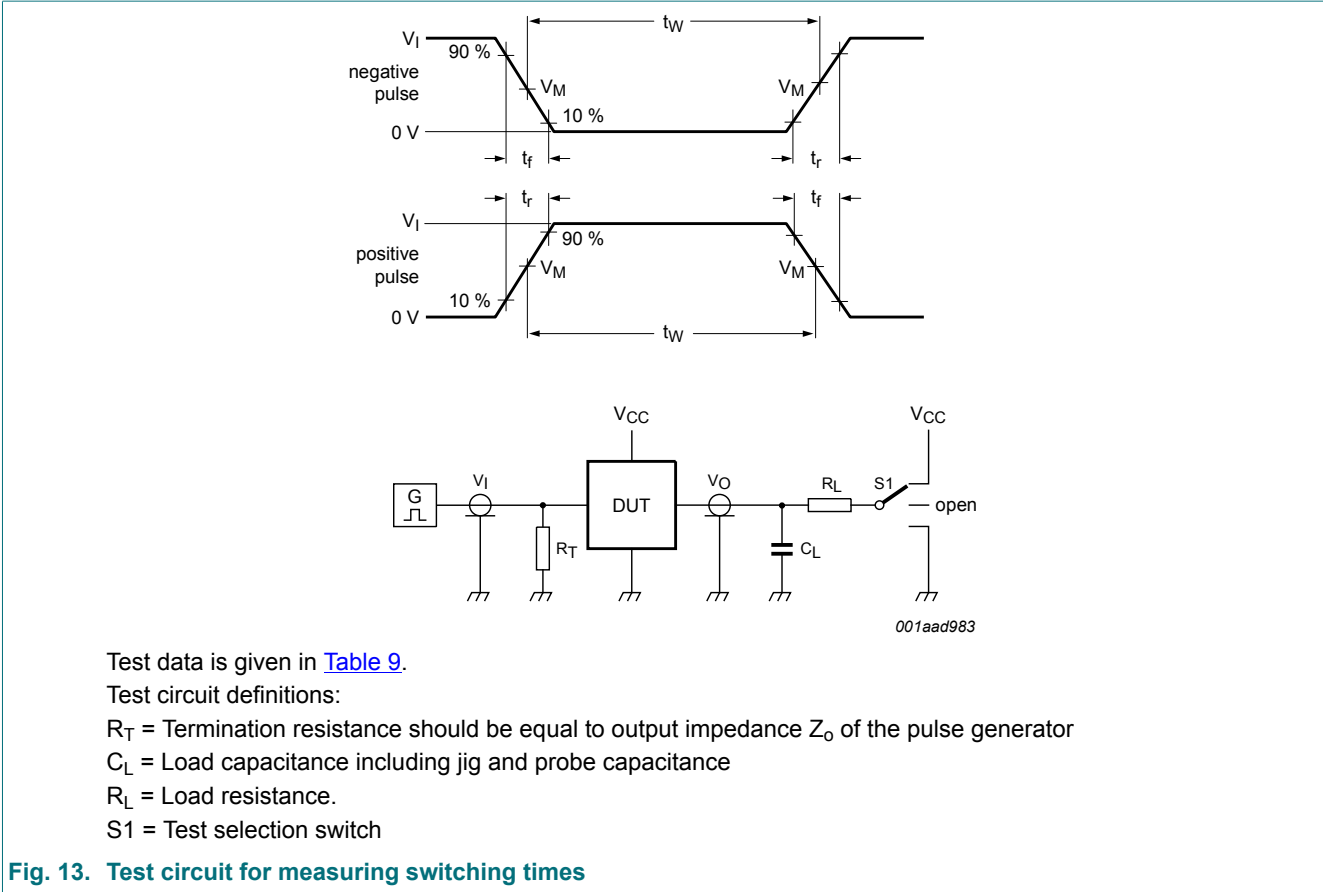


Fig. 13. Test circuit for measuring switching times

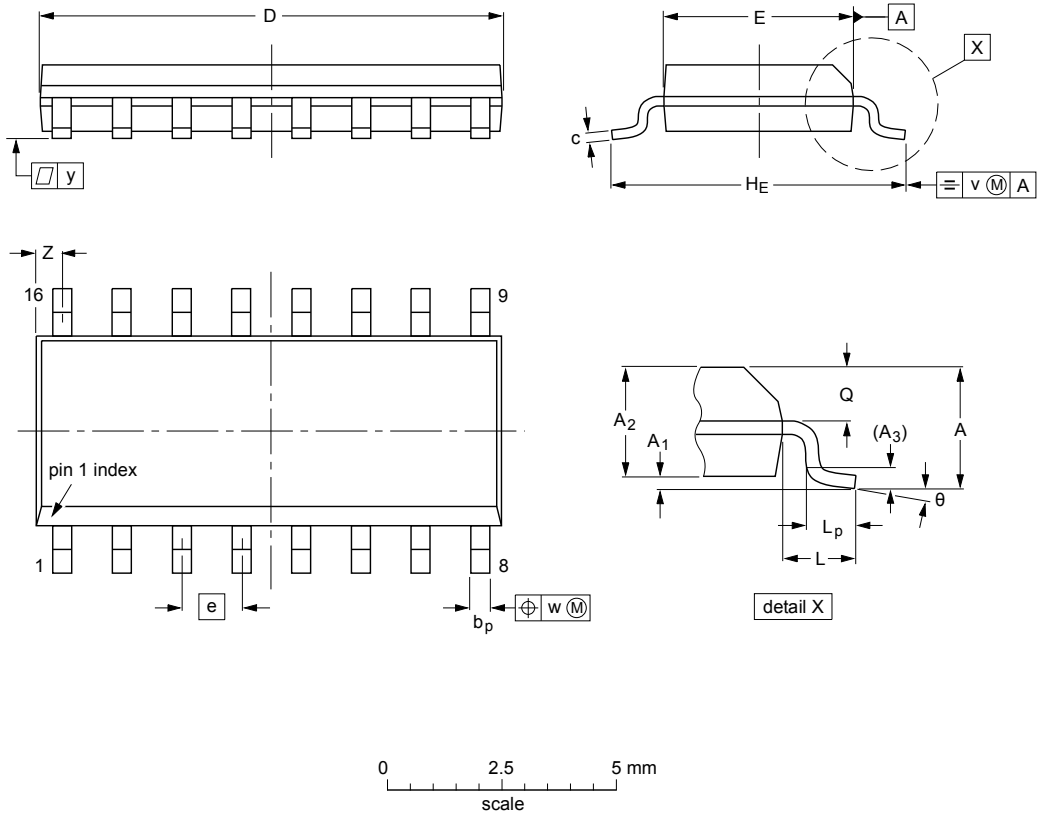
Table 9. Test data

| Input | | Load | | S1 position |
|----------|------------|--------------|-------|--------------------|
| V_I | t_r, t_f | C_L | R_L | t_{PHL}, t_{PLH} |
| V_{CC} | 6 ns | 15 pF, 50 pF | 1 kΩ | open |

11. Package outline

SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

| UNIT | A max. | A ₁ | A ₂ | A ₃ | b _p | c | D ⁽¹⁾ | E ⁽¹⁾ | e | H _E | L | L _p | Q | v | w | y | Z ⁽¹⁾ | θ |
|--------|--------|----------------|----------------|----------------|----------------|------------------|------------------|------------------|------|----------------|-------|----------------|----------------|------|------|-------|------------------|----------|
| mm | 1.75 | 0.25 0.10 | 1.45 1.25 | 0.25 | 0.49 0.36 | 0.25 0.19 | 10.0 9.8 | 4.0 3.8 | 1.27 | 6.2 5.8 | 1.05 | 1.0 0.4 | 0.7 0.6 | 0.25 | 0.25 | 0.1 | 0.7 0.3 | 8° 0° |
| inches | 0.069 | 0.010 0.004 | 0.057 0.049 | 0.01 | 0.019 0.014 | 0.0100 0.0075 | 0.39 0.38 | 0.16 0.15 | 0.05 | 0.244 0.228 | 0.041 | 0.039 0.016 | 0.028 0.020 | 0.01 | 0.01 | 0.004 | 0.028 0.012 | |

Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

| OUTLINE VERSION | REFERENCES | | | | EUROPEAN PROJECTION | ISSUE DATE |
|-----------------|------------|--------|-------|--|---------------------|----------------------|
| | IEC | JEDEC | JEITA | | | |
| SOT109-1 | 076E07 | MS-012 | | | | 99-12-27 03-02-19 |

Fig. 14. Package outline SOT109-1 (SO16)

12. Abbreviations

Table 10. Abbreviations

| Acronym | Description |
|---------|---|
| CMOS | Complementary Metal-Oxide Semiconductor |
| DUT | Device Under Test |
| ESD | ElectroStatic Discharge |
| HBM | Human Body Model |
| MM | Machine Model |

13. Revision history

Table 11. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|-----------------|---|-----------------------|---------------|-----------------|
| 74HC160 v.4 | 20190327 | Product data sheet | - | 74HC_HCT160 v.3 |
| Modifications: | <ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type number 74HC160DB (SOT338-1) removed. | | | |
| 74HC160 v.3 | 20160927 | Product data sheet | - | 74HC_HCT160 v.2 |
| Modifications: | <ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. Type numbers 74HCT160D, 74HCT160PW, 74HCT160N, 74HC160N and 74HC160PW removed. | | | |
| 74HC_HCT160 v.2 | 19901201 | Product specification | - | - |

14. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

| | |
|--|-----------|
| 1. General description | 1 |
| 2. Features and benefits | 1 |
| 3. Ordering information | 1 |
| 4. Functional diagram | 2 |
| 5. Pinning information | 3 |
| 5.1. Pinning..... | 3 |
| 5.2. Pin description..... | 4 |
| 6. Functional description | 4 |
| 7. Limiting values | 5 |
| 8. Recommended operating conditions | 6 |
| 9. Static characteristics | 6 |
| 10. Dynamic characteristics | 7 |
| 10.1. Waveforms and test circuit..... | 9 |
| 11. Package outline | 12 |
| 12. Abbreviations | 13 |
| 13. Revision history | 13 |
| 14. Legal information | 14 |

© Nexperia B.V. 2019. All rights reserved

For more information, please visit: <http://www.nexperia.com>
For sales office addresses, please send an email to: salesaddresses@nexperia.com
Date of release: 27 March 2019

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [Counter ICs](#) category:

Click to view products by [Nexperia](#) manufacturer:

Other Similar products are found below :

[CD4018BE](#) [CD4033BE](#) [CD4060BE](#) [NLV14040BDR2G](#) [NLV14017BDG](#) [74VHC163FT](#) [74HCT4040BQ-Q100X](#) [74VHC161FT\(BJ\)](#)
[74VHC163FT\(BJ\)](#) [74HC393D.652](#) [74HCT4040D.653](#) [74HC191D.652](#) [74HC160D,652](#) [74HC390DB,118](#) [74HC163PW.112](#)
[74HC191PW.112](#) [74HC393DB.118](#) [74HC4024D.652](#) [74HCT193DB.112](#) [74HCT390DB.112](#) [74HC193PW.112](#) [74HC390D.652](#)
[74HC4017PW.112](#) [74HC4020DB.112](#) [74HC4020PW.112](#) [74HC4040DB.112](#) [74HC4040PW.112](#) [74HC4060DB.112](#) [74HC4520D.112](#)
[74HCT393DB.112](#) [74HCT6323AD.112](#) [74LV393D.112](#) [74LV393PW.112](#) [74LV4060D.112](#) [74LV4060DB.112](#) [74LV4060PW.112](#)
[74LVC161D.112](#) [74LVC161PW.112](#) [XD74LS90](#) [XD74LS93](#) [CD4017BE](#) [XD74LS161](#) [XD74LS192](#) [XD74LS193](#) [CD4060BE](#) [XD4553](#)
[XD74LS163](#) [XD74LS190](#) [XD40192](#) [CD4040BE](#)