74HC1G125; 74HCT1G125

Bus buffer/line driver; 3-state Rev. 7 — 17 January 2022

Product data sheet

1. General description

The 74HC1G125; 74HCT1G125 is a single buffer/line driver with 3-state output. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of $V_{\rm CC}$.

2. Features and benefits

- Wide supply voltage range from 2.0 V to 6.0 V
- CMOS low power dissipation
- High noise immunity
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Input levels:
 - For 74HC1G125: CMOS level
 - For 74HCT1G125: TTL level
- Symmetrical output impedance
- · High noise immunity
- Balanced propagation delays
- · Complies with JEDEC standards:
 - JESD8C (2.7 V to 3.6 V)
 - JESD7A (2.0 V to 6.0 V)
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to 85 °C and -40 °C to 125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package	Package								
	Temperature range	Name	Description	Version						
74HC1G125GW	-40 °C to +125 °C		plastic thin shrink small outline package; 5 leads;	SOT353-1						
74HCT1G125GW			body width 1.25 mm							
74HC1G125GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753						
74HCT1G125GV	1									



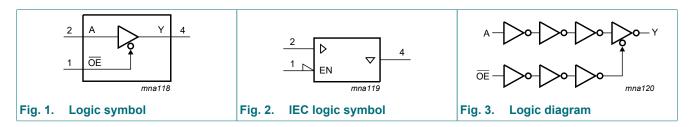
4. Marking

Table 2. Marking

Type number	Marking code[1]
74HC1G125GW	НМ
74HCT1G125GW	TM
74HC1G125GV	H25
74HCT1G125GV	T25

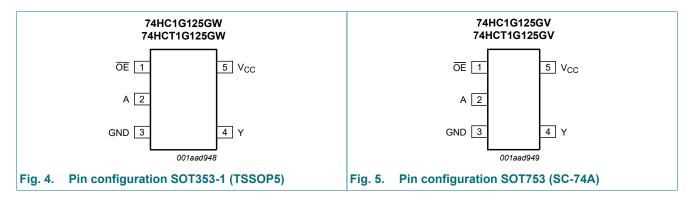
^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Tuble 6.1 in description					
Symbol	Pin	Description			
ŌE	1	output enable input (active LOW)			
A	2	data input			
GND	3	ground (0 V)			
Υ	4	data output			
V _{CC}	5	supply voltage			

7. Functional description

Table 4. Function table

 $H = HIGH \text{ voltage level}; L = LOW \text{ voltage level}; X = don't care; Z = high-impedance OFF-state.}$

Control	Input	Output
OE	A	Υ
L	L	L
L	Н	Н
Н	X	Z

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
I _{IK}	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{CC} + 0.5 \text{ V}$	1]	-	±20	mA
I _{OK}	output clamping current	$V_{O} < -0.5 \text{ V or } V_{O} > V_{CC} + 0.5 \text{ V}$	1]	-	±20	mA
Io	output current	$V_{O} = -0.5 \text{ V to } (V_{CC} + 0.5 \text{ V})$	1]	-	±35	mA
I _{CC}	supply current			-	70	mA
I _{GND}	ground current			-70	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	2]	-	250	mW

The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	arameter Conditions 74HC1G125		74	Unit				
			Min	Тур	Max	Min	Тур	Max	
V_{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V _{CC}	0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and	V _{CC} = 2.0 V	-	-	625	-	-	-	ns/V
	fall rate	V _{CC} = 4.5 V	-	1.67	139	-	1.67	139	ns/V
		V _{CC} = 6.0 V	-	-	83	-	-	-	ns/V

For SOT353-1 (TSSOP5) package: Ptot derates linearly with 3.3 mW/K above 74 °C. For SOT753 (SC-74A) package: Ptot derates linearly with 3.8 mW/K above 85 °C.

10. Static characteristics

Table 7. Static characteristics 74HC1G125

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40	-40 °C to +85 °C			-40 °C to +125 °C		
			Min	Typ[1]	Max	Min	Max		
V _{IH}	HIGH-level input	V _{CC} = 2.0 V	1.5	1.2	-	1.5	-	V	
	voltage	V _{CC} = 4.5 V	3.15	2.4	-	3.15	-	V	
		V _{CC} = 6.0 V	4.2	3.2	-	4.2	-	V	
V _{IL}	LOW-level input	V _{CC} = 2.0 V	-	0.8	0.5	-	0.5	V	
	voltage	V _{CC} = 4.5 V	-	2.1	1.35	-	1.35	V	
		V _{CC} = 6.0 V	-	2.8	1.8	-	1.8	V	
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL}							
	voltage	I _O = -20 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	V	
		I_{O} = -20 μ A; V_{CC} = 4.5 V	4.4	4.5	-	4.4	-	V	
		I _O = -20 μA; V _{CC} = 6.0 V	5.9	6.0	-	5.9	-	V	
		I _O = -6.0 mA; V _{CC} = 4.5 V	3.84	4.32	-	3.7	-	V	
		I _O = -7.8 mA; V _{CC} = 6.0 V	5.34	5.81	-	5.2	-	V	
V_{OL}	LOW-level output	$V_I = V_{IH}$ or V_{IL}							
	voltage	I _O = 20 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	V	
		I _O = 20 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	V	
		I _O = 20 μA; V _{CC} = 6.0 V	-	0	0.1	-	0.1	V	
		I _O = 6.0 mA; V _{CC} = 4.5 V	-	0.15	0.33	-	0.4	V	
		I _O = 7.8 mA; V _{CC} = 6.0 V	-	0.16	0.33	-	0.4	V	
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	1.0	-	1.0	μΑ	
l _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	5	-	10	μA	
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0$ V	-	-	10	-	20	μΑ	
C _I	input capacitance		-	1.5	-	-	-	pF	

^[1] All typical values are measured at T_{amb} = 25 °C.

Table 8. Static characteristics 74HCT1G125

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		°C to +8	5°C	-40 °C to	Unit	
			Min	Typ[1]	Max	Min	Max	
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	1.6	-	2.0	-	V
V_{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	1.2	8.0	-	0.8	V
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$						
	voltage	I _O = -20 μA	4.4	4.5	-	4.4	-	V
		I _O = -6.0 mA	3.84	4.32	-	3.7	-	V
V_{OL}	LOW-level output	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$						
	voltage	I _O = 20 μA	-	0	0.1	-	0.1	V
		I _O = 6.0 mA	-	0.16	0.33	-	0.4	V
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	1.0	μΑ
I _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	5	-	10	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	10	-	20	μΑ
ΔI _{CC}	additional supply current	V _I = V _{CC} - 2.1 V; I _O = 0 A; V _{CC} = 4.5 V to 5.5 V	-	-	500	-	850	μA
Cı	input capacitance		-	1.5	-	-	-	рF

^[1] All typical values are measured at T_{amb} = 25 °C.

11. Dynamic characteristics

Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); C_L = 50 pF unless otherwise specified; for test circuit see Fig. 8

Symbol	Parameter	Conditions		-40	°C to +8	5 °C	-40 °C t	o +125 °C	Unit
					Typ[1]	Max	Min	Max	
74HC1G	125								
t _{pd}	propagation delay	A to Y; see Fig. 6	[2]						
		V _{CC} = 2.0 V		-	24	125	-	150	ns
		V _{CC} = 4.5 V		-	10	25	-	30	ns
		V _{CC} = 5 V; C _L = 15 pF		-	9	-	-	-	ns
		V _{CC} = 6.0 V		-	8	21	-	26	ns
t _{en}	enable time	OE to Y; see Fig. 7	[2]						
		V _{CC} = 2.0 V		-	19	155	-	190	ns
		V _{CC} = 4.5 V		-	9	31	-	38	ns
		V _{CC} = 6.0 V		-	7	26	-	32	ns
t _{dis}	disable time	OE to Y; see Fig. 7	[2]						
		V _{CC} = 2.0 V		-	18	155	-	190	ns
		V _{CC} = 4.5 V		-	12	31	-	38	ns
		V _{CC} = 6.0 V		-	11	26	-	32	ns
C _{PD}	power dissipation capacitance	V_I = GND to V_{CC}	[3]	-	30	-	-	-	pF
74HCT1	G125		<u>'</u>						,
t _{pd}	propagation delay	A to Y; see Fig. 6	[2]						
		V _{CC} = 4.5 V		-	11	30	-	36	ns
		V _{CC} = 5 V; C _L = 15 pF		-	10	-	-	-	ns
t _{en}	enable time	$V_{CC} = 4.5 \text{ V}$; $\overline{\text{OE}}$ to Y; see $\overline{\text{Fig. 7}}$	[2]	-	10	35	-	42	ns
t _{dis}	disable time	$V_{CC} = 4.5 \text{ V}$; $\overline{\text{OE}}$ to Y; see $\underline{\text{Fig. 7}}$	[2]	-	11	31	-	38	ns
C _{PD}	power dissipation capacitance	V_I = GND to V_{CC} - 1.5 V	[3]	-	27	-	-	-	pF

All typical values are measured at T_{amb} = 25 °C.

 t_{en} is the same as t_{PZL} and t_{PZH} .

 $\begin{array}{ll} t_{dis} \text{ is the same as } t_{PLZ} \text{ and } t_{PHZ}. \\ [3] \quad C_{PD} \text{ is used to determine the dynamic power dissipation } (P_D \text{ in } \mu \text{W}). \end{array}$

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

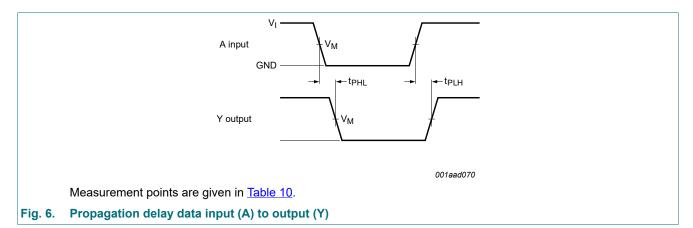
V_{CC} = supply voltage in V;

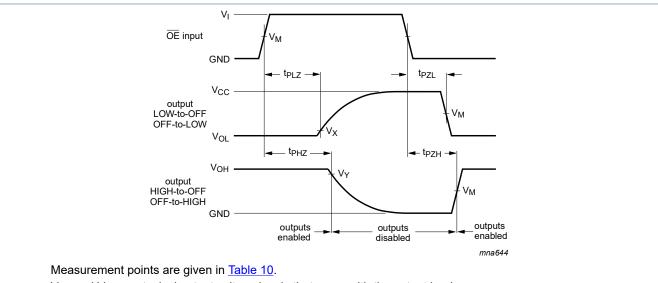
N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

 t_{pd} is the same as t_{PLH} and t_{PHL} .

11.1. Waveforms and test circuit



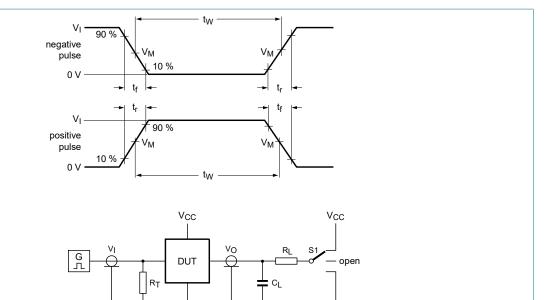


 V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 7. **Enable and disable times**

Table 10. Measurement points

Туре	Input	Output				
	V _M	V _M	V _X	V _Y		
74HC1G125	0.5V _{CC}	0.5V _{CC}	V _{OL} + 0.3 V	V _{OH} - 0.3 V		
74HCT1G125	1.3 V	1.3 V	V _{OL} + 0.3 V	V _{OH} - 0.3 V		



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Test data is given in Table 11.

Definitions for test circuit:

 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator

 C_L = Load capacitance including jig and probe capacitance

R_L = Load resistance

S1 = Test selection switch

Fig. 8. Test circuit for measuring switching times

Table 11. Test data

Туре	Input	nput		Load		S1 position		
	V _I	t _r , t _f	CL	R_L	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}	
74HC1G125	V _{CC}	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}	
74HCT1G125	3 V	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}	

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12. Package outline

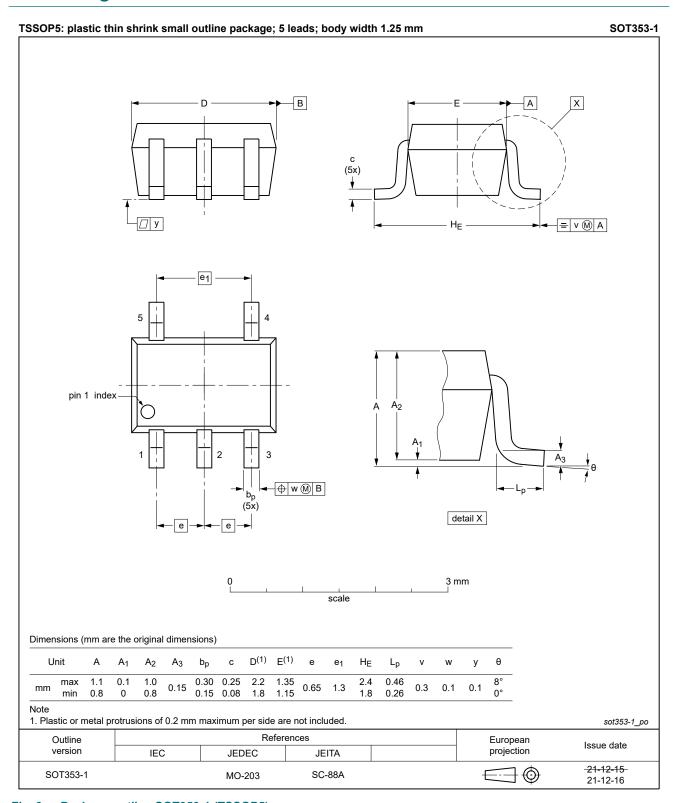


Fig. 9. Package outline SOT353-1 (TSSOP5)

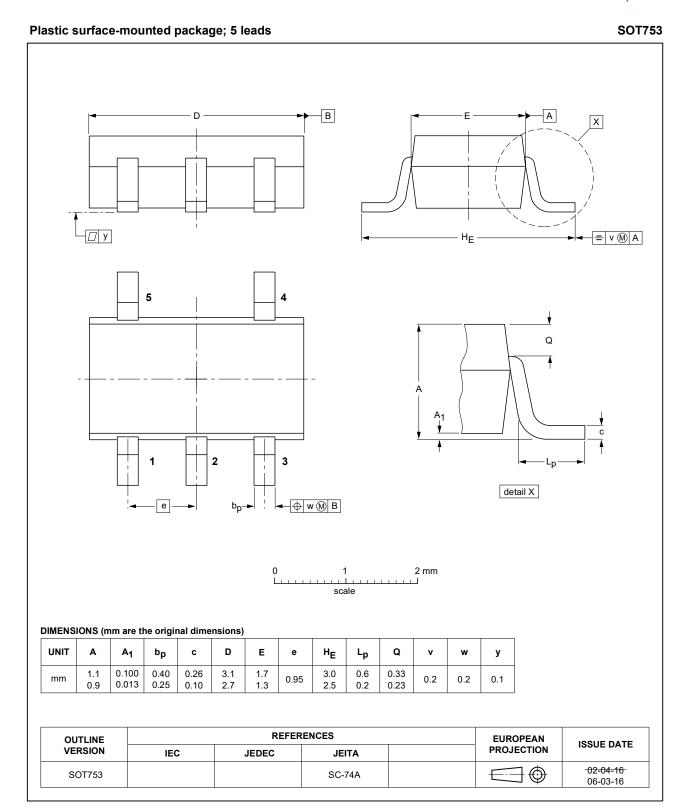


Fig. 10. Package outline SOT753 (SC-74A)

13. Abbreviations

Table 12. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74HC_HCT1G125 v.7	20220117	Product data sheet	-	74HC_HCT1G125 v.6	
Modifications:		ted. ating values for P _{tot} total po e outline drawing SOT353-	·		
74HC_HCT1G125 v.6	20170906	Product data sheet	-	74HC_HCT1G125 v.5	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 				
74HC_HCT1G125 v.5	20051223	Product data sheet	ECN05_085	74HC_HCT1G125 v.4	
Modifications:	information star In Table 5 Limit Io: changed I _{CC} : changed I _{GND} : changed In Table 7 Stati V _{OH} : changed V _{OL} : changed V _{OH} : changed	ndard of Philips Semiconduting values If max value ± 12.5 into ± 35 and max value 25 into 70 and max value -25 into -70 and condition $I_O = -2.0$ mA in ed condition $I_O = 2.0$ mA in a condition $I_O = -2.0$ mA in	25 nto $I_O = -6.0$ mA and to $I_O = 6.0$ mA to $I_O = 6.0$ mA nto $I_O = 6.0$ mA nto $I_O = 6.0$ mA to $I_O = 6.0$ mA to $I_O = 6.0$ mA to $I_O = 6.0$ mA and $I_O = 6.0$ mA and to $I_O = 6.0$ mA and to $I_O = 6.0$ mA	with the new presentation and with the new presentation and with min value from 4.13 into 3.84 min value from 5.63 into 5.34 min value from 4.13 into 3.84 pp value from 0.15 into 0.16	
74HC_HCT1G125 v.4	20040727	Product specification	-	74HC_HCT1G125 v.3	
74HC_HCT1G125 v.3	20020517	Product specification	-	74HC_HCT1G125 v.2	
74HC_HCT1G125 v.2	20010302	Product specification	-	74HC_HCT1G125 v.1	
74HC_HCT1G125 v.1	19981110	Product specification	-	-	

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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