74HC3GU04

Triple unbuffered inverter Rev. 6 — 29 January 2019

Product data sheet

1. General description

The 74HC3GU04 is a triple unbuffered inverter. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

2. Features and benefits

- Wide supply voltage range from 2.0 V to 6.0 V
- Symmetrical output impedance
- High noise immunity
- · Low-power dissipation
- Balanced propagation delays
- Multiple package options
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number Package						
	Temperature range	Name	Description	Version		
74HC3GU04DP	-40 °C to +125 °C	TSSOP8	plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm	SOT505-2		
74HC3GU04DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1		

4. Marking

Table 2. Marking

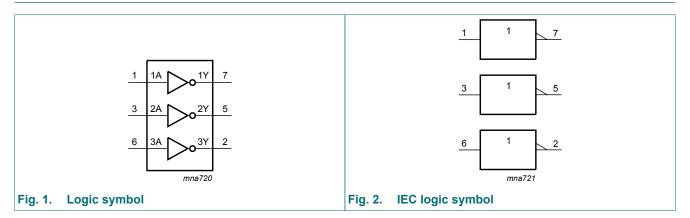
Type number	Marking code[1]
74HC3GU04DP	HU4
74HC3GU04DC	HU4

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.



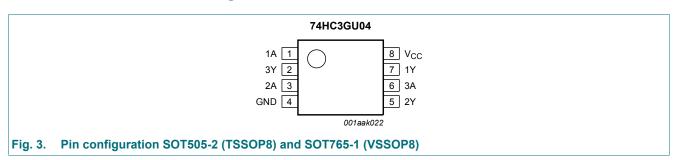
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5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description				
1A, 2A, 3A	1, 3, 6	data input				
1Y, 2Y, 3Y	7, 5, 2	data output				
GND	4	ground (0 V)				
V _{CC}	8	supply voltage				

7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Output
nA	nY
L	Н
Н	L

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7.0	V
I _{IK}	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
I _{OK}	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
Io	output current	$V_{O} = -0.5 \text{ V to } (V_{CC} + 0.5 \text{ V})$ [1]	-	±25	mA
I _{CC}	quiescent supply current	[1]	-	50	mA
I_{GND}	ground current	[1]	-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C to } +125 ^{\circ}\text{C}$ [2]	-	300	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		2.0	5.0	6.0	V
VI	input voltage		0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.0 V	-	-	625	ns/V
		V _{CC} = 4.5 V	-	1.67	139	ns/V
		V _{CC} = 6.0 V	-	-	83	ns/V

10. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C t	Unit	
			Min	Typ[1]	Max	Min	Max	
V_{IH}	HIGH-level input voltage	V _{CC} = 2.0 V	1.7	1.1	-	1.7	-	V
		V _{CC} = 4.5 V	3.6	2.4	-	3.6	-	V
		V _{CC} = 6.0 V	4.8	3.1	-	4.8	-	V
V_{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	0.9	0.3	-	0.3	V
		V _{CC} = 4.5 V	-	2.1	0.9	-	0.9	V
		V _{CC} = 6.0 V	-	2.9	1.2	-	1.2	V

^[2] For TSSOP8 package: above 55 °C the value of P_{tot} derates linearly with 2.5 mW/K. For VSSOP8 package: above 110 °C the value of P_{tot} derates linearly with 8 mW/K.

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Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL}						
	voltage	I _O = -20 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	V
		I _O = -20 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	V
		I _O = -20 μA; V _{CC} = 6.0 V	5.9	6.0	-	5.9	-	V
		$I_{\rm O}$ = -4.0 mA; $V_{\rm CC}$ = 4.5 V	4.13	4.32	-	3.7	-	V
		I _O = -5.2 mA; V _{CC} = 6.0 V	5.63	5.81	-	5.2	-	V
V_{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}						
		I _O = 20 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 6.0 V	-	0	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 4.5 V	-	0.15	0.33	-	0.4	V
		I _O = 5.2 mA; V _{CC} = 6.0 V	-	0.16	0.33	-	0.4	V
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	±1.0	-	±1.0	μA
I _{CC}	supply current	per input pin; $V_I = V_{CC}$ or GND; $I_O = 0A$; $V_{CC} = 6.0 \text{ V}$	-	-	10	-	20	μΑ
C _I	input capacitance		-	3.0	-	-	-	pF

^[1] All typical values are measured at T_{amb} = 25 °C.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 5.

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
t _{pd}	propagation delay	nA to nY; see Fig. 4 [2]						
		V _{CC} = 2.0 V	-	13	75	-	90	ns
		V _{CC} = 4.5 V	-	6	15	-	18	ns
		V _{CC} = 6.0 V	-	5	13	-	15	ns
t _t	transition time	nY; see Fig. 4 [3]						
		V _{CC} = 2.0 V	-	18	95	-	125	ns
		V _{CC} = 4.5 V	-	6	19	-	25	ns
		V _{CC} = 6.0 V	-	5	16	-	20	ns
C _{PD}	power dissipation capacitance	$V_I = GND \text{ to } V_{CC}$ [4]	-	5	-	-	-	pF

- All typical values are measured at T_{amb} = 25 °C.
- t_{pd} is the same as t_{PLH} and t_{PHL} .
- [3] t_t is the same as t_{TLH} and t_{THL}.
 [4] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

 C_L = output load capacitance in pF;

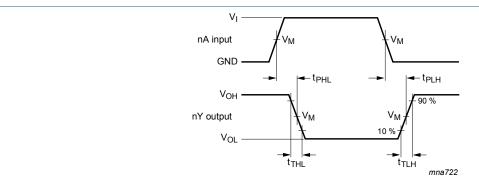
V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of outputs.

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11.1. Waveforms and test circuit

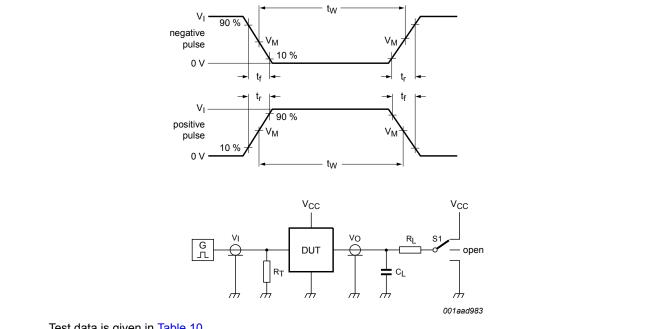


Measurement points are given in Table 9.

Propagation delay data input (nA) to data output (nY) and transition time output (nY)

Table 9. Measurement points

Туре	Input	Output	
	V _M	V _M	
74HC3GU04	0.5 × V _{CC}	0.5 × V _{CC}	



Test data is given in Table 10.

Definitions for test circuit:

 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator.

 C_L = Load capacitance including jig and probe capacitance.

R_L = Load resistance.

S1 = Test selection switch.

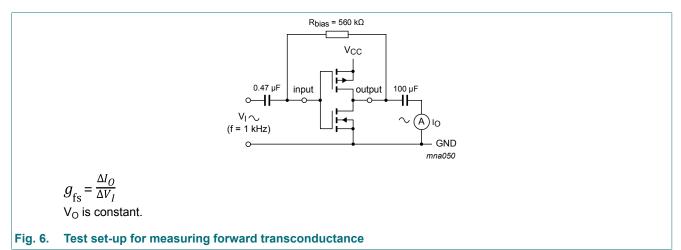
Test circuit for measuring switching times Fig. 5.

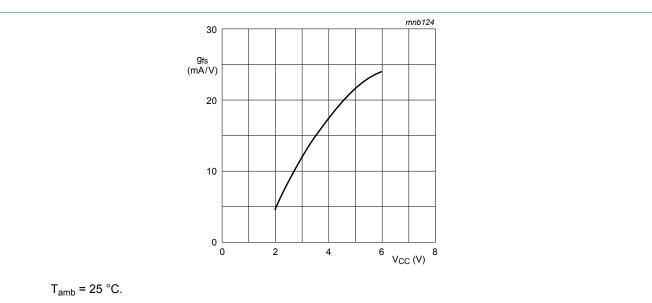
Table 10. Test data

Туре	Input		Load	S1 position	
	V _I	t _r , t _f	CL	R_L	t _{PHL} , t _{PLH}
74HC3GU04	GND to V _{CC}	≤ 6 ns	50 pF	1 kΩ	open

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11.2. Additional characteristics





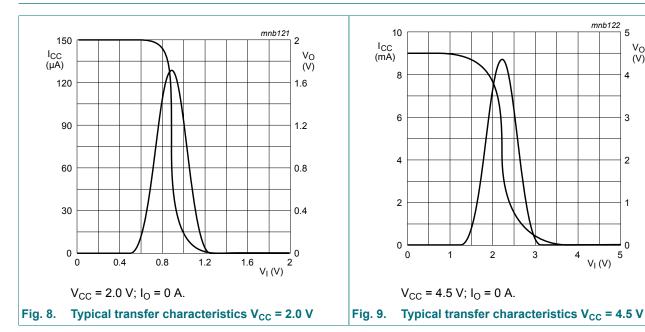
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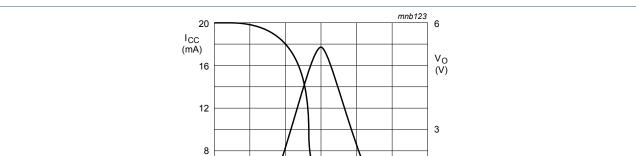
V_O (V)

3

2

12. Typical transfer characteristics





 V_{CC} = 6.0 V; I_{O} = 0 A.

Fig. 10. Typical transfer characteristics V_{CC} = 6.0 V

0

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 $V_{I}(V)$

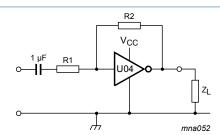
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13. Application information

Some applications for the 74HC3GU04 are:

- Linear amplifier (see <u>Fig. 11</u>)
- · Crystal oscillator (see Fig. 13).

All values given are typical values unless otherwise specified.



 $Z_L > 10 \text{ k}\Omega$.

R1 ≥ 3 k Ω .

 $R2 \le 1 M\Omega$.

Open loop amplification: $A_{OL} = 20$ (typical).

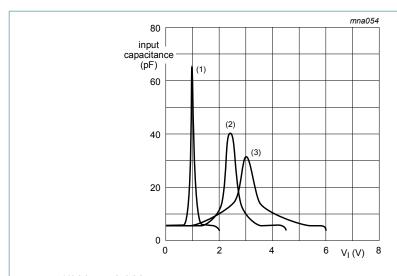
Voltage amplification:
$$A_V = -\frac{A_{\rm OL}}{1 + \frac{{\rm R1}}{{
m R2}} \left(1 + A_{\rm OL}\right)}$$
 .

 $V_{o(p-p)} = V_{CC}$ - 1.5 V centered at 0.5 × V_{CC} .

Unity gain bandwidth product is 5 MHz (typical).

Input capacitance see Fig. 12.

Fig. 11. Linear amplifier application



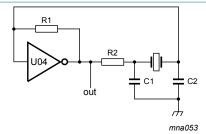
(1) $V_{CC} = 2.0 \text{ V}.$

(2) $V_{CC} = 4.5 \text{ V}$.

(3) $V_{CC} = 6.0 \text{ V}$.

Fig. 12. Typical input capacitance as a function of the input voltage

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Test data is given in <u>Table 11</u> and <u>Table 12</u>.

C1 = 47 pF (typical).

C2 = 22 pF (typical).

R1 = 1 M Ω to 10 M Ω (typical).

R2 optimum value depends on the frequency and required stability against changes in V_{CC} or average minimum I_{CC} .

(I_{CC} = 2 mA at V_{CC} = 3.0 V and f = 1 MHz.)

Fig. 13. Crystal oscillator application

Table 11. External components for resonator (f < 1 MHz)

Frequency	R1	R2	C1	C2
10 kHz to 15.9 kHz	2.2 ΜΩ	220 kΩ	56 pF	20 pF
16 kHz to 24.9 kHz	2.2 ΜΩ	220 kΩ	56 pF	10 pF
25 kHz to 54.9 kHz	2.2 ΜΩ	100 kΩ	56 pF	10 pF
55 kHz to 129.9 kHz	2.2 ΜΩ	100 kΩ	47 pF	5 pF
130 kHz to 199.9 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF
200 kHz to 349.9 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF
350 kHz to 600 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF

Table 12. Optimum value for R2

Frequency	R2	Optimum	
3 kHz	2.0 kΩ	minimum required I _{CC}	
	8.0 kΩ	minimum influence due to change in V _{CC}	
6 kHz	1.0 kΩ	minimum required I _{CC}	
	4.7 kΩ	minimum influence by V _{CC}	
10 kHz	0.5 kΩ	minimum required I _{CC}	
	2.0 kΩ	minimum influence by V _{CC}	
14 kHz	0.5 kΩ	minimum required I _{CC}	
	2.0 kΩ	minimum influence by V _{CC}	
> 14 kHz	replace R2 by C3 = 35 pF (typical)		

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14. Package outline

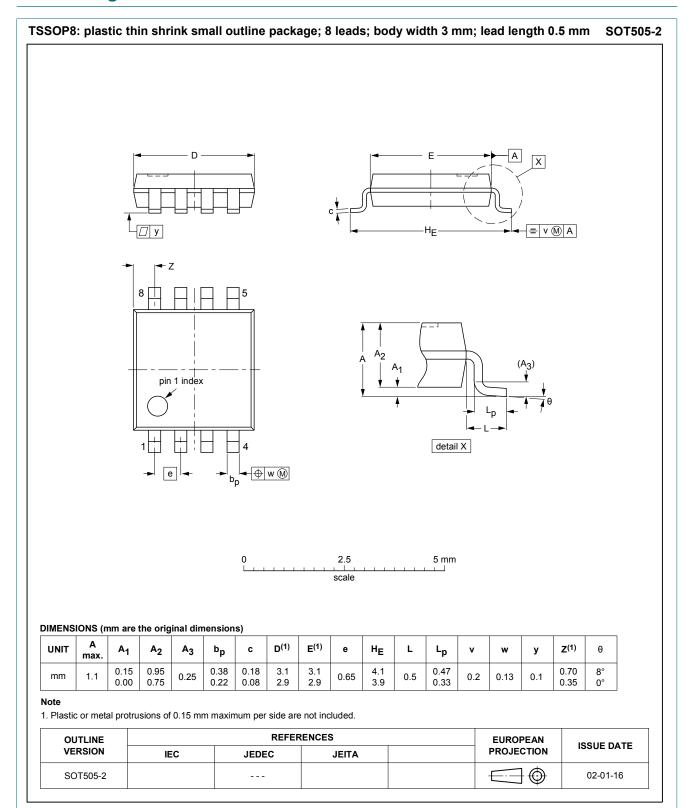


Fig. 14. Package outline SOT505-2 (TSSOP8)

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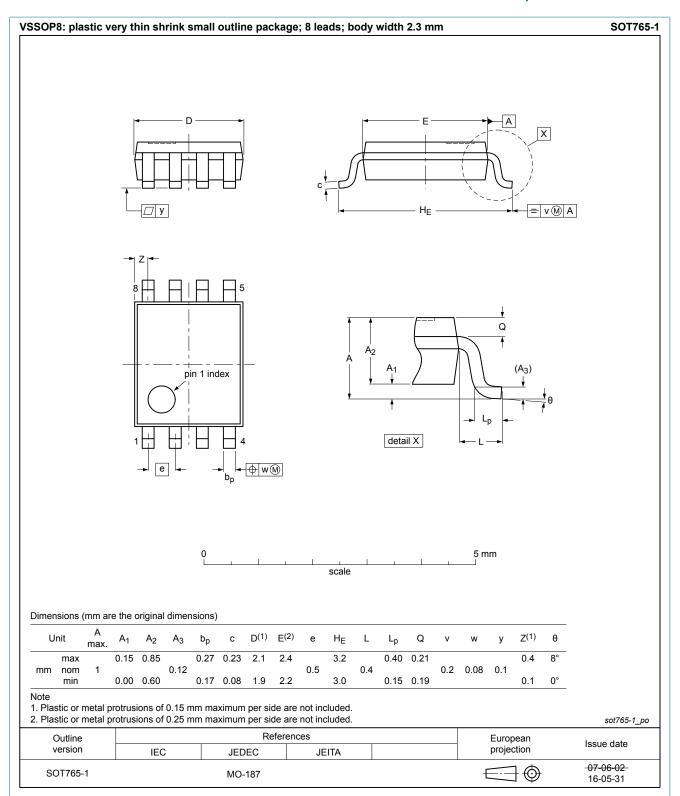


Fig. 15. Package outline SOT765-1 (VSSOP8)

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15. Abbreviations

Table 13. Abbreviations

Acronym	Description
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

16. Revision history

Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74HC3GU04 v.6	20190129	Product data sheet	-	74HC3GU04 v.5		
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type number 74HC3GU04GD (SOT996-2/XSON8) removed. 					
74HC3GU04 v.5	20131002	Product data sheet	-	74HC3GU04 v.4		
Modifications:	For type number 74HC3GU04GD XSON8U has changed to XSON8.					
74HC3GU04 v.4	20100111	Product data sheet	-	74HC3GU04 v.3		
Modifications:	Marking code for 74HC3GU04DP package changed from HU04 to HU4					
74HC3GU04 v.3	20090511	Product data sheet	-	74HC3GU04 v.2		
74HC3GU04 v.2	20031126	Product specification	-	74HC3GU04 v.1		
74HC3GU04 v.1	20030818	Product specification	-	-		

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17. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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