# 74HC366; 74HCT366

## Hex buffer/line driver; 3-state; inverting

Rev. 6 — 17 February 2021 Product data sheet

## 1. General description

The 74HC366; 74HCT366 is a hex inverting buffer/line driver with 3-state outputs controlled by the output enable inputs ($\overline{OE}n$). A HIGH on $\overline{OE}n$ causes the outputs to assume a high impedance OFF-state. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of $V_{CC}$.

## 2. Features and benefits

- Wide supply voltage range from 2.0 V to 6.0 V
- CMOS low power dissipation
- High noise immunity
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Complies with JEDEC standards:
  - JESD8C (2.7 V to 3.6 V)
  - JESD7A (2.0 V to 6.0 V)
- Inverting outputs
- Input levels:
  - For 74HC366: CMOS level
  - For 74HCT366: TTL level
- ESD protection:
- • HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

## 3. Ordering information

**Table 1. Ordering information**

| Type number | Package | | | |
|---|---|---|---|---|
| | Temperature range | Name | Description | Version |
| 74HC366D | -40 °C to +125 °C | SO16 | plastic small outline package; 16 leads; body width 3.9 mm | SOT109-1 |
| 74HCT366D | | | | |
| 74HC366PW | -40 °C to +125 °C | TSSOP16 | plastic thin shrink small outline package; 16 leads; body width 4.4 mm | SOT403-1 |
| 74HCT366PW | | | | |

## 4. Functional diagram

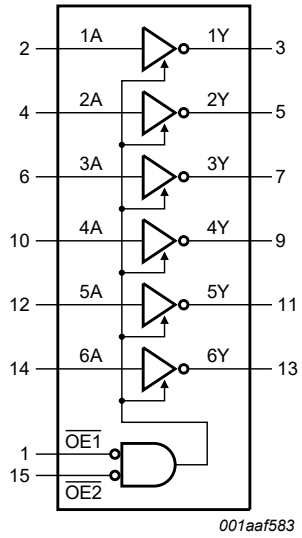



Fig. 1. Functional diagram

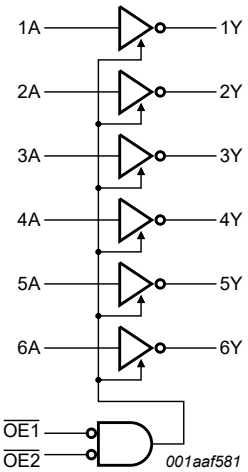



Fig. 2. Logic symbol

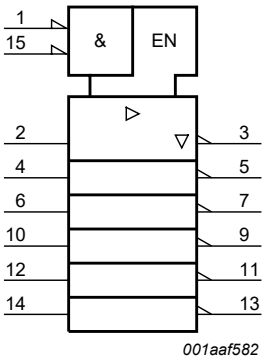



Fig. 3. IEC logic symbol

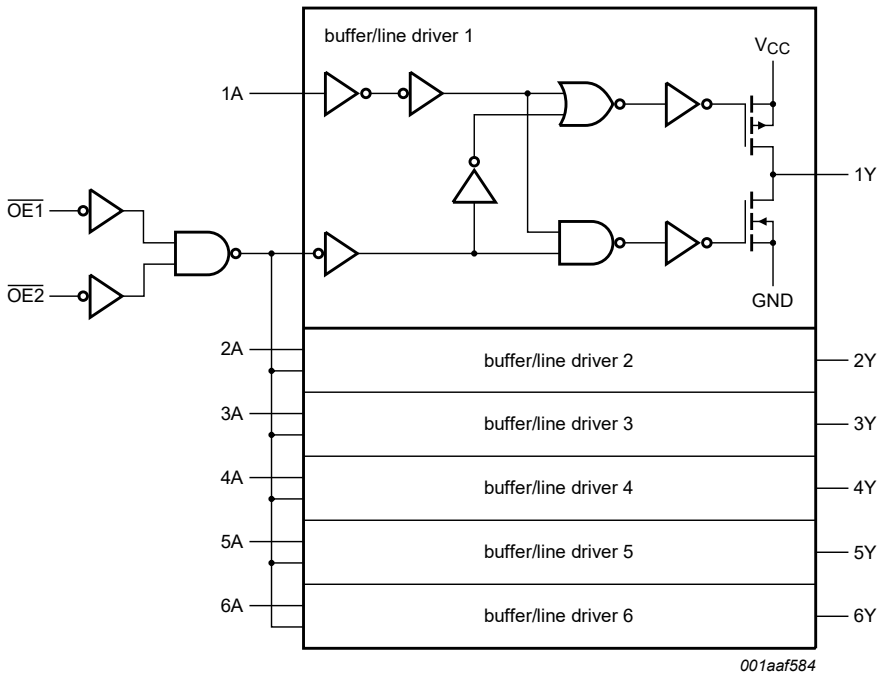



Fig. 4. Logic diagram

# 5. Pinning information

## 5.1. Pinning

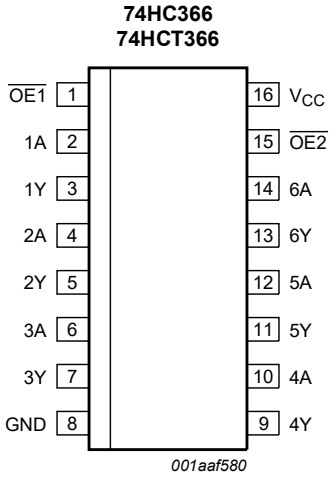



**Fig. 5. Pin configuration for SOT109-1 (SO16) and SOT403-1 (TSSOP16)**

## 5.2. Pin description

**Table 2. Pin description**

| Symbol | Pin | Description |
|---|---|---|
| $\overline{OE1}$, $\overline{OE2}$ | 1, 15 | output enable input (active LOW) |
| 1A, 2A, 3A, 4A, 5A, 6A | 2, 4, 6, 10, 12, 14 | data input |
| 1Y, 2Y, 3Y, 4Y, 5Y, 6Y | 3, 5, 7, 9, 11, 13 | data output |
| GND | 8 | ground (0 V) |
| $V_{CC}$ | 16 | supply voltage |

# 6. Functional description

**Table 3. Function table**

*H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.*

| Control | | Input | Output |
|---|---|---|---|
| **$\overline{OE1}$** | **$\overline{OE2}$** | **nA** | **nY** |
| L | L | L | H |
| L | L | H | L |
| X | H | X | Z |
| H | X | X | Z |

# 7. Limiting values

**Table 4. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).*

| Symbol | Parameter | Conditions | Min | Max | Unit |
|---|---|---|---|---|---|
| $V_{CC}$ | supply voltage | | -0.5 | +7 | V |
| $I_{IK}$ | input clamping current | $V_I < -0.5$ V or $V_I > V_{CC} + 0.5$ V | - | ±20 | mA |
| $I_{OK}$ | output clamping current | $V_O < -0.5$ V or $V_O > V_{CC} + 0.5$ V | - | ±20 | mA |
| $I_O$ | output current | $V_O = -0.5$ V to $(V_{CC} + 0.5$ V) | - | ±35 | mA |
| $I_{CC}$ | supply current | | - | 70 | mA |
| $I_{GND}$ | ground current | | - | -70 | mA |
| $T_{stg}$ | storage temperature | | -65 | +150 | °C |
| $P_{tot}$ | total power dissipation | [1] | - | 500 | mW |

[1] For SOT109-1 (SO16) package: $P_{tot}$ derates linearly with 12.4 mW/K above 110 °C.
For SOT403-1 (TSSOP16) package: $P_{tot}$ derates linearly with 8.5 mW/K above 91 °C.

# 8. Recommended operating conditions

**Table 5. Recommended operating conditions**

*Voltages are referenced to GND (ground = 0 V)*

| Symbol | Parameter | Conditions | 74HC366 | | | 74HCT366 | | | Unit |
|---|---|---|---|---|---|---|---|---|---|
| | | | Min | Typ | Max | Min | Typ | Max | |
| $V_{CC}$ | supply voltage | | 2.0 | 5.0 | 6.0 | 4.5 | 5.0 | 5.5 | V |
| $V_I$ | input voltage | | 0 | - | $V_{CC}$ | 0 | - | $V_{CC}$ | V |
| $V_O$ | output voltage | | 0 | - | $V_{CC}$ | 0 | - | $V_{CC}$ | V |
| $T_{amb}$ | ambient temperature | | -40 | +25 | +125 | -40 | +25 | +125 | °C |
| Δt/ΔV | input transition rise and fall rate | $V_{CC}$ = 2.0 V | - | - | 625 | - | - | - | ns/V |
| | | $V_{CC}$ = 4.5 V | - | 1.67 | 139 | - | 1.67 | 139 | ns/V |
| | | $V_{CC}$ = 6.0 V | - | - | 83 | - | - | - | ns/V |

# 9. Static characteristics

**Table 6. Static characteristics 74HC366**

*At recommended operating conditions; voltages are referenced to GND (ground = 0 V).*

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| **$T_{amb}$ = 25 °C** | | | | | | |
| $V_{IH}$ | HIGH-level input voltage | $V_{CC}$ = 2.0 V | 1.5 | 1.2 | - | V |
| | | $V_{CC}$ = 4.5 V | 3.15 | 2.4 | - | V |
| | | $V_{CC}$ = 6.0 V | 4.2 | 3.2 | - | V |
| $V_{IL}$ | LOW-level input voltage | $V_{CC}$ = 2.0 V | - | 0.8 | 0.5 | V |
| | | $V_{CC}$ = 4.5 V | - | 2.1 | 1.35 | V |
| | | $V_{CC}$ = 6.0 V | - | 2.8 | 1.8 | V |
| $V_{OH}$ | HIGH-level output voltage | $V_I$ = $V_{IH}$ or $V_{IL}$ | - | - | - | |
| | | $I_O$ = -20 µA; $V_{CC}$ = 2.0 V | 1.9 | 2.0 | - | V |
| | | $I_O$ = -20 µA; $V_{CC}$ = 4.5 V | 4.4 | 4.5 | - | V |
| | | $I_O$ = -20 µA; $V_{CC}$ = 6.0 V | 5.9 | 6.0 | - | V |
| | | $I_O$ = -6.0 mA; $V_{CC}$ = 4.5 V | 3.98 | 4.32 | - | V |
| | | $I_O$ = -7.8 mA; $V_{CC}$ = 6.0 V | 5.48 | 5.81 | - | V |
| $V_{OL}$ | LOW-level output voltage | $V_I$ = $V_{IH}$ or $V_{IL}$ | | | | |
| | | $I_O$ = 20 µA; $V_{CC}$ = 2.0 V | - | 0 | 0.1 | V |
| | | $I_O$ = 20 µA; $V_{CC}$ = 4.5 V | - | 0 | 0.1 | V |
| | | $I_O$ = 20 µA; $V_{CC}$ = 6.0 V | - | 0 | 0.1 | V |
| | | $I_O$ = 6.0 mA; $V_{CC}$ = 4.5 V | - | 0.15 | 0.26 | V |
| | | $I_O$ = 7.8 mA; $V_{CC}$ = 6.0 V | - | 0.16 | 0.26 | V |
| $I_I$ | input leakage current | $V_I$ = $V_{CC}$ or GND; $V_{CC}$ = 6.0 V | - | - | ±0.1 | µA |
| $I_{OZ}$ | OFF-state output current | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_O$ = $V_{CC}$ or GND; $V_{CC}$ = 6.0 V | - | - | ±0.5 | µA |
| $I_{CC}$ | supply current | $V_I$ = $V_{CC}$ or GND; $I_O$ = 0 A; $V_{CC}$ = 6.0 V | - | - | 8.0 | µA |
| $C_I$ | input capacitance | | - | 3.5 | - | pF |

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| **$T_{amb}$ = -40 °C to +85 °C** | | | | | | |
| $V_{IH}$ | HIGH-level input voltage | $V_{CC}$ = 2.0 V | 1.5 | - | - | V |
| | | $V_{CC}$ = 4.5 V | 3.15 | - | - | V |
| | | $V_{CC}$ = 6.0 V | 4.2 | - | - | V |
| $V_{IL}$ | LOW-level input voltage | $V_{CC}$ = 2.0 V | - | - | 0.5 | V |
| | | $V_{CC}$ = 4.5 V | - | - | 1.35 | V |
| | | $V_{CC}$ = 6.0 V | - | - | 1.8 | V |
| $V_{OH}$ | HIGH-level output voltage | $V_I = V_{IH}$ or $V_{IL}$ | | | | |
| | | $I_O$ = -20 µA; $V_{CC}$ = 2.0 V | 1.9 | - | - | V |
| | | $I_O$ = -20 µA; $V_{CC}$ = 4.5 V | 4.4 | - | - | V |
| | | $I_O$ = -20 µA; $V_{CC}$ = 6.0 V | 5.9 | - | - | V |
| | | $I_O$ = -6.0 mA; $V_{CC}$ = 4.5 V | 3.84 | - | - | V |
| | | $I_O$ = -7.8 mA; $V_{CC}$ = 6.0 V | 5.34 | - | - | V |
| $V_{OL}$ | LOW-level output voltage | $V_I = V_{IH}$ or $V_{IL}$ | | | | |
| | | $I_O$ = 20 µA; $V_{CC}$ = 2.0 V | - | - | 0.1 | V |
| | | $I_O$ = 20 µA; $V_{CC}$ = 4.5 V | - | - | 0.1 | V |
| | | $I_O$ = 20 µA; $V_{CC}$ = 6.0 V | - | - | 0.1 | V |
| | | $I_O$ = 6.0 mA; $V_{CC}$ = 4.5 V | - | - | 0.33 | V |
| | | $I_O$ = 7.8 mA; $V_{CC}$ = 6.0 V | - | - | 0.33 | V |
| $I_I$ | input leakage current | $V_I = V_{CC}$ or GND; $V_{CC}$ = 6.0 V; | - | - | ±1.0 | µA |
| $I_{OZ}$ | OFF-state output current | $V_I = V_{IH}$ or $V_{IL}$; $V_O = V_{CC}$ or GND; $V_{CC}$ = 6.0 V | - | - | ±5.0 | µA |
| $I_{CC}$ | supply current | $V_I = V_{CC}$ or GND; $I_O$ = 0 A; $V_{CC}$ = 6.0 V | - | - | 80 | µA |
| **$T_{amb}$ = -40 °C to +125 °C** | | | | | | |
| $V_{IH}$ | HIGH-level input voltage | $V_{CC}$ = 2.0 V | 1.5 | - | - | V |
| | | $V_{CC}$ = 4.5 V | 3.15 | - | - | V |
| | | $V_{CC}$ = 6.0 V | 4.2 | - | - | V |
| $V_{IL}$ | LOW-level input voltage | $V_{CC}$ = 2.0 V | - | - | 0.5 | V |
| | | $V_{CC}$ = 4.5 V | - | - | 1.35 | V |
| | | $V_{CC}$ = 6.0 V | - | - | 1.8 | V |
| $V_{OH}$ | HIGH-level output voltage | $V_I = V_{IH}$ or $V_{IL}$ | | | | |
| | | $I_O$ = -20 µA; $V_{CC}$ = 2.0 V | 1.9 | - | - | V |
| | | $I_O$ = -20 µA; $V_{CC}$ = 4.5 V | 4.4 | - | - | V |
| | | $I_O$ = -20 µA; $V_{CC}$ = 6.0 V | 5.9 | - | - | V |
| | | $I_O$ = -6.0 mA; $V_{CC}$ = 4.5 V | 3.7 | - | - | V |
| | | $I_O$ = -7.8 mA; $V_{CC}$ = 6.0 V | 5.2 | - | - | V |
| $V_{OL}$ | LOW-level output voltage | $V_I = V_{IH}$ or $V_{IL}$ | | | | |
| | | $I_O$ = 20 µA; $V_{CC}$ = 2.0 V | - | - | 0.1 | V |
| | | $I_O$ = 20 µA; $V_{CC}$ = 4.5 V | - | - | 0.1 | V |
| | | $I_O$ = 20 µA; $V_{CC}$ = 6.0 V | - | - | 0.1 | V |
| | | $I_O$ = 6.0 mA; $V_{CC}$ = 4.5 V | - | - | 0.4 | V |
| | | $I_O$ = 7.8 mA; $V_{CC}$ = 6.0 V | - | - | 0.4 | V |
| $I_I$ | input leakage current | $V_I = V_{CC}$ or GND; $V_{CC}$ = 6.0 V | - | - | ±1.0 | µA |
| $I_{OZ}$ | OFF-state output current | $V_I = V_{IH}$ or $V_{IL}$; $V_O = V_{CC}$ or GND; $V_{CC}$ = 6.0 V | - | - | ±10.0 | µA |
| $I_{CC}$ | supply current | $V_I = V_{CC}$ or GND; $I_O$ = 0 A; $V_{CC}$ = 6.0 V | - | - | 160 | µA |

**Table 7. Static characteristics 74HCT366**

*At recommended operating conditions; voltages are referenced to GND (ground = 0 V).*

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| **$T_{amb}$ = 25 °C** | | | | | | |
| $V_{IH}$ | HIGH-level input voltage | $V_{CC}$ = 4.5 V to 5.5 V | 2.0 | 1.6 | - | V |
| $V_{IL}$ | LOW-level input voltage | $V_{CC}$ = 4.5 V to 5.5 V | - | 1.2 | 0.8 | V |
| $V_{OH}$ | HIGH-level output voltage | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_{CC}$ = 4.5 V | | | | |
| | | $I_O$ = -20 µA | 4.4 | 4.5 | - | V |
| | | $I_O$ = -6.0 mA | 3.98 | 4.32 | - | V |
| $V_{OL}$ | LOW-level output voltage | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_{CC}$ = 4.5 V | | | | |
| | | $I_O$ = 20 µA | - | 0 | 0.1 | V |
| | | $I_O$ = 6.0 mA | - | 0.16 | 0.26 | V |
| $I_I$ | input leakage current | $V_I$ = $V_{CC}$ or GND; $V_{CC}$ = 5.5 V | - | - | ±0.1 | µA |
| $I_{OZ}$ | OFF-state output current | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_O$ = $V_{CC}$ or GND; $V_{CC}$ = 5.5 V | - | - | ±0.5 | µA |
| $I_{CC}$ | supply current | $V_I$ = $V_{CC}$ or GND; $I_O$ = 0 A; $V_{CC}$ = 5.5 V | - | - | 8.0 | µA |
| $\Delta I_{CC}$ | additional supply current | $V_I$ = $V_{CC}$ - 2.1 V; other inputs at $V_{CC}$ or GND; $I_O$ = 0 A | | | | |
| | | pins nA | - | 100 | 360 | µA |
| | | pin $\overline{OE1}$ | - | 100 | 360 | µA |
| | | pin $\overline{OE2}$ | - | 90 | 320 | µA |
| $C_I$ | input capacitance | | - | 3.5 | - | pF |
| **$T_{amb}$ = -40 °C to +85 °C** | | | | | | |
| $V_{IH}$ | HIGH-level input voltage | $V_{CC}$ = 4.5 V to 5.5 V | 2.0 | - | - | V |
| $V_{IL}$ | LOW-level input voltage | $V_{CC}$ = 4.5 V to 5.5 V | - | - | 0.8 | V |
| $V_{OH}$ | HIGH-level output voltage | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_{CC}$ = 4.5 V | | | | |
| | | $I_O$ = -20 µA | 4.4 | - | - | V |
| | | $I_O$ = -6.0 mA | 3.84 | - | - | V |
| $V_{OL}$ | LOW-level output voltage | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_{CC}$ = 4.5 V | | | | |
| | | $I_O$ = 20 µA | - | - | 0.1 | V |
| | | $I_O$ = 6.0 mA | - | - | 0.33 | V |
| $I_I$ | input leakage current | $V_I$ = $V_{CC}$ or GND; $V_{CC}$ = 5.5 V | - | - | ±1.0 | µA |
| $I_{OZ}$ | OFF-state output current | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_O$ = $V_{CC}$ or GND; $V_{CC}$ = 5.5 V | | | ±5.0 | µA |
| $I_{CC}$ | supply current | $V_I$ = $V_{CC}$ or GND; $I_O$ = 0 A; $V_{CC}$ = 5.5 V | - | - | 80 | µA |
| $\Delta I_{CC}$ | additional supply current | $V_I$ = $V_{CC}$ - 2.1 V; other inputs at $V_{CC}$ or GND; $I_O$ = 0 A | | | | |
| | | pins nA | - | - | 450 | µA |
| | | pin $\overline{OE1}$ | - | - | 450 | µA |
| | | pin $\overline{OE2}$ | - | - | 400 | µA |

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| **$T_{amb}$ = -40 °C to +125 °C** | | | | | | |
| $V_{IH}$ | HIGH-level input voltage | $V_{CC}$ = 4.5 V to 5.5 V | 2.0 | - | - | V |
| $V_{IL}$ | LOW-level input voltage | $V_{CC}$ = 4.5 V to 5.5 V | - | - | 0.8 | V |
| $V_{OH}$ | HIGH-level output voltage | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_{CC}$ = 4.5 V | | | | |
| | | $I_O$ = -20 µA | 4.4 | - | - | V |
| | | $I_O$ = -6.0 mA | 3.7 | - | - | V |
| $V_{OL}$ | LOW-level output voltage | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_{CC}$ = 4.5 V | | | | |
| | | $I_O$ = 20 µA | - | - | 0.1 | V |
| | | $I_O$ = 6.0 mA | - | - | 0.4 | V |
| $I_I$ | input leakage current | $V_I$ = $V_{CC}$ or GND; $V_{CC}$ = 5.5 V | - | - | ±1.0 | µA |
| $I_{OZ}$ | OFF-state output current | $V_I$ = $V_{IH}$ or $V_{IL}$; $V_O$ = $V_{CC}$ or GND; $V_{CC}$ = 5.5 V | - | - | ±10.0 | µA |
| $I_{CC}$ | supply current | $V_I$ = $V_{CC}$ or GND; $I_O$ = 0 A; $V_{CC}$ = 5.5 V | - | - | 160 | µA |
| $\Delta I_{CC}$ | additional supply current | $V_I$ = $V_{CC}$ - 2.1 V; other inputs at $V_{CC}$ or GND; $I_O$ = 0 A | | | | |
| | | pins nA | - | - | 490 | µA |
| | | pin $\overline{OE1}$ | - | - | 490 | µA |
| | | pin $\overline{OE2}$ | - | - | 441 | µA |

## 10. Dynamic characteristics

**Table 8. Dynamic characteristics 74HC366**

*Voltages are referenced to GND (ground = 0 V); $C_L$ = 50 pF unless otherwise specified; see test circuit Fig. 8.*

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| **$T_{amb}$ = 25 °C** | | | | | | |
| $t_{pd}$ | propagation delay | nA to nY; see Fig. 6 [1] | | | | |
| | | $V_{CC}$ = 2.0 V | - | 33 | 100 | ns |
| | | $V_{CC}$ = 4.5 V | - | 12 | 20 | ns |
| | | $V_{CC}$ = 5 V; $C_L$ = 15 pF | - | 10 | - | ns |
| | | $V_{CC}$ = 6.0 V | - | 10 | 17 | ns |
| $t_{en}$ | enable time | $\overline{OEn}$ to nY; see Fig. 7 [2] | | | | |
| | | $V_{CC}$ = 2.0 V | - | 44 | 150 | ns |
| | | $V_{CC}$ = 4.5 V | - | 16 | 30 | ns |
| | | $V_{CC}$ = 6.0 V | - | 13 | 26 | ns |
| $t_{dis}$ | disable time | $\overline{OEn}$ to nY; see Fig. 7 [3] | | | | |
| | | $V_{CC}$ = 2.0 V | - | 55 | 150 | ns |
| | | $V_{CC}$ = 4.5 V | - | 20 | 30 | ns |
| | | $V_{CC}$ = 6.0 V | - | 16 | 26 | ns |
| $t_t$ | transition time | see Fig. 6 [4] | | | | |
| | | $V_{CC}$ = 2.0 V | - | 14 | 60 | ns |
| | | $V_{CC}$ = 4.5 V | - | 5 | 12 | ns |
| | | $V_{CC}$ = 6.0 V | - | 4 | 10 | ns |
| $C_{PD}$ | power dissipation capacitance | per buffer; $V_I$ = GND to $V_{CC}$ [5] | - | 30 | - | pF |

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| **$T_{amb}$ = -40 °C to +85 °C** | | | | | | |
| $t_{pd}$ | propagation delay | nA to nY; see Fig. 6 [1] | | | | |
| | | $V_{CC}$ = 2.0 V | - | - | 125 | ns |
| | | $V_{CC}$ = 4.5 V | - | - | 25 | ns |
| | | $V_{CC}$ = 6.0 V | - | - | 21 | ns |
| $t_{en}$ | enable time | $\overline{OE}n$ to nY; see Fig. 7 [2] | | | | |
| | | $V_{CC}$ = 2.0 V | - | - | 190 | ns |
| | | $V_{CC}$ = 4.5 V | - | - | 38 | ns |
| | | $V_{CC}$ = 6.0 V | - | - | 33 | ns |
| $t_{dis}$ | disable time | $\overline{OE}n$ to nY; see Fig. 7 [3] | | | | |
| | | $V_{CC}$ = 2.0 V | - | - | 190 | ns |
| | | $V_{CC}$ = 4.5 V | - | - | 38 | ns |
| | | $V_{CC}$ = 6.0 V | - | - | 33 | ns |
| $t_t$ | transition time | see Fig. 6 [4] | | | | |
| | | $V_{CC}$ = 2.0 V | - | - | 75 | ns |
| | | $V_{CC}$ = 4.5 V | - | - | 15 | ns |
| | | $V_{CC}$ = 6.0 V | - | - | 13 | ns |
| **$T_{amb}$ = -40 °C to +125 °C** | | | | | | |
| $t_{pd}$ | propagation delay | nA to nY; see Fig. 6 [1] | | | | |
| | | $V_{CC}$ = 2.0 V | - | - | 150 | ns |
| | | $V_{CC}$ = 4.5 V | - | - | 30 | ns |
| | | $V_{CC}$ = 6.0 V | - | - | 26 | ns |
| $t_{en}$ | enable time | $\overline{OE}n$ to nY; see Fig. 7 [2] | | | | |
| | | $V_{CC}$ = 2.0 V | - | - | 225 | ns |
| | | $V_{CC}$ = 4.5 V | - | - | 45 | ns |
| | | $V_{CC}$ = 6.0 V | - | - | 38 | ns |
| $t_{dis}$ | disable time | $\overline{OE}n$ to nY; see Fig. 7 [3] | | | | |
| | | $V_{CC}$ = 2.0 V | - | - | 225 | ns |
| | | $V_{CC}$ = 4.5 V | - | - | 45 | ns |
| | | $V_{CC}$ = 6.0 V | - | - | 38 | ns |
| $t_t$ | transition time | see Fig. 6 [4] | | | | |
| | | $V_{CC}$ = 2.0 V | - | - | 90 | ns |
| | | $V_{CC}$ = 4.5 V | - | - | 18 | ns |
| | | $V_{CC}$ = 6.0 V | - | - | 15 | ns |

[1] $t_{pd}$ is the same as $t_{PHL}$ and $t_{PLH}$.
[2] $t_{en}$ is the same as $t_{PZH}$ and $t_{PZL}$.
[3] $t_{dis}$ is the same as $t_{PHZ}$ and $t_{PLZ}$.
[4] $t_t$ is the same as $t_{THL}$ and $t_{TLH}$.
[5] $C_{PD}$ is used to determine the dynamic power dissipation ($P_D$ in µW).
$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:
$f_i$ = input frequency in MHz;
$f_o$ = output frequency in MHz;
$C_L$ = output load capacitance in pF;
$V_{CC}$ = supply voltage in V;
N = number of inputs switching;
$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

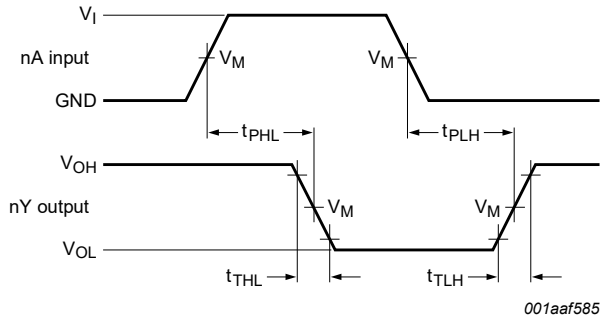
**Table 9. Dynamic characteristics 74HCT366**

*Voltages are referenced to GND (ground = 0 V); $C_L$ = 50 pF unless otherwise specified; see test circuit Fig. 8.*

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| **$T_{amb}$ = 25 °C** | | | | | | | |
| $t_{pd}$ | propagation delay | nA to nY; see Fig. 6 | [1] | | | | |
| | | $V_{CC}$ = 4.5 V | | - | 13 | 24 | ns |
| | | $V_{CC}$ = 5 V; $C_L$ = 15 pF | | - | 11 | - | ns |
| $t_{en}$ | enable time | $\overline{OEn}$ to nY; $V_{CC}$ = 4.5 V; see Fig. 7 | [2] | - | 16 | 35 | ns |
| $t_{dis}$ | disable time | $\overline{OEn}$ to nY; $V_{CC}$ = 4.5 V; see Fig. 7 | [3] | - | 20 | 35 | ns |
| $t_t$ | transition time | $V_{CC}$ = 4.5 V; see Fig. 6 | [4] | - | 5 | 12 | ns |
| $C_{PD}$ | power dissipation capacitance | per buffer; $V_I$ = GND to ($V_{CC}$ - 1.5 V) | [5] | - | 30 | - | pF |
| **$T_{amb}$ = -40 °C to +85 °C** | | | | | | | |
| $t_{pd}$ | propagation delay | nA to nY; $V_{CC}$ = 4.5 V; see Fig. 6 | [1] | - | - | 30 | ns |
| $t_{en}$ | enable time | $\overline{OEn}$ to nY; $V_{CC}$ = 4.5 V; see Fig. 7 | [2] | - | - | 44 | ns |
| $t_{dis}$ | disable time | $\overline{OEn}$ to nY; $V_{CC}$ = 4.5 V; see Fig. 7 | [3] | - | - | 44 | ns |
| $t_t$ | transition time | $V_{CC}$ = 4.5 V; see Fig. 6 | [4] | - | - | 15 | ns |
| **$T_{amb}$ = -40 °C to +125 °C** | | | | | | | |
| $t_{pd}$ | propagation delay | nA to nY; $V_{CC}$ = 4.5 V; see Fig. 6 | [1] | - | - | 36 | ns |
| $t_{en}$ | enable time | $\overline{OEn}$ to nY; $V_{CC}$ = 4.5 V; see Fig. 7 | [2] | - | - | 53 | ns |
| $t_{dis}$ | disable time | $\overline{OEn}$ to nY; $V_{CC}$ = 4.5 V; see Fig. 7 | [3] | - | - | 53 | ns |
| $t_t$ | transition time | $V_{CC}$ = 4.5 V; see Fig. 6 | [4] | - | - | 18 | ns |

[1] $t_{pd}$ is the same as $t_{PHL}$ and $t_{PLH}$.
[2] $t_{en}$ is the same as $t_{PZH}$ and $t_{PZL}$.
[3] $t_{dis}$ is the same as $t_{PHZ}$ and $t_{PLZ}$.
[4] $t_t$ is the same as $t_{THL}$ and $t_{TLH}$.
[5] $C_{PD}$ is used to determine the dynamic power dissipation ($P_D$ in µW).
$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:
$f_i$ = input frequency in MHz;
$f_o$ = output frequency in MHz;
$C_L$ = output load capacitance in pF;
$V_{CC}$ = supply voltage in V;
N = number of inputs switching;
$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

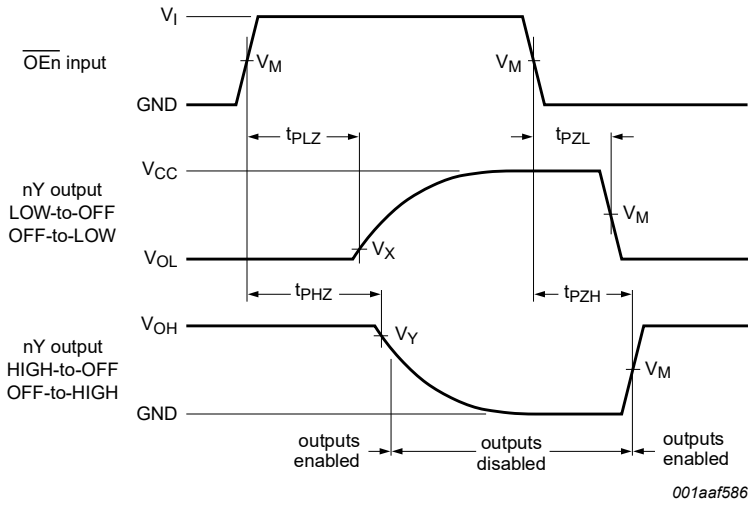
## 10.1. Waveforms and test circuit




Measurement points are given in Table 10.

$V_{OL}$ and $V_{OH}$ are typical output voltage levels that occur with the output load.

**Fig. 6. Propagation delay data input (nA) to output (nY) and output transition time**



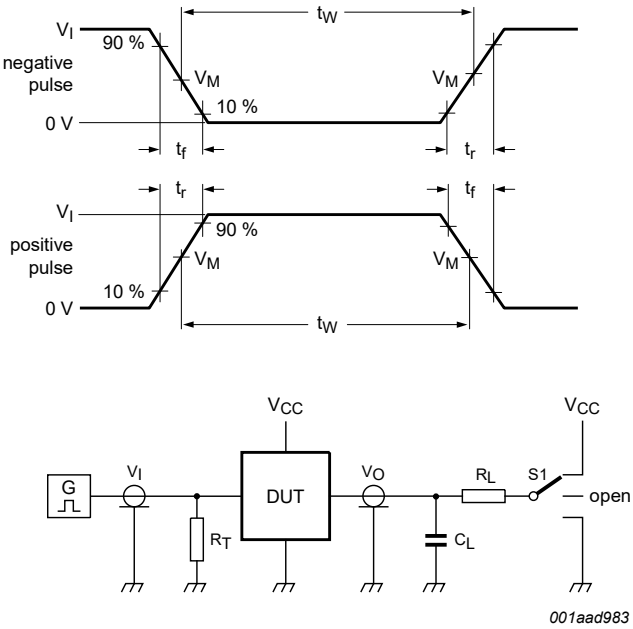

Measurement points are given in Table 10.

$V_{OL}$ and $V_{OH}$ are typical output voltage levels that occur with the output load.

**Fig. 7. 3-state enable and disable times**

**Table 10. Measurement points**

| Type | Input | Output | | |
|---|---|---|---|---|
| | $V_M$ | $V_M$ | $V_X$ | $V_Y$ |
| 74HC366 | $0.5V_{CC}$ | $0.5V_{CC}$ | $0.1 \times V_{CC}$ | $0.9 \times V_{CC}$ |
| 74HCT366 | 1.3 V | 1.3 V | $0.1 \times V_{CC}$ | $0.9 \times V_{CC}$ |




Test data is given in Table 11.

Definitions test circuit:

$R_T$ = Termination resistance should be equal to output impedance $Z_o$ of the pulse generator

$C_L$ = Load capacitance including jig and probe capacitance

$R_L$ = Load resistance

S1 = Test selection switch

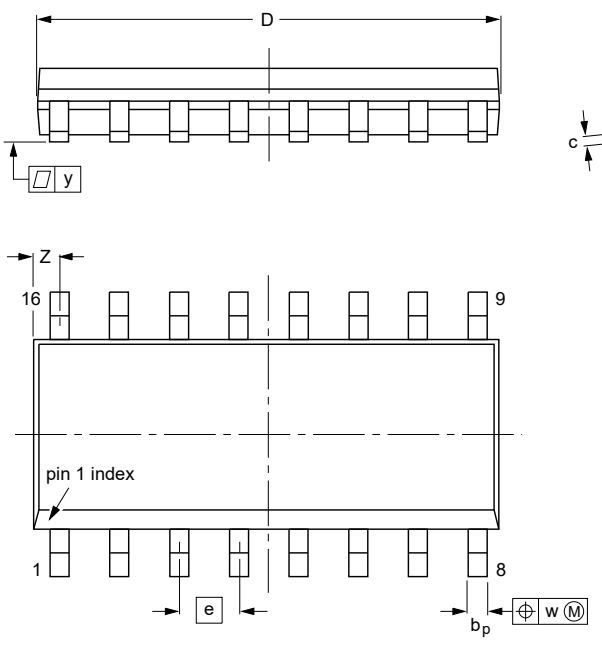
**Fig. 8. Test circuit for measuring switching times**

**Table 11. Test data**

| Type | Input | | Load | | S1 position | | |
|---|---|---|---|---|---|---|---|
| | $V_I$ | $t_r$, $t_f$ | $C_L$ | $R_L$ | $t_{PHL}$, $t_{PLH}$ | $t_{PZH}$, $t_{PHZ}$ | $t_{PZL}$, $t_{PLZ}$ |
| 74HC366 | $V_{CC}$ | 6 ns | 15 pF, 50 pF | 1 kΩ | open | GND | $V_{CC}$ |
| 74HCT366 | 3 V | 6 ns | 15 pF, 50 pF | 1 kΩ | open | GND | $V_{CC}$ |

# 11. Package outline

**SO16: plastic small outline package; 16 leads; body width 3.9 mm** **SOT109-1**




**DIMENSIONS (inch dimensions are derived from the original mm dimensions)**

| UNIT | A max. | $A_1$ | $A_2$ | $A_3$ | $b_p$ | c | $D^{(1)}$ | $E^{(1)}$ | e | $H_E$ | L | $L_p$ | Q | v | w | y | $Z^{(1)}$ | θ |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| mm | 1.75 | 0.25<br>0.10 | 1.45<br>1.25 | 0.25 | 0.49<br>0.36 | 0.25<br>0.19 | 10.0<br>9.8 | 4.0<br>3.8 | 1.27 | 6.2<br>5.8 | 1.05 | 1.0<br>0.4 | 0.7<br>0.6 | 0.25 | 0.25 | 0.1 | 0.7<br>0.3 | 8°<br>0° |
| inches | 0.069 | 0.010<br>0.004 | 0.057<br>0.049 | 0.01 | 0.019<br>0.014 | 0.0100<br>0.0075 | 0.39<br>0.38 | 0.16<br>0.15 | 0.05 | 0.244<br>0.228 | 0.041 | 0.039<br>0.016 | 0.028<br>0.020 | 0.01 | 0.01 | 0.004 | 0.028<br>0.012 | |

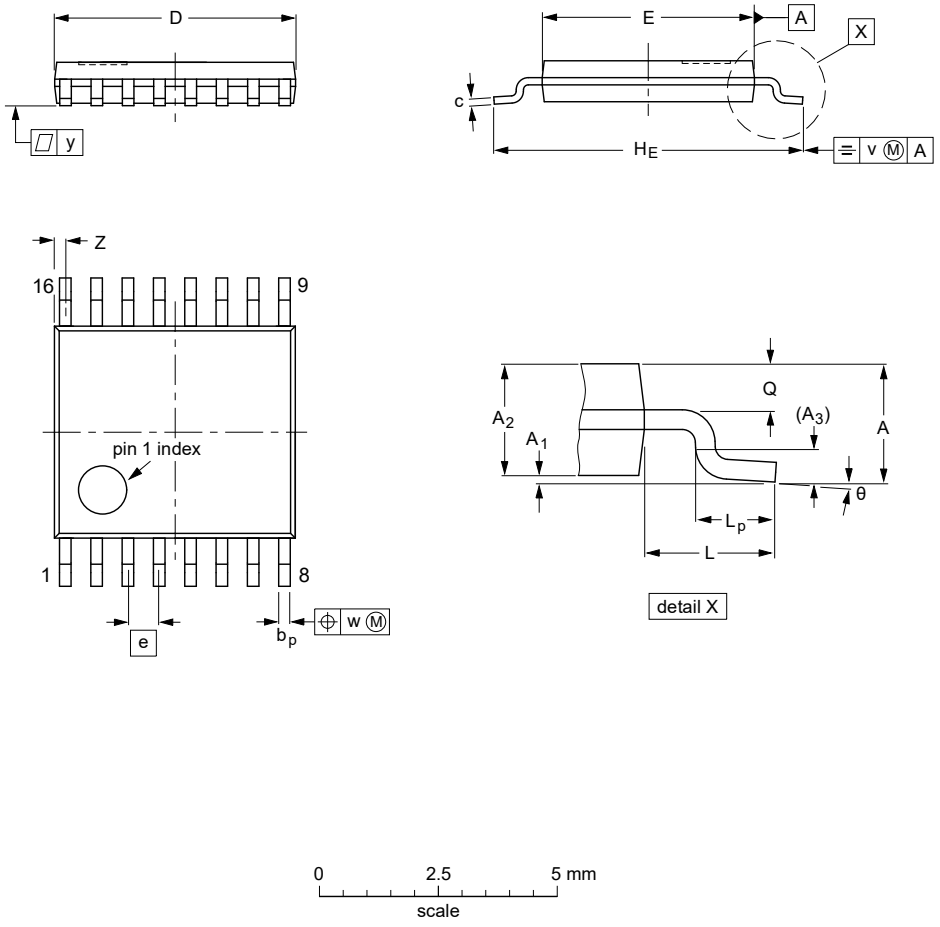
**Note**

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

| OUTLINE VERSION | REFERENCES | | | | EUROPEAN PROJECTION | ISSUE DATE |
|---|---|---|---|---|---|---|
| | IEC | JEDEC | JEITA | | | |
| SOT109-1 | 076E07 | MS-012 | | | | ~~99-12-27~~<br>03-02-19 |

**Fig. 9. Package outline SOT109-1 (SO16)**

**TSSOP16: plastic thin shrink small outline package; 16 leads; body width 4.4 mm** **SOT403-1**




**DIMENSIONS (mm are the original dimensions)**

| UNIT | A max. | $A_1$ | $A_2$ | $A_3$ | $b_p$ | c | D (1) | E (2) | e | $H_E$ | L | $L_p$ | Q | v | w | y | Z (1) | θ |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| mm | 1.1 | 0.15<br>0.05 | 0.95<br>0.80 | 0.25 | 0.30<br>0.19 | 0.2<br>0.1 | 5.1<br>4.9 | 4.5<br>4.3 | 0.65 | 6.6<br>6.2 | 1 | 0.75<br>0.50 | 0.4<br>0.3 | 0.2 | 0.13 | 0.1 | 0.40<br>0.06 | 8°<br>0° |

**Notes**

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

| OUTLINE VERSION | REFERENCES | | | | EUROPEAN PROJECTION | ISSUE DATE |
|---|---|---|---|---|---|---|
| | IEC | JEDEC | JEITA | | | |
| SOT403-1 | | MO-153 | | | | ~~99-12-27~~<br>03-02-18 |

**Fig. 10. Package outline SOT403-1 (TSSOP16)**

## 12. Abbreviations

**Table 12. Abbreviations**

| Acronym | Description |
|---|---|
| CMOS | Complementary Metal Oxide Semiconductor |
| DUT | Device Under Test |
| ESD | ElectroStatic Discharge |
| HBM | Human Body Model |
| MM | Machine Model |

## 13. Revision history

**Table 13. Revision history**

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|---|---|---|---|---|
| 74HC_HCT366 v.6 | 20210217 | Product data sheet | - | 74HC_HCT366 v.5 |
| Modifications: | • The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.<br>• Legal texts have been adapted to the new company name where appropriate.<br>• Section 2 updated.<br>• Section 7: Derating values for $P_{tot}$ total power dissipation updated.<br>• Type number 74HCT366DB (SOT338-1 / SSOP16) removed. | | | |
| 74HC_HCT366 v.5 | 20160202 | Product data sheet | - | 74HC_HCT366 v.4 |
| Modifications: | • Type numbers 74HC366N and 74HCT366N (SOT38-4) removed. | | | |
| 74HC_HCT366 v.4 | 20120904 | Product data sheet | - | 74HC_HCT366 v.3 |
| Modifications: | • Legal pages updated. | | | |
| 74HC_HCT366 v.3 | 20061121 | Product data sheet | - | 74HC_HCT366_CNV v.2 |
| 74HC_HCT366_CNV v.2 | 19901201 | Product specification | - | - |

# 14. Legal information

## Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|---|---|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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