# 74LVC2G17-Q100

# Dual non-inverting Schmitt trigger with 5 V tolerant input Rev. 4 — 21 June 2021 Product data sheet

### 1. General description

The 74LVC2G17-Q100 is a dual buffer with Schmitt-trigger inputs. Inputs can be driven from either 3.3 V or 5 V devices. This feature allows the use of these devices as translators in mixed 3.3 V and 5 V environments.

This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

### 2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
  - Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 1.65 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- ±24 mA output drive (V<sub>CC</sub> = 3.0 V)
- CMOS low-power consumption
- Latch-up performance exceeds 250 mA
- · Direct interface with TTL levels
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Complies with JEDEC standard:
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD-8B/JESD36 (2.7 V to 3.6 V)
- ESD protection:
  - MIL-STD-883, method 3015 exceeds 2000 V
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)

# 3. Applications

Wave and pulse shapers for highly noisy environments

# 4. Ordering information

**Table 1. Ordering information** 

Type number	per Package							
	Temperature range	Name	Description	Version				
74LVC2G17GW-Q100	-40 °C to +125 °C	SC-88	plastic surface-mounted package; 6 leads	SOT363				
74LVC2G17GV-Q100	-40 °C to +125 °C	SC-74; TSOP6	plastic surface-mounted package; 6 leads	SOT457				



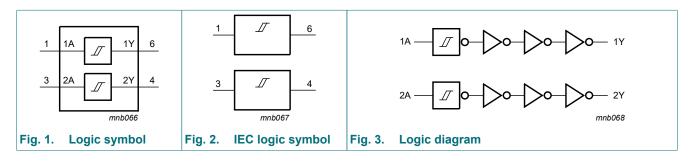
### 5. Marking

### Table 2. Marking codes

Type number	Marking code [1]
74LVC2G17GW-Q100	VV
74LVC2G17GV-Q100	VV

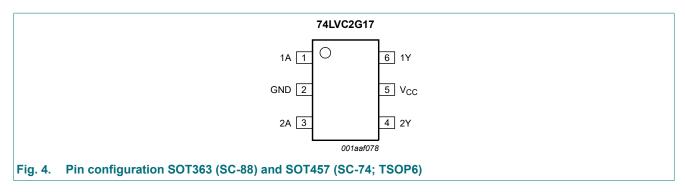
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

## 6. Functional diagram



# 7. Pinning information

### 7.1. Pinning



### 7.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
1A	1	data input
GND	2	ground (0 V)
2A	3	data input
2Y	4	data output
V <sub>CC</sub>	5	supply voltage
1Y	6	data output

### 8. Functional description

#### **Table 4. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$ 

Input	Output
nA	nY
L	L
Н	Н

### 9. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+6.5	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-	-50	mA
VI	input voltage	]	1]	-0.5	+6.5	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V		-	-50	mA
Vo	output voltage	Active mode [	1]	-0.5	V <sub>CC</sub> + 0.5	V
		Power-down mode [1] [	2]	-0.5	+6.5	V
I <sub>O</sub>	output current	V <sub>O</sub> = 0 V to V <sub>CC</sub>		-	±50	mA
I <sub>CC</sub>	supply current			-	100	mA
$I_{GND}$	ground current			-	-100	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C	3]	-	250	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 10. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		1.65	-	5.5	V
$V_{I}$	input voltage		0	-	5.5	V
V <sub>O</sub>	output voltage		0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	-	+125	°C

<sup>[2]</sup> When  $V_{CC} = 0 \text{ V}$  (Power-down mode), the output voltage can be 5.5 V in normal operation.

<sup>[3]</sup> For SOT363 (SC-88) package: P<sub>tot</sub> derates linearly with 3.7 mW/K above 83 °C. For SOT457 (SC-74; TSOP6) package: P<sub>tot</sub> derates linearly with 4.1 mW/K above 89 °C.

### 11. Static characteristics

#### **Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbo	Parameter	Conditions	Min	Typ [1]	Max	Unit
T <sub>amb</sub> =	-40 °C to +85 °C					
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = 100 $\mu$ A; $V_{CC}$ = 1.65 V to 5.5 V	-	-	0.1	V
		I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V	-	-	0.45	V
		I <sub>O</sub> = 8 mA; V <sub>CC</sub> = 2.3 V	-	-	0.3	V
		I <sub>O</sub> = 12 mA; V <sub>CC</sub> = 2.7 V	-	-	0.4	V
		I <sub>O</sub> = 24 mA; V <sub>CC</sub> = 3.0 V	-	-	0.55	V
		I <sub>O</sub> = 32 mA; V <sub>CC</sub> = 4.5 V	-	-	0.55	V
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		I <sub>O</sub> = -100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -4 mA; V <sub>CC</sub> = 1.65 V	1.2	-	-	V
		$I_{O}$ = -8 mA; $V_{CC}$ = 2.3 V	1.9	-	-	V
		$I_{O}$ = -12 mA; $V_{CC}$ = 2.7 V	2.2	-	-	V
		$I_{O}$ = -24 mA; $V_{CC}$ = 3.0 V	2.3	-	-	V
		$I_{O}$ = -32 mA; $V_{CC}$ = 4.5 V	3.8	-	-	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 5.5 V	-	±0.1	±1	μΑ
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 5.5 V; V <sub>CC</sub> = 0 V	-	±0.1	±2	μΑ
I <sub>CC</sub>	supply current	$V_{I} = V_{CC}$ or GND; $I_{O} = 0$ A; $V_{CC} = 5.5$ V	-	0.1	4	μΑ
ΔI <sub>CC</sub>	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 2.3 \text{ V to } 5.5 \text{ V}$	-	5	500	μA
Cı	input capacitance		-	3.5	-	pF

Symbol	Parameter	Conditions	Min	Typ [1]	Max	Unit
T <sub>amb</sub> = -	40 °C to +125 °C	-	-1	'		
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = 100 $\mu$ A; $V_{CC}$ = 1.65 V to 5.5 V	-	-	0.1	V
		I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V	-	-	0.70	V
		I <sub>O</sub> = 8 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 12 mA; V <sub>CC</sub> = 2.7 V	-	-	0.60	V
		I <sub>O</sub> = 24 mA; V <sub>CC</sub> = 3.0 V	-	-	0.80	V
		I <sub>O</sub> = 32 mA; V <sub>CC</sub> = 4.5 V	-	-	0.80	V
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		I <sub>O</sub> = -100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -4 mA; V <sub>CC</sub> = 1.65 V	0.95	-	-	V
		$I_{O}$ = -8 mA; $V_{CC}$ = 2.3 V	1.7	-	-	V
		$I_{O}$ = -12 mA; $V_{CC}$ = 2.7 V	1.9	-	-	V
		$I_{O}$ = -24 mA; $V_{CC}$ = 3.0 V	2.0	-	-	V
		$I_{O}$ = -32 mA; $V_{CC}$ = 4.5 V	3.4	-	-	V
lį	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 5.5 V	-	±0.1	±1	μΑ
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 5.5 V; V <sub>CC</sub> = 0 V	-	-	±2	μΑ
I <sub>CC</sub>	supply current	$V_{I} = V_{CC}$ or GND; $I_{O} = 0$ A; $V_{CC} = 5.5$ V	-	-	4	μΑ
Δl <sub>CC</sub>	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 2.3 \text{ V} \text{ to } 5.5 \text{ V}$	-	-	500	μΑ

<sup>[1]</sup> All typical values are measured at  $V_{CC}$  = 3.3 V and  $T_{amb}$  = 25 °C.

## 12. Dynamic characteristics

**Table 8. Dynamic characteristics** 

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 6.

Symbol	Parameter	Conditions	-40 °C to +85 °C		-40 °C to +85 °C		-40 °C to +125 °C	
			Min	Typ [1]	Max	Min	Max	
t <sub>pd</sub>	propagation delay	nA to nY; see Fig. 5 [2]						
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.5	5.6	10.5	1.5	13.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.0	3.7	6.5	1.0	8.5	ns
		V <sub>CC</sub> = 2.7 V	1.0	3.8	6.5	1.0	8.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.0	3.6	5.7	1.0	7.1	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	1.0	2.7	4.3	1.0	5.4	ns
$C_{PD}$	power dissipation capacitance	per buffer; $V_{CC} = 3.3 \text{ V}$ ; [3] $V_I = \text{GND to } V_{CC}$	-	16.3	-	-	-	pF

Typical values are measured at  $T_{amb}$  = 25 °C and  $V_{CC}$  = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

fo = output frequency in MHz;

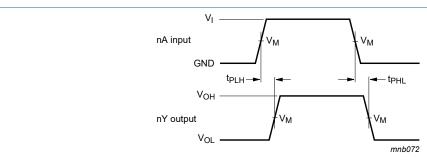
C<sub>L</sub> = output load capacitance in pF;

 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;  $\sum (C_L \times V_{CC}^2 \times f_0)$  = sum of outputs.

 $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu W$ ).

#### 12.1. Waveforms and test circuit



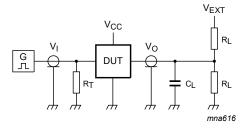
Measurement points are given in Table 9.

V<sub>OL</sub> and V<sub>OH</sub> are typical output voltage drop that occur with the output load.

Fig. 5. The input (nA) to output (nY) propagation delays and the output transition times

**Table 9. Measurement points** 

Supply voltage	Input	Output
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>
1.65 V to 1.95 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>
2.3 V to 2.7 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>
2.7 V	1.5 V	1.5 V
3.0 V to 3.6 V	1.5 V	1.5 V
4.5 V to 5.5 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>



Measurement points are given in <u>Table 10</u>.

Definitions for test circuit:

R<sub>L</sub> = Load resistance.

C<sub>L</sub> = Load capacitance including jig and probe capacitance.

 $R_{T}$  = Termination resistance should be equal to output impedance  $Z_{o}$  of the pulse generator.

 $V_{\mathsf{EXT}}$  = External voltage for measuring switching times.

Fig. 6. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Input		Load	Load		
V <sub>CC</sub>	V <sub>I</sub>	t <sub>r</sub> , t <sub>f</sub>	C <sub>L</sub>	R <sub>L</sub>	t <sub>PLH</sub> , t <sub>PHL</sub>	
1.65 V to 1.95 V	V <sub>CC</sub>	≤ 2.0 ns	30 pF	1 kΩ	open	
2.3 V to 2.7 V	V <sub>CC</sub>	≤ 2.0 ns	30 pF	500 Ω	open	
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	
4.5 V to 5.5 V	V <sub>CC</sub>	≤ 2.5 ns	50 pF	500 Ω	open	

### 13. Transfer characteristics

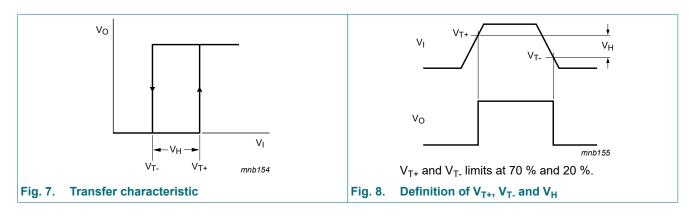
**Table 11. Transfer characteristics** 

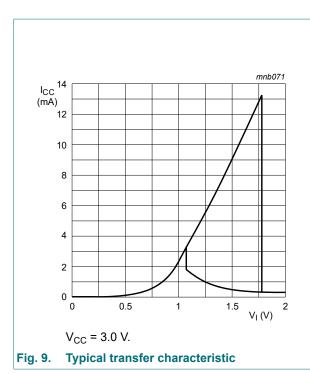
At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

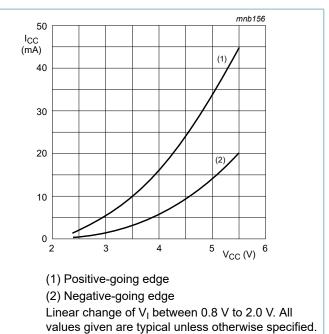
Symbol	Parameter	Conditions	-40	°C to +85	°C	-40 °C to	Unit	
			Min	Typ [1]	Max	Min	Max	
V <sub>T+</sub>	positive-going	see Fig. 7 and Fig. 8						
	threshold voltage	V <sub>CC</sub> = 1.8 V	0.70	1.10	1.50	0.70	1.70	V
		V <sub>CC</sub> = 2.3 V	1.00	1.40	1.80	1.00	2.00	V
		V <sub>CC</sub> = 3.0 V	1.30	1.76	2.20	1.30	2.40	V
		V <sub>CC</sub> = 4.5 V	1.90	2.47	3.10	1.90	3.30	V
		V <sub>CC</sub> = 5.5 V	2.20	2.91	3.60	2.20	3.80	V
V <sub>T-</sub>	negative-going threshold voltage	see Fig. 7 and Fig. 8						
		V <sub>CC</sub> = 1.8 V	0.25	0.61	0.90	0.25	1.10	V
		V <sub>CC</sub> = 2.3 V	0.40	0.80	1.15	0.40	1.35	V
		V <sub>CC</sub> = 3.0 V	0.60	1.04	1.50	0.60	1.70	V
		V <sub>CC</sub> = 4.5 V	1.00	1.55	2.00	1.00	2.20	V
		V <sub>CC</sub> = 5.5 V	1.20	1.86	2.30	1.20	2.50	V
V <sub>H</sub>	hysteresis voltage	(V <sub>T+</sub> - V <sub>T-</sub> ); see <u>Fig. 7</u> , <u>Fig. 8</u> and <u>Fig. 9</u>						
		V <sub>CC</sub> = 1.8 V	0.15	0.49	1.00	0.15	1.20	V
		V <sub>CC</sub> = 2.3 V	0.25	0.60	1.10	0.25	1.30	V
		V <sub>CC</sub> = 3.0 V	0.40	0.73	1.20	0.40	1.40	V
		V <sub>CC</sub> = 4.5 V	0.60	0.92	1.50	0.60	1.70	V
		V <sub>CC</sub> = 5.5 V	0.70	1.02	1.70	0.70	1.90	V

<sup>[1]</sup> All typical values are measured at  $T_{amb}$  = 25 °C.

### 13.1. Waveforms transfer characteristics







# 14. Package outline

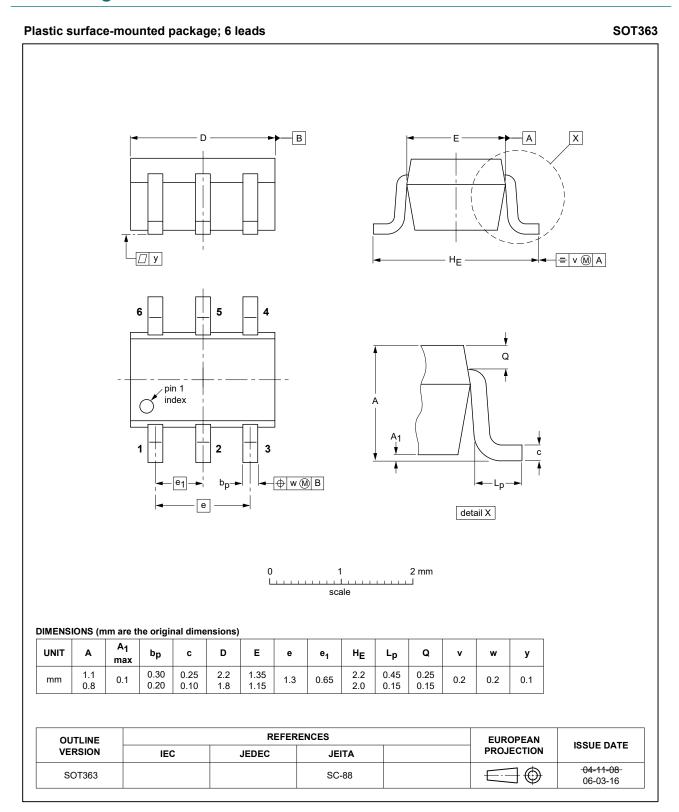


Fig. 11. Package outline SOT363 (SC-88)

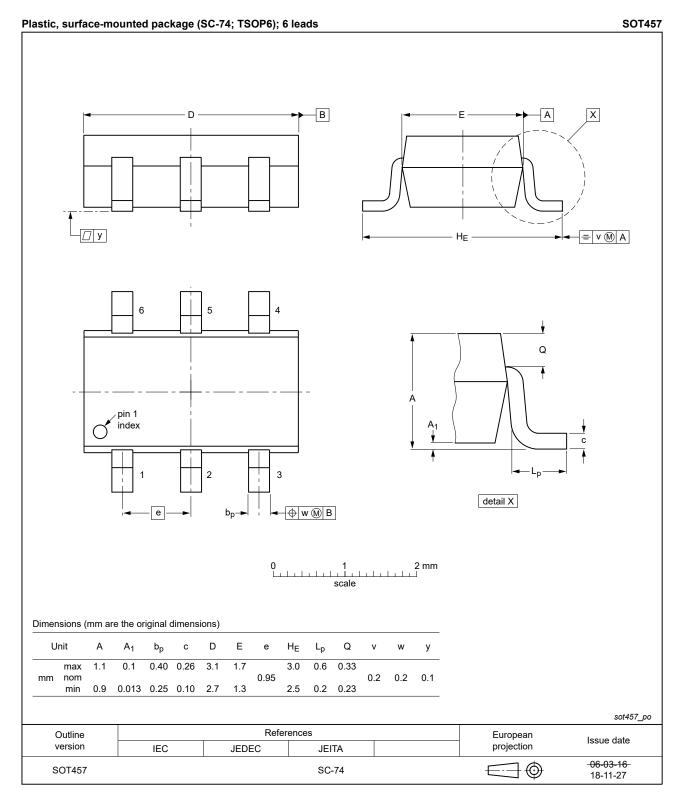


Fig. 12. Package outline SOT457 (SC-74; TSOP6)

### 15. Abbreviations

#### **Table 12. Abbreviations**

Acronym	Description	
CMOS	Complementary Metal-Oxide Semiconductor	
DUT	Device Under Test	
ESD	ElectroStatic Discharge	
НВМ	Human Body Model	
MIL	Military	
MM	Machine Model	
TTL	Transistor-Transistor Logic	

# 16. Revision history

#### **Table 13. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes	
Document ib	ixelease uale	Data Sileet Status	Change notice	Superseues	
74LVC2G17_Q100 v.4	20210621	Product data sheet	-	74LVC2G17_Q100 v.3	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Section 1 and Section 2 updated.</li> <li>Section 9: Derating values for P<sub>tot</sub> total power dissipation updated.</li> <li>Fig. 12: Package outline drawing SOT457 (SC-74; TSOP6) updated.</li> </ul>				
74LVC2G17_Q100 v.3	20161214	Product data sheet	-	74LVC2G17_Q100 v.2	
Modifications:	<u>Table 7</u> : The maximum limits for leakage current and supply current have changed.				
74LVC2G17_Q100 v.2	20130502	Product data sheet	-	74LVC2G17_Q100 v.1	
Modifications:	<u>Table 3</u> : the description of pin 6 changed from data input to data output.				
74LVC2G17_Q100 v.1	20120807	Product data sheet	-	-	

### 17. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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