Schottky barrier diode Rev. 4 — 14 November 2012

Product data sheet

Product profile

1.1 General description

Planar Schottky barrier diode with an integrated guard ring for stress protection, encapsulated in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

1.2 Features and benefits

- Low forward voltage
- Low capacitance
- AEC-Q101 qualified

1.3 Applications

- Ultra high-speed switching
- Line termination

- Voltage clamping
- Reverse polarity protection

1.4 Quick reference data

Table 1. Quick reference data

 $T_{amb} = 25$ °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{R}	reverse voltage		-	-	40	V
V _F	forward voltage	$I_F = 500 \text{ mA}$	<u>[1]</u> _	-	550	mV
I _R	reverse current	$V_{R} = 35 \text{ V}$	<u>[1]</u> _	-	100	μΑ

^[1] Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

2. **Pinning information**

Table 2. **Pinning**

Pin	Description	Simplified outline	Graphic symbol
1	anode	_	
2	not connected	3	3
3	cathode	1 2	1 2 n.c. 006aaa436



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3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BAT720	-	plastic surface-mounted package; 3 leads	SOT23

4. Marking

Table 4. Marking codes

Type number	Marking code[1]
BAT720	L6*

^{[1] * =} placeholder for manufacturing site code.

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_R	reverse voltage		-	40	V
I _F	forward current		-	500	mA
I _{FSM}	non-repetitive peak forward current	square wave; t _p < 10 ms	<u>[1]</u> -	2	Α
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$	[2] -	200	mW
T _j	junction temperature		-	125	°C
T _{amb}	ambient temperature		–55	+125	°C
T _{stg}	storage temperature		-65	+150	°C

^[1] $T_i = 25$ °C before surge.

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	500	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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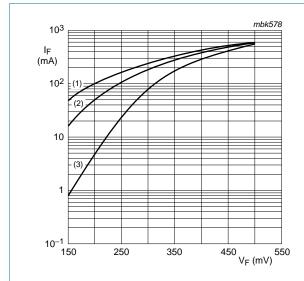
7. Characteristics

Table 7. Characteristics

 $T_i = 25$ °C unless otherwise specified.

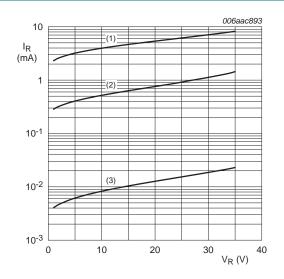
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{F}	forward voltage	$I_F = 500 \text{ mA}$	<u>[1]</u> -	-	550	mV
I _R	reverse current	$V_{R} = 35 \text{ V}$	<u>[1]</u> -	-	100	μΑ
		$V_R = 35 \text{ V}; T_j = 100 ^{\circ}\text{C}$	<u>[1]</u> -	-	10	mA
C _d	diode capacitance	$f = 1 MHz; V_R = 0 V$	60	-	90	pF

[1] Pulse test: $t_0 \le 300 \ \mu s; \ \delta \le 0.02$.



- (1) T_{amb} = 125 °C
- (2) T_{amb} = 85 °C
- (3) T_{amb} = 25 °C

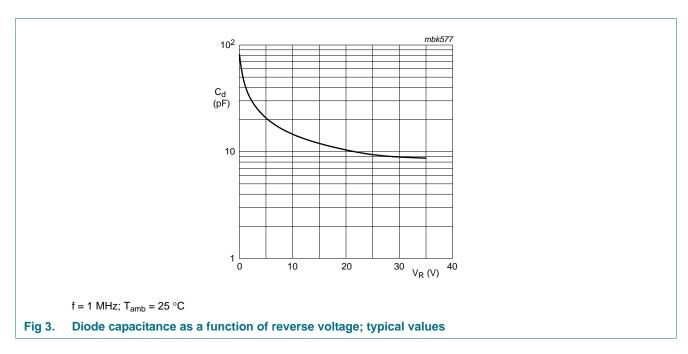
Fig 1. Forward current as a function of forward voltage; typical values



- (1) $T_{amb} = 125 \, ^{\circ}C$
- (2) $T_{amb} = 85 \, ^{\circ}C$
- (3) $T_{amb} = 25 \, ^{\circ}C$

Fig 2. Reverse current as a function of reverse voltage; typical values

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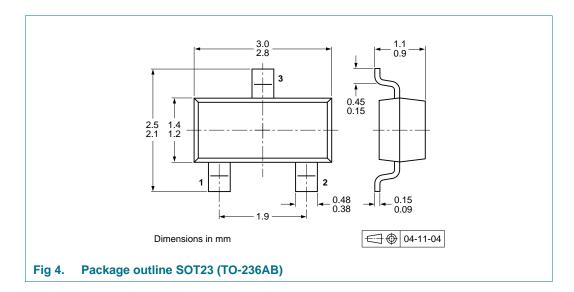
8. Test information

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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9. Package outline

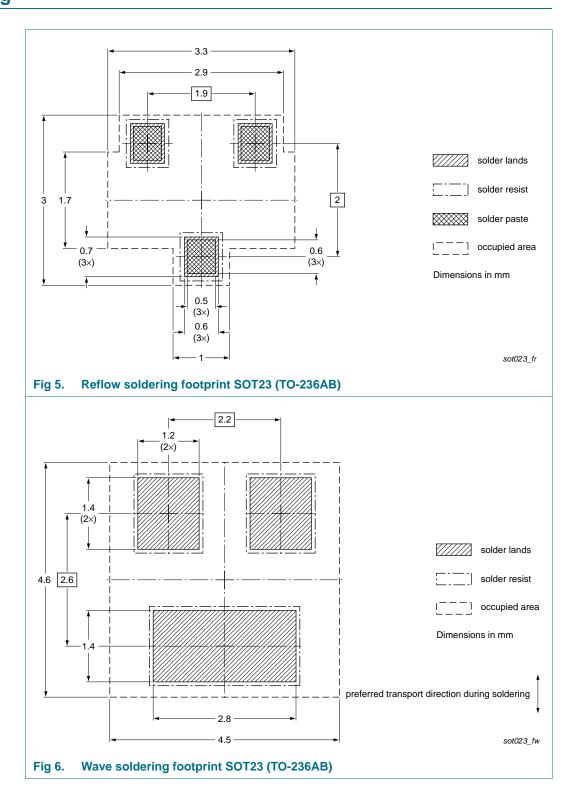


10. Packing information

Please refer to packing information on www.nexperia.com.

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11. Soldering



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12. Revision history

Table 9. Revision history

Release date	Data sheet status	Change notice	Supersedes		
20121114	Product data sheet	-	BAT720 v.3		
		redesigned to comply w	ith the new identity		
 Legal texts have been adapted to the new company name where appropriate. 					
• Section 1: u	ıpdated				
Section 4: updated					
 <u>Table 5</u>: added ambient temperature T_{amb} and total power dissipation P_{tot} 					
• Figure 2: updated					
Section 8 "Test information": added					
Figure 4: replaced by minimized package outline drawing					
 Section 10 "Packing information": added 					
Section 11 "Soldering": added					
Section 13 ⁶	"Legal information": updated	d			
20030325	Product data sheet	-	BAT720 v.2		
19990526	Product specification	-	BAT720 v.1		
	20121114 The format guidelines of Legal texts Section 1: U Section 4: U Table 5: add Figure 2: up Section 8 "T Figure 4: re Section 10 u Section 11 u Section 11 u Section 13 u 20030325	Product data sheet The format of this document has been used guidelines of NXP Semiconductors. Legal texts have been adapted to the new Section 1: updated Section 4: updated Table 5: added ambient temperature Table 5: added ambient temperature Table 5: added ambient temperature Table 5: updated Section 8 "Test information": added Figure 4: replaced by minimized package Section 10 "Packing information": added Section 11 "Soldering": added Section 13 "Legal information": updated	The format of this document has been redesigned to comply w guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name whe Section 1: updated Section 4: updated Table 5: added ambient temperature T _{amb} and total power diss Figure 2: updated Section 8 "Test information": added Figure 4: replaced by minimized package outline drawing Section 10 "Packing information": added Section 11 "Soldering": added Section 13 "Legal information": updated		

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13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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