

BC806 series

80 V, 500 mA PNP general-purpose transistors

Rev. 2 — 5 November 2019

Product data sheet

1. General description

PNP general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		NPN complement:
	Nexperia	JEDEC	
BC806-16	SOT23	TO-236AB	BC816-16
BC806-25	SOT23	TO-236AB	BC816-25

2. Features and benefits

- High current
- High voltage
- Two current gain selections
- AEC-Q101 qualified

3. Applications

- General-purpose switching and amplification
- 48 V automotive board net

4. Quick reference data

Table 2. Quick reference data

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-80	V
I _C	collector current			-	-	-500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-1	А
h _{FE}	DC current gain						
	BC806-16	V _{CE} = -1 V; I _C = -100 mA	[1]	100	-	250	
	BC806-25		[1]	160	-	400	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$



5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	c
2	E	emitter		в
3	С	collector		۳ ا م
				É
			1 2	006aaa231
			TO-236AB (SOT23)	

6. Ordering information

Table 4. Ordering information

Type number	Package	ackage					
	Name	Description	Version				
BC806-16	TO-236AB	plastic, surface-mounted package; 3 leads	SOT23				
BC806-25	1						

7. Marking

Table 5. Marking

Type number	Marking code [1]
BC806-16	%GR
BC806-25	%GS

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 6. Limiting values

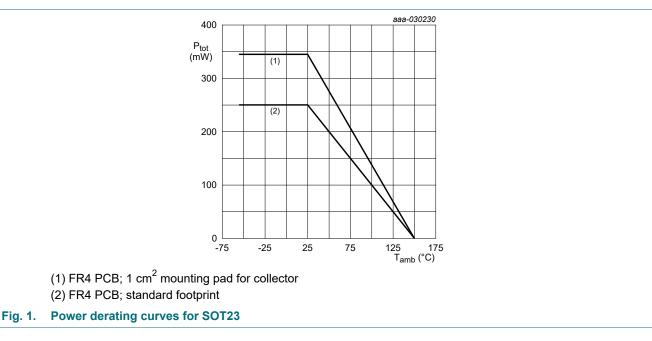
In accordance with the Absolute Maximum Rating System (IEC 60134).

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{CBO}	collector-base voltage	open emitter		-	-80	V
V _{CEO}	collector-emitter voltage	open base		-	-80	V
V _{EBO}	emitter-base voltage	open collector		-	-8	V
I _C	collector current			-	-500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-1	А
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
			[2]	-	345	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm².



9. Thermal characteristics

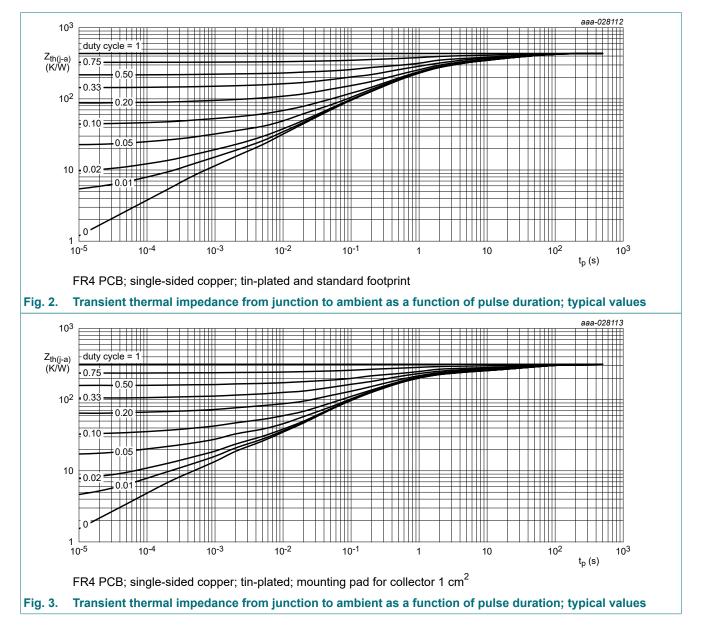
Table 7. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W
			[2]	-	-	363	K/W

[1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm².



10. Characteristics

Table 8. Characteristics

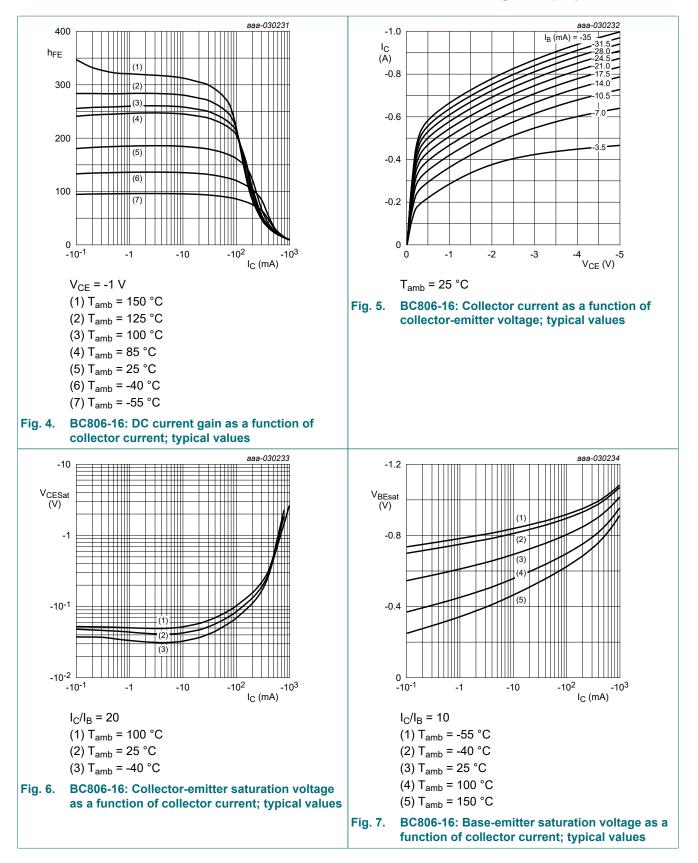
 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-80	-		V	
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = -2 mA; I _E = 0 A		-80	-		V	
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = -100 μA; I _C = 0 A		-8	-		V	
I _{CBO} collector-base cut-off current		V _{CB} = -64 V; I _E = 0 A		-	-	-100	nA	
	cut-off current	V _{CB} = -64 V; I _E = 0 A; T _j = 150 °C		-	-	-5	μA	
I _{EBO}	emitter-base cut-off current	V _{EB} = -6.4 V; I _C = 0 A		-	-	-100	nA	
h _{FE}	DC current gain	C current gain						
	BC806-16	V _{CE} = -1 V; I _C = -100 mA	[1]	100	-	250		
	BC806-25	V _{CE} = -1 V; I _C = -100 mA	[1]	160	-	400		
		V _{CE} = -2 V; I _C = -500 mA	[1]	30	-	-		
V _{CEsat}	collector-emitter	I _C = -100 mA; I _B = -10 mA	[1]	-	-	-150	mV	
	saturation voltage	I _C = -500 mA; I _B = -50 mA	[1]	-	-	-400	mV	
V _{BE}	base-emitter voltage	V _{CE} = -1 V; I _C = -500 mA	[1]	-	-	-1.2	V	
f _T	transition frequency	V _{CE} = -5 V; I _C = -50 mA; f = 100 MHz		80	-	-	MHz	
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz		-	5	-	pF	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

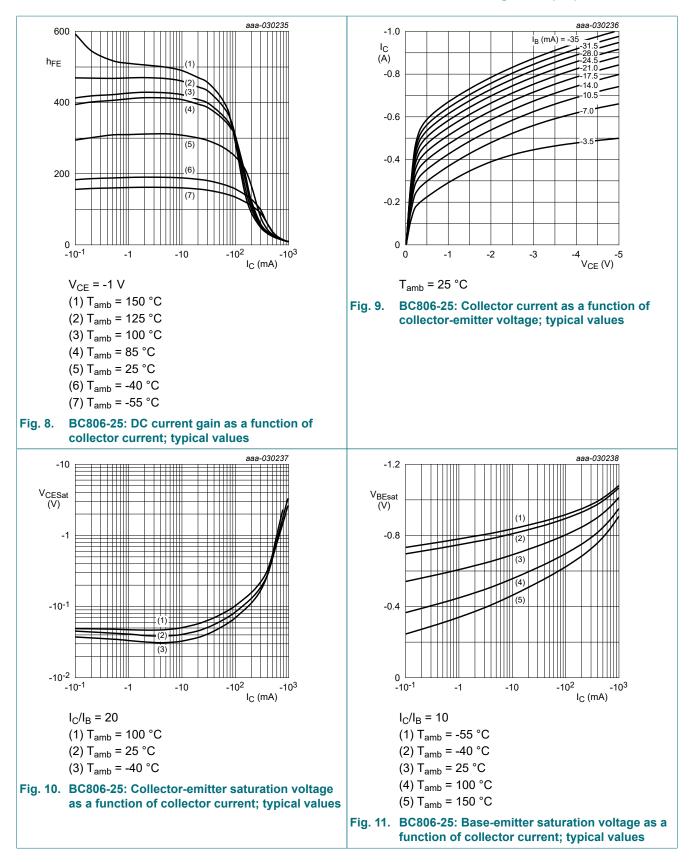
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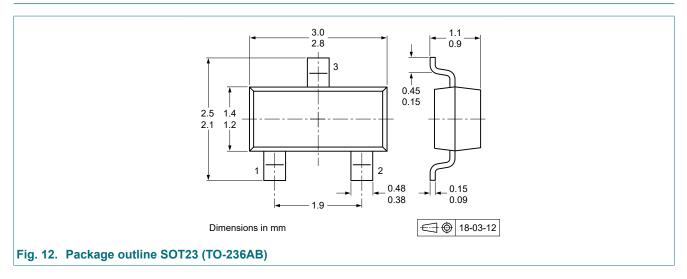
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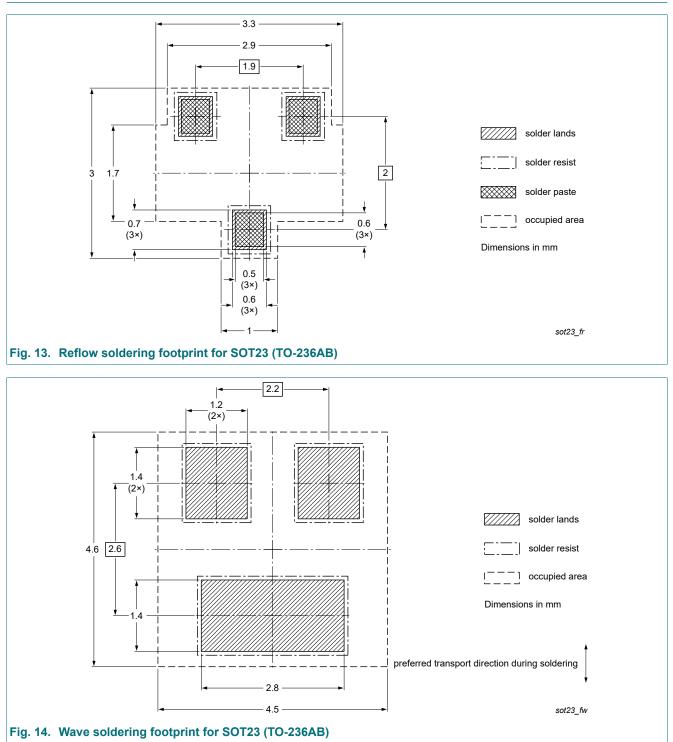
11. Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 9. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BC806_SER v.2	20191105	Product data sheet	-	BC806_SER v.1			
Modifications:	Product status chang	Product status changed					
BC806_SER v.1	20190909	Preliminary data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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