

45 V, 500 mA PNP general-purpose transistors Rev. 1 — 30 August 2013 F

Product data sheet

1. **Product profile**

1.1 General description

500 mA PNP general-purpose transistors in a leadless ultra small DFN1010D-3 (SOT1215) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

Table 1. Product overview

| Type number | Package | Package | |
|-------------|------------|---------|------------|
| | Nexperia | JEITA | |
| BC807-25QA | DFN1010D-3 | | BC817-25QA |
| BC807-40QA | (SOT1215) | | BC817-40QA |

1.2 Features and benefits

- General-purpose transistor
- Two current gain selections
- Low package height of 0.37 mm
- AEC-Q101 qualified

1.3 Applications

- General-purpose switching and amplification
- Mobile applications

1.4 Quick reference data

Table 2. **Quick reference data**

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------|---------------------------|--|------------|-----|------|------|
| V_{CEO} | collector-emitter voltage | open base | - | - | -45 | V |
| I _C | collector current | | - | - | -500 | mA |
| h _{FE} | DC current gain | $V_{CE} = -1 \text{ V}; I_{C} = -100 \text{ mA}$ | <u>[1]</u> | | | |
| | BC807-25QA | | 160 | - | 400 | |
| | BC807-40QA | | 250 | - | 600 | |

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.



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2. Pinning information

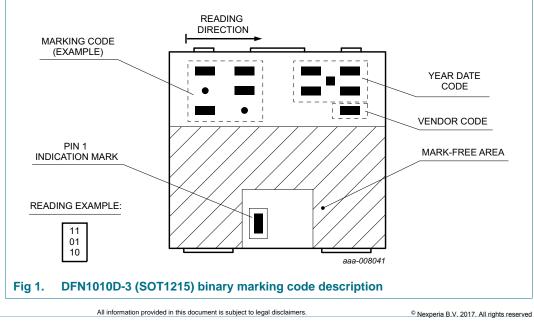
| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|----------------------|----------------|
| 1 | В | base | | |
| 2 | Е | emitter | | c J |
| 3 | С | collector | | в-К |
| 4 | С | collector | 4 3 | E |
| | | | | sym132 |
| | | | | |
| | | | Transparent top view | |

3. Ordering information

| Table 4. Ordering information | | | | | | |
|-------------------------------|------------|---|---------|--|--|--|
| Туре | Package | | | | | |
| number | Name | Description | Version | | | |
| BC807-25QA | DFN1010D-3 | plastic thermal enhanced ultra thin small outline | SOT1215 | | | |
| BC807-40QA | - | package; no leads; 3 terminals; body: $1.1 \times 1.0 \times 0.37$ mm | | | | |

4. Marking

| Table 5. Marking codes | |
|------------------------|--------------|
| Type number | Marking code |
| BC807-25QA | 01 01 00 |
| BC807-40QA | 00 11 00 |



BC807-25QA 40QA

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5. Limiting values

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|---------------------------|--------------------------------------|--------------|------|------|
| V _{CBO} | collector-base voltage | open emitter | - | -50 | V |
| V _{CEO} | collector-emitter voltage | open base | - | -45 | V |
| V _{EBO} | emitter-base voltage | open collector | - | -5 | V |
| I _C | collector current | | - | -500 | mA |
| I _{CM} | peak collector current | single pulse; $t_p \le 1 \text{ ms}$ | - | -1 | А |
| I _{BM} | peak base current | single pulse; $t_p \le 1 \text{ ms}$ | - | -200 | mA |
| P _{tot} | total power dissipation | $T_{amb} \leq 25 \ ^{\circ}C$ | | | |
| | | | <u>[1]</u> _ | 300 | mW |
| | | | [2] _ | 500 | mW |
| | | | [3] _ | 560 | mW |
| | | | [4] | 900 | mW |
| Tj | junction temperature | | - | 150 | °C |
| T _{amb} | ambient temperature | | -55 | +150 | °C |
| T _{stg} | storage temperature | | -65 | +150 | °C |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

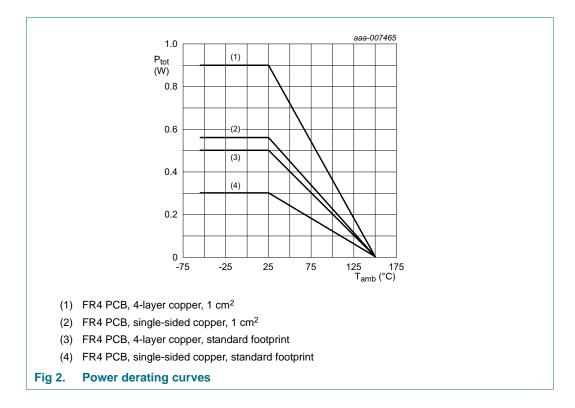
[2] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 1 cm².

[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated mounting pad for collector 1 cm².

BC807-25QA; BC807-40QA

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6. Thermal characteristics

| Table 7. | Thermal characteristics | | | | | |
|----------------------|-------------------------|--------------|--------------|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| R _{th(j-a)} | thermal resistance from | in free air | | | | |
| junction to ambient | | <u>[1]</u> - | - | 417 | K/W | |
| | | [2] _ | - | 250 | K/W | |
| | | | [3] _ | - | 223 | K/W |
| | | | <u>[4]</u> _ | - | 139 | K/W |

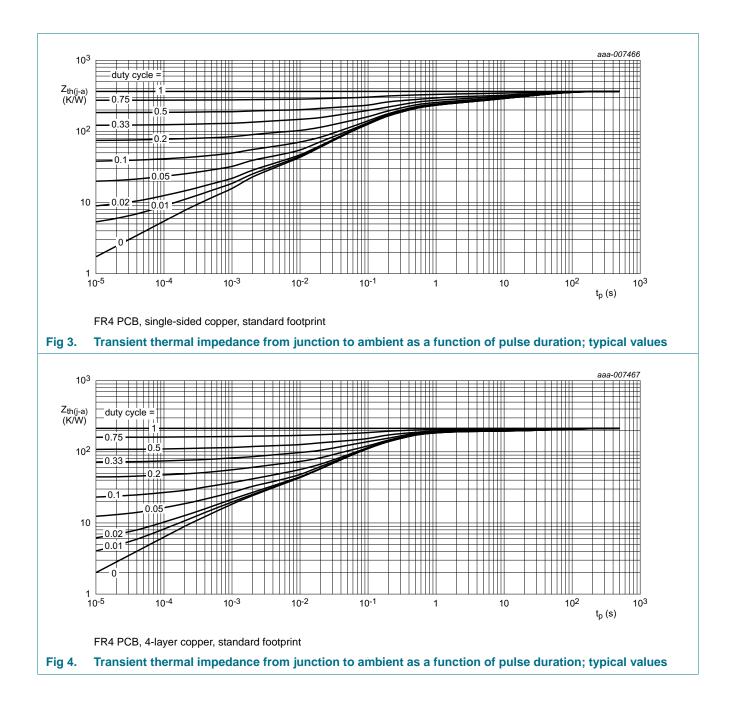
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

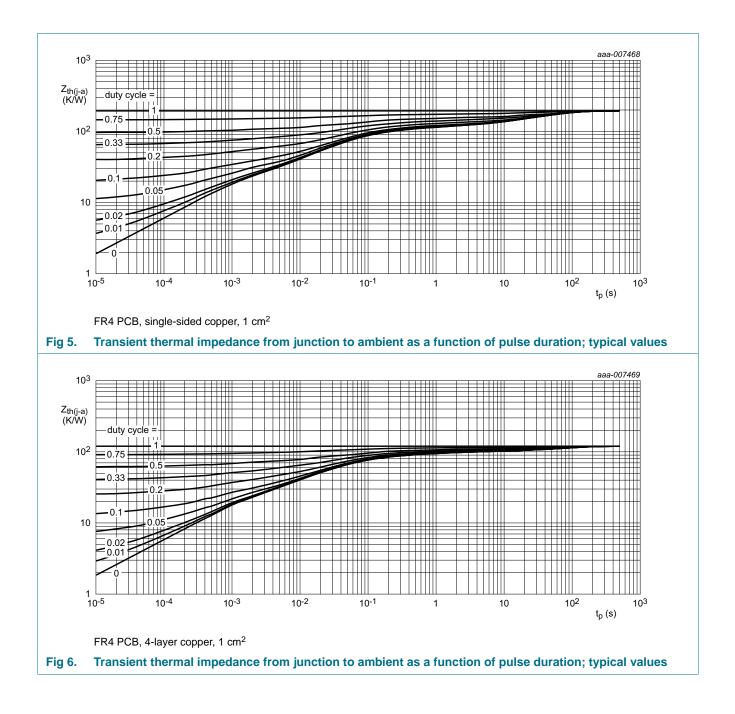
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 1 cm².

[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated mounting pad for collector 1 cm².

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BC807-25QA; BC807-40QA

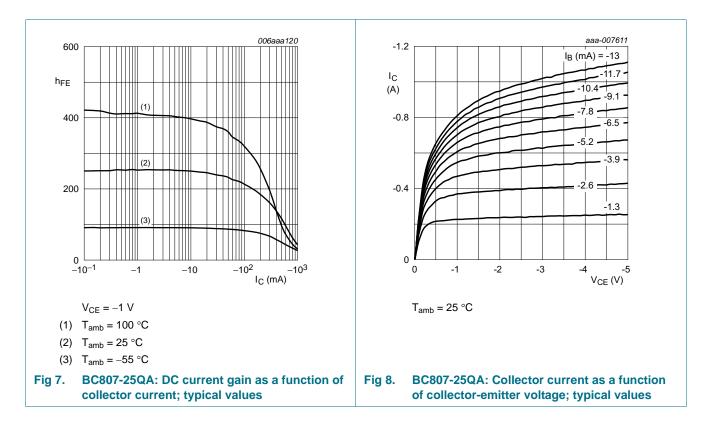


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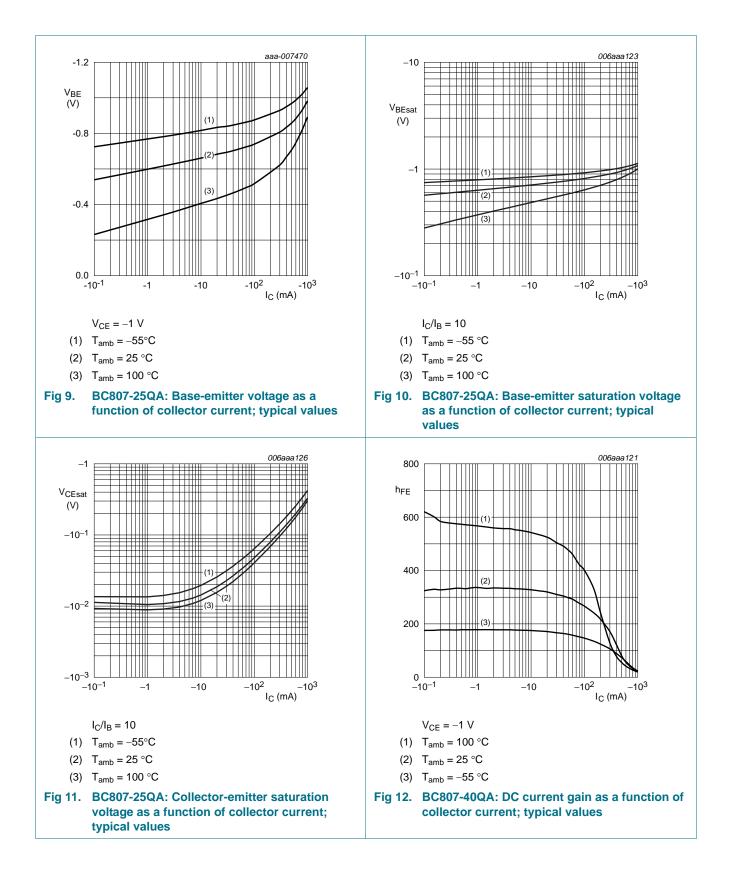
7. Characteristics

| Table 8. $T_{amb} = 25$ | Characteristics °C unless otherwise spe | ecified. | | | | | |
|--------------------------------|--|--|------------|-----|-----|------|------|
| Symbol | Parameter | Conditions | | Min | Тур | Мах | Unit |
| I _{CBO} | collector-base | $V_{CB} = -20 \text{ V}; I_E = 0 \text{ A}$ | | - | - | -100 | nA |
| | cut-off current | $\label{eq:VCB} \begin{array}{l} V_{CB} = -20 \ V; \ I_E = 0 \ A; \\ T_j = 150 \ ^\circ C \end{array}$ | | - | - | -5 | μΑ |
| I _{EBO} | emitter-base cut-off current | $V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$ | | - | - | -100 | nA |
| h _{FE} | DC current gain | $V_{CE} = -1 \text{ V}; \text{ I}_{C} = -100 \text{ mA}$ | [1] | | | | |
| | BC807-25QA | | | 160 | - | 400 | |
| | BC807-40QA | | | 250 | - | 600 | |
| h _{FE} | DC current gain | $V_{CE} = -1 \text{ V}; I_{C} = -500 \text{ mA}$ | <u>[1]</u> | 40 | - | - | |
| V _{CEsat} | collector-emitter saturation voltage | $I_{C} = -500 \text{ mA}; I_{B} = -50 \text{ mA}$ | <u>[1]</u> | - | - | -700 | mV |
| V_{BE} | base-emitter voltage | I_C = -500 mA; V_{CE} = -1 V | <u>[1]</u> | - | - | -1.2 | V |
| C _c | collector capacitance | $\label{eq:VCB} \begin{array}{l} V_{CB}=-10 \text{ V}; I_{E}=i_{e}=0 \text{ A}; \\ f=1 \text{ MHz} \end{array}$ | | - | 6 | - | pF |
| f _T | transition frequency | $V_{CE} = -5 \text{ V}; I_C = -10 \text{ mA};$ f = 100 MHz | | 80 | - | - | MHz |
| | | | | | | | |

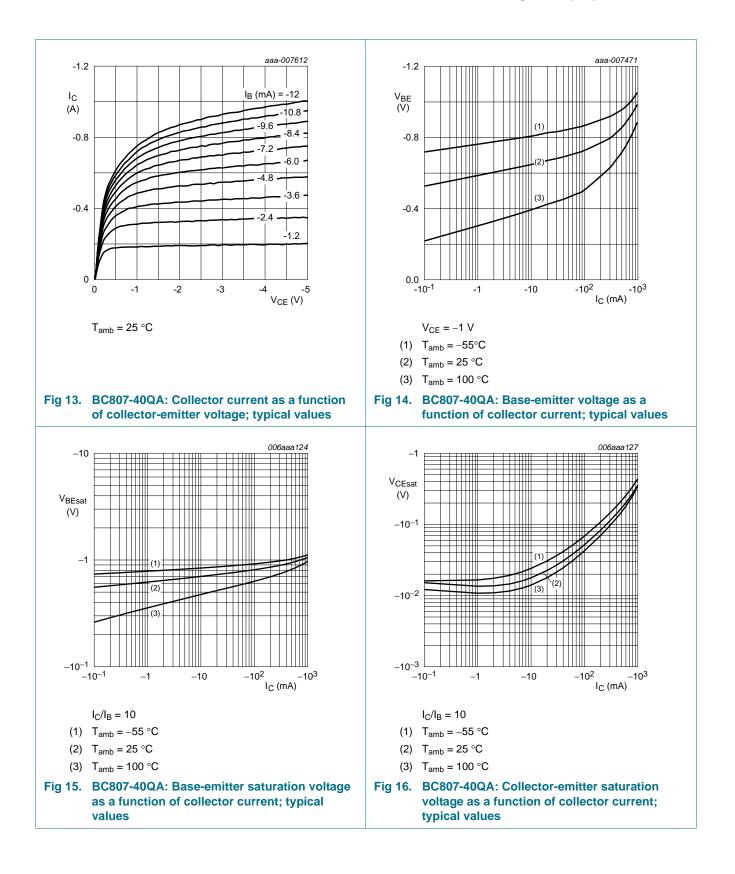
 $\label{eq:point} \begin{tabular}{ll} \mbox{Pulse test: } t_p \leq 300 \ \mu \mbox{s; } \delta \leq 0.02. \end{tabular}$



BC807-25QA; BC807-40QA



BC807-25QA; BC807-40QA



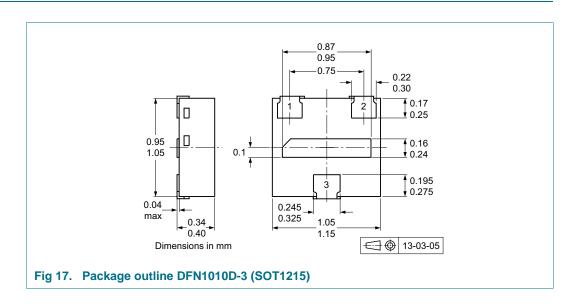
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8. Test information

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

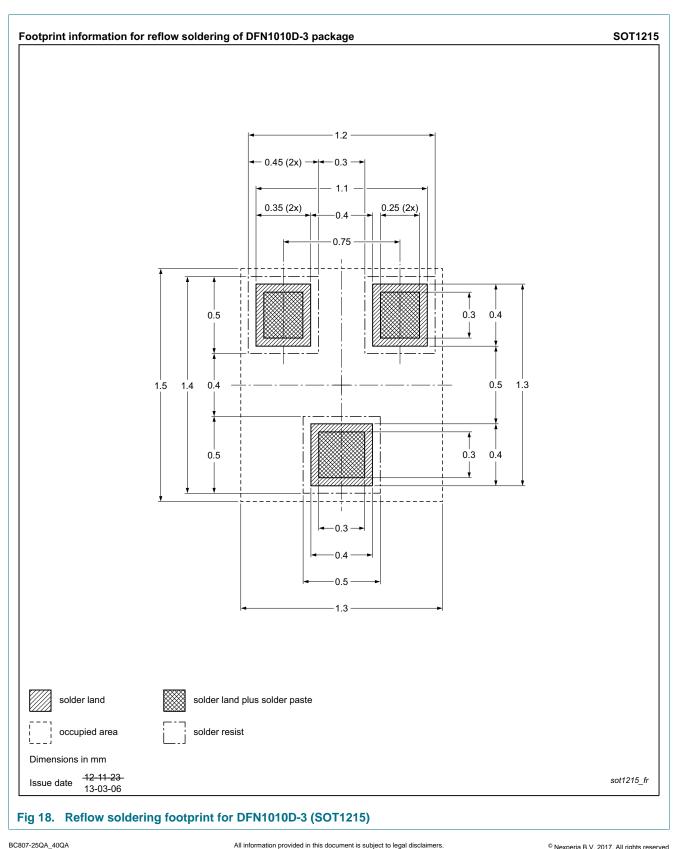
9. Package outline



BC807-25QA 40QA

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10. Soldering



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11. Revision history

| Table 9. Revision hist | Revision history | | | | | | |
|------------------------|------------------|--------------------|---------------|------------|--|--|--|
| Document ID | Release date | Data sheet status | Change notice | Supersedes | | | |
| BC807-25QA_40QA v.1 | 20130830 | Product data sheet | - | - | | | |

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12. Legal information

12.1 Data sheet status

| Document status[1][2] | Product status ^[3] | Definition |
|--------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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[2] The term 'short data sheet' is explained in section "Definitions".

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